
A Survey of High-Level Synthesis Systems

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Survey Of High Level Synthesis Systems

Brian T. Graham



Survey Of High Level Synthesis Systems:

A Survey of High-Level Synthesis Systems Robert A. Walker, Raul Camposano, 2012-12-06 After long years of work that have seen little industrial application high level synthesis is finally on the verge of becoming a practical tool The state of high level synthesis today is similar to the state of logic synthesis ten years ago At present logic synthesis tools are widely used in digital system design In the future high level synthesis will play a key role in mastering design complexity and in truly exploiting the potential of ASIs and PLDs which demand extremely short design cycles Work on high level synthesis began over twenty years ago Since substantial progress has been made in understanding the basic then problems involved although no single universally accepted theoretical framework has yet emerged There is a growing number of publications devoted to high level synthesis specialized workshops are held regularly and tutorials on the topic are commonly held at major conferences This book gives an extensive survey of the research and development in high level synthesis In Part I a short tutorial explains the basic concepts used in high level synthesis and follows an example design throughout the synthesis process In Part II current high level synthesis systems are surveyed [A Survey of High-level Synthesis Systems](#)

Carnegie-Mellon University. SRC-CMU Research Center for Computer-Aided Design, R. A. Walker, 1989 **Low Power Design in Deep Submicron Electronics** W. Nebel, Jean Mermet, 2013-06-29 Low Power Design in Deep Submicron Electronics deals with the different aspects of low power design for deep submicron electronics at all levels of abstraction from system level to circuit level and technology Its objective is to guide industrial and academic engineers and researchers in the selection of methods technologies and tools and to provide a baseline for further developments Furthermore the book has been written to serve as a textbook for postgraduate student courses In order to achieve both goals it is structured into different chapters each of which addresses a different phase of the design a particular level of abstraction a unique design style or technology These design related chapters are amended by motivations in Chapter 2 which presents visions both of future low power applications and technology advancements and by some advanced case studies in Chapter 9 From the Foreword This global nature of design for low power was well understood by Wolfgang Nebel and Jean Mermet when organizing the NATO workshop which is the origin of the book They invited the best experts in the field to cover all aspects of low power design As a result the chapters in this book are covering deep submicron CMOS digital system design for low power in a systematic way from process technology all the way up to software design and embedded software systems Low Power Design in Deep Submicron Electronics is an excellent guide for the practicing engineer the researcher and the student interested in this crucial aspect of actual CMOS design It contains about a thousand references to all aspects of the recent five years of feverish activity in this exciting aspect of design Hugo de Man Professor K U Leuven Belgium Senior Research Fellow IMEC Belgium [Hardware Implementation of Intelligent Systems](#) Horia-Nicolai Teodorescu, Abraham Kandel, 2013-11-11 Intelligent systems are now being used more commonly than in the past These involve cognitive evolving

and artificial life robotic and decision making systems to name a few Due to the tremendous speed of development on both fundamental and technological levels it is virtually impossible to offer an up to date yet comprehensive overview of this field Nevertheless the need for a volume presenting recent developments and trends in this domain is huge and the demand for such a volume is continually increasing in industrial and academic engineering communities Although there are a few volumes devoted to similar issues none offer a comprehensive coverage of the field moreover they risk rapidly becoming obsolete The editors of this volume cannot pretend to fill such a large gap However it is the editors intention to fill a significant part of this gap A comprehensive coverage of the field should include topics such as neural networks fuzzy systems neuro fuzzy systems genetic algorithms evolvable hardware cellular automata based systems and various types of artificial life system implementations including autonomous robots In this volume we have focused on the first five topics listed above The volume is composed of four parts each part being divided into chapters with the exception of part 4 In Part 1 the topics of Evolvable Hardware and GAs are addressed In Chapter 1 Automated Design Synthesis and Partitioning for Adaptive Reconfigurable Hardware Ranga Vemuri and co authors present state of the art adaptive architectures their classification and their applications

VLSI Design Methodologies for Digital Signal Processing Architectures

Magdy A. Bayoumi, 2012-12-06 Designing VLSI systems represents a challenging task It is a transformation among different specifications corresponding to different levels of design abstraction behavioral structural and physical The behavioral level describes the functionality of the design It consists of two components static and dynamic The static component describes operations whereas the dynamic component describes sequencing and timing The structural level contains information about components control and connectivity The physical level describes the constraints that should be imposed on the floor plan the placement of components and the geometry of the design Constraints of area speed and power are also applied at this level To implement such multilevel transformation a design methodology should be devised taking into consideration the constraints limitations and properties of each level The mapping process between any of these domains is non isomorphic A single behavioral component may be transformed into more than one structural component Design methodologies are the most recent evolution in the design automation era which started off with the introduction and subsequent usage of module generation especially for regular structures such as PLA's and memories A design methodology should offer an integrated design system rather than a set of separate unrelated routines and tools A general outline of a desired integrated design system is as follows Decide on a certain unified framework for all design levels Derive a design method based on this framework Create a design environment to implement this design method

The SECD Microprocessor Brian T.

Graham, 2012-12-06 This is a milestone in machine assisted microprocessor verification Gordon 20 and Hunt 32 led the way with their verifications of simple designs Cohn 12 13 followed this with the verification of parts of the VIPER microprocessor This work illustrates how much these and other pioneers achieved in developing tractable models scalable tools and a robust

methodology A condensed review of previous research emphasising the behavioural model underlying this style of verification is followed by a careful and remarkably readable account of the SECD architecture its formalisation and a report on the organisation and execution of the automated correctness proof in HOL This monograph reports on Graham's MSc project demonstrating that in the right hands the tools and methodology for formal verification can and therefore should now be applied by someone with little previous expertise in formal methods to verify a non trivial microprocessor in a limited timescale This is not to belittle Graham's achievement the production of this proof working as Graham did from the previous literature goes well beyond a typical MSc project The achievement is that with this exposition to hand an engineer tackling the verification of similar microprocessor designs will have a clear view of the milestones that must be passed on the way and of the methods to be applied to achieve them

Advances in Hardware Design and Verification Hon Li, David Probst, 2016-01-09 CHARM 97 is the ninth in a series of working conferences devoted to the development and use of formal techniques in digital hardware design and verification This series is held in collaboration with IFIP WG 10.5 Previous meetings were held in Europe every other year

Advances in Computers, 1993-09-14 Advances in Computers

Algorithms for VLSI Design Automation Sabih H. Gerez, 1999-01-05 Modern microprocessors such as Intel's Pentium chip typically contain many millions of transistors They are known generically as Very Large Scale Integrated VLSI systems and their sheer scale and complexity has necessitated the development of CAD tools to automate their design This book focuses on the algorithms which are the building blocks of the design automation software which generates the layout of VLSI circuits Courses on this area are typically elective courses taken at senior undergrad or graduate level by students of Electrical and Electronic Engineering and sometimes in Computer Science or Computer Engineering

Sequential Logic Testing and Verification Abhijit Ghosh, Srinivas Devadas, A. Richard Newton, 2012-12-06 In order to design and build computers that achieve and sustain high performance it is essential that reliability issues be considered carefully The problem has several aspects Certainly considering reliability implies that an engineer must be able to analyze how design decisions affect the incidence of failure For instance in order design reliable integrated circuits it is necessary to analyze how decisions regarding design rules affect the yield i.e. the percentage of functional chips obtained by the manufacturing process Of equal importance in producing reliable computers is the detection of failures in its Very Large Scale Integrated VLSI circuit components caused by errors in the design specification implementation or manufacturing processes Design verification involves the checking of the specification of a design for correctness prior to carrying out an implementation Implementation verification ensures that the manual design or automatic synthesis process is correct i.e. the mask level description correctly implements the specification Manufacture test involves the checking of the complex fabrication process for correctness i.e. ensuring that there are no manufacturing defects in the integrated circuit It should be noted that all the above verification mechanisms deal not only with verifying the functionality of the integrated circuit but also its performance

Edge Computing Acceleration Patrick Hung, Hongwei Kan, Greg Knopf, 2024-12-03 Discover the latest advances in computer architecture and software at the dawn of the 5G 6G era In *Edge Computing Acceleration From 5G to 6G and Beyond* distinguished researchers Dr Patrick Hung Hongwei Kan and Greg Knopf deliver a comprehensive overview of personal computer architecture and software design usage in the upcoming 5G decade The authors begin by introducing key components and exploring different hardware acceleration architectures They move on to discuss 5G data security and data integrity and offer a survey of network virtualization technologies including accelerated virtualization technologies The book analyzes 5G 6G system performance investigating key design considerations and trade offs and introducing high level synthesis flow It concludes with chapters exploring design verification and validation flow illustrations of 5G applications based on artificial intelligence and other emerging technologies and offering highlights of emerging 6G research and roadmaps Readers will enjoy the combination of accessible descriptions of new technologies presented side by side as a step by step guide to designing effective 5G systems The book also includes A thorough introduction to key 5G 6G components including new wireless communication protocols edge and fog computing acceleration technologies IoE architectures software designed networks network function virtualization and data security Explorations of various hardware acceleration architectures like FPGA and GPU acceleration architectures Practical discussions of 5G 6G data security data integrity and a survey of network virtualization technologies In depth treatments of 5G 6G system performance key design considerations high level synthesis flow design verification and validation flow Perfect for undergraduate and graduate students in programs related to communications technology engineering and computer science *Edge Computing Acceleration From 5G to 6G and Beyond* is a must have resource for engineers programmers system architects technical managers communications business executives telco operators and government regulators who regularly interact with cutting edge communications equipment

Fault Covering Problems in Reconfigurable VLSI Systems Ran Libeskind-Hadas, Nany Hasan, Jingsheng Jason Cong, Philip McKinley, C.L. Liu, 2012-12-06 *Fault Covering Problems in Reconfigurable VLSI Systems* describes the authors recent research on reconfiguration problems for fault tolerance in VLSI and WSI Systems The book examines solutions to a number of reconfiguration problems Efficient algorithms are given for tractable covering problems and general techniques are given for dealing with a large number of intractable covering problems The book begins with an investigation of algorithms for the reconfiguration of large redundant memories Next a number of more general covering problems are considered and the complexity of these problems is analyzed Finally a general and uniform approach is proposed for solving a wide class of covering problems The results and techniques described here will be useful to researchers and students working in this area As such the book serves as an excellent reference and may be used as the text for an advanced course on the topic

Mechatronics in Engineering Design and Product Development Dobrivoj Popovich, 1998-09-30 This work presents a systematic and comprehensive overview to the theory and applications of mechatronic processes emphasizing the

adaptation and incorporation of this important tool in fulfilling desired performance and quality requirements The authors address the core technologies needed for the design and development of the mechatronic product The Design of Communicating Systems C.J. Koomen,2012-12-06 The professional schools will resume their professional responsibilities just to the degree that they can discover a science of design a body of intellectually tough partly formalizable partly empirical teachable doctrine about the design process H A Simon 1968 Design is aimed at the transformation or translation of a specification or high level description into a description in terms of some real world primitives As such it involves the removal of the uncertainty about the way in which a required system can be realized To optimally support the design of systems we must look at the design process as a whole and at the strong relationship that exists between a designer the applied design method the required design tools and the ways in which designs can be expressed This book focuses on that relationship The application field we are concerned with is the design of systems in which the communication between system elements is a major design feature Examples of such communicating systems are communication protocols telephone exchange control systems process control systems highly modular systems embedded software interactive systems and VLSI systems In summary we are concerned with systems in which concurrency plays a major role concurrency defines the mutual relationship between the activities in the different parts of a system or within a collection of systems

Field-Programmable Gate Arrays Stephen D. Brown,Robert J. Francis,Jonathan Rose,Zvonko G. Vranesic,2012-12-06 Field Programmable Gate Arrays FPGAs have emerged as an attractive means of implementing logic circuits providing instant manufacturing turnaround and negligible prototype costs They hold the promise of replacing much of the VLSI market now held by mask programmed gate arrays FPGAs offer an affordable solution for customized VLSI over a wide variety of applications and have also opened up new possibilities in designing reconfigurable digital systems Field Programmable Gate Arrays discusses the most important aspects of FPGAs in a textbook manner It provides the reader with a focused view of the key issues using a consistent notation and style of presentation It provides detailed descriptions of commercially available FPGAs and an in depth treatment of the FPGA architecture and CAD issues that are the subjects of current research The material presented is of interest to a variety of readers including those who are not familiar with FPGA technology but wish to be introduced to it as well as those who already have an understanding of FPGAs but who are interested in learning about the research directions that are of current interest **VLSI: Integrated Systems on Silicon** Ricardo A. Reis,Luc Claesen,2013-06-05 This book contains the papers that have been presented at the ninth Very Large Scale Integrated Systems conference VLSI 97 that is organized biannually by IFIP Working Group 10.5 It took place at Hotel Serra Azul in Gramado Brazil from 26-30 August 1997 Previous conferences have taken place in Edinburgh Trondheim Vancouver Munich Grenoble and Tokyo The papers in this book report on all aspects of importance to the design of the current and future integrated systems The current trend towards the realization of versatile Systems on a Chip require

attention of embedded hardware software systems dedicated ASIC hardware sensors and actuators mixed analog digital design video and image processing low power battery operation and wireless communication The papers as presented in Jhis book have been organized in two tracks where one is dealing with VLSI System Design and Applications and the other presents VLSI Design Methods and CAD The following topics are addressed VLSI System Design and Applications Track VLSI for Video and Image Processing Microsystem and Mixed mode design Communication And Memory System Design Low voltage Low power Analog Circuits High Speed Circuit Techniques Application Specific DSP Architectures VLSI Design Methods and CAD Track Specification and Simulation at System Level Synthesis and Technology Mapping CAD Techniques for Low Power Design Physical Design Issues in Sub micron Technologies Architectural Design and Synthesis Testing in Complex Mixed Analog and Digital Systems

FPGAs for Software Programmers Dirk Koch, Frank Hannig, Daniel Ziener, 2016-06-17 This book makes powerful Field Programmable Gate Array FPGA and reconfigurable technology accessible to software engineers by covering different state of the art high level synthesis approaches e g OpenCL and several C to gates compilers It introduces FPGA technology its programming model and how various applications can be implemented on FPGAs without going through low level hardware design phases Readers will get a realistic sense for problems that are suited for FPGAs and how to implement them from a software designer's point of view The authors demonstrate that FPGAs and their programming model reflect the needs of stream processing problems much better than traditional CPU or GPU architectures making them well suited for a wide variety of systems from embedded systems performing sensor processing to large setups for Big Data number crunching This book serves as an invaluable tool for software designers and FPGA design engineers who are interested in high design productivity through behavioural synthesis domain specific compilation and FPGA overlays Introduces FPGA technology to software developers by giving an overview of FPGA programming models and design tools as well as various application examples Provides a holistic analysis of the topic and enables developers to tackle the architectural needs for Big Data processing with FPGAs Explains the reasons for the energy efficiency and performance benefits of FPGA processing Provides a user oriented approach and a sense for where and how to apply FPGA technology

VLSI: Systems on a Chip Luis Miguel Silveira, Srinivas Devadas, Ricardo A.

Reis, 2013-11-11 For over three decades now silicon capacity has steadily been doubling every year and a half with equally staggering improvements continuously being observed in operating speeds This increase in capacity has allowed for more complex systems to be built on a single silicon chip Coupled with this functionality increase speed improvements have fueled tremendous advancements in computing and have enabled new multi media applications Such trends aimed at integrating higher levels of circuit functionality are tightly related to an emphasis on compactness in consumer electronic products and a widespread growth and interest in wireless communications and products These trends are expected to persist for some time as technology and design methodologies continue to evolve and the era of Systems on a Chip has definitely come of age While

technology improvements and spiraling silicon capacity allow designers to pack more functions onto a single piece of silicon they also highlight a pressing challenge for system designers to keep up with such amazing complexity To handle higher operating speeds and the constraints of portability and connectivity new circuit techniques have appeared Intensive research and progress in EDA tools design methodologies and techniques is required to empower designers with the ability to make efficient use of the potential offered by this increasing silicon capacity and complexity and to enable them to design test verify and build such systems

Handbook of Computer Architecture Anupam Chattopadhyay, 2024-12-20 This handbook presents the key topics in the area of computer architecture covering from the basic to the most advanced topics including software and hardware design methodologies It will provide readers with the most comprehensive updated reference information covering applications in single core processors multicore processors application specific processors reconfigurable architectures emerging computing architectures processor design and programming flows test and verification This information benefits the readers as a full and quick technical reference with a high level review of computer architecture technology detailed technical descriptions and the latest practical applications

Low Power Digital CMOS Design Anantha P. Chandrakasan, Robert W. Brodersen, 2012-12-06 Power consumption has become a major design consideration for battery operated portable systems as well as high performance desktop systems Strict limitations on power dissipation must be met by the designer while still meeting ever higher computational requirements A comprehensive approach is thus required at all levels of system design ranging from algorithms and architectures to the logic styles and the underlying technology Potentially one of the most important techniques involves combining architecture optimization with voltage scaling allowing a trade off between silicon area and low power operation Architectural optimization enables supply voltages of the order of 1 V using standard CMOS technology Several techniques can also be used to minimize the switched capacitance including representation optimizing signal correlations minimizing spurious transitions optimizing sequencing of operations activity driven power down etc The high efficiency of DC DC converter circuitry required for efficient low voltage and low current level operation is described by Stratakos Sullivan and Sanders The application of various low power techniques to a chip set for multimedia applications shows that orders of magnitude reduction in power consumption is possible The book also features an analysis by Professor Meindl of the fundamental limits of power consumption achievable at all levels of the design hierarchy Svensson of ISI describes emerging adiabatic switching techniques that can break the CV²f barrier and reduce the energy per computation at a fixed voltage Srivastava of AT T presents the application of aggressive shut down techniques to microprocessor applications

Survey Of High Level Synthesis Systems Book Review: Unveiling the Magic of Language

In an electronic era where connections and knowledge reign supreme, the enchanting power of language has are more apparent than ever. Its power to stir emotions, provoke thought, and instigate transformation is actually remarkable. This extraordinary book, aptly titled "**Survey Of High Level Synthesis Systems**," compiled by a highly acclaimed author, immerses readers in a captivating exploration of the significance of language and its profound effect on our existence. Throughout this critique, we shall delve to the book is central themes, evaluate its unique writing style, and assess its overall influence on its readership.

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Table of Contents Survey Of High Level Synthesis Systems

1. Understanding the eBook Survey Of High Level Synthesis Systems
 - The Rise of Digital Reading Survey Of High Level Synthesis Systems
 - Advantages of eBooks Over Traditional Books
2. Identifying Survey Of High Level Synthesis Systems
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Survey Of High Level Synthesis Systems
 - User-Friendly Interface
4. Exploring eBook Recommendations from Survey Of High Level Synthesis Systems
 - Personalized Recommendations
 - Survey Of High Level Synthesis Systems User Reviews and Ratings
 - Survey Of High Level Synthesis Systems and Bestseller Lists

5. Accessing Survey Of High Level Synthesis Systems Free and Paid eBooks
 - Survey Of High Level Synthesis Systems Public Domain eBooks
 - Survey Of High Level Synthesis Systems eBook Subscription Services
 - Survey Of High Level Synthesis Systems Budget-Friendly Options
6. Navigating Survey Of High Level Synthesis Systems eBook Formats
 - ePub, PDF, MOBI, and More
 - Survey Of High Level Synthesis Systems Compatibility with Devices
 - Survey Of High Level Synthesis Systems Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Survey Of High Level Synthesis Systems
 - Highlighting and Note-Taking Survey Of High Level Synthesis Systems
 - Interactive Elements Survey Of High Level Synthesis Systems
8. Staying Engaged with Survey Of High Level Synthesis Systems
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Survey Of High Level Synthesis Systems
9. Balancing eBooks and Physical Books Survey Of High Level Synthesis Systems
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Survey Of High Level Synthesis Systems
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Survey Of High Level Synthesis Systems
 - Setting Reading Goals Survey Of High Level Synthesis Systems
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Survey Of High Level Synthesis Systems
 - Fact-Checking eBook Content of Survey Of High Level Synthesis Systems
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning

- Utilizing eBooks for Skill Development
- Exploring Educational eBooks

14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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