# A specification language must:

Be formal

 Founded on a mathematical theory (semantics)

Allow proofs of properties

Enable the development of support tools

5/21/2018 22

# **Specification Language And Methodolgy**

Jeannette M. Wing, Jim Woodcook, Jim Davies

#### **Specification Language And Methodolgy:**

SPECC: Specification Language and Methodology Daniel D. Gajski, Jianwen Zhu, Rainer Dömer, Andreas Gerstlauer, Shuging Zhao, 2012-12-06 For the near future the recent predictions and roadmaps of silicon semiconductor technology all agree that the number of transistors on a chip will keep growing exponentially according to Moore's Law pushing technology towards the system on a chip SOC era However we are increasingly experiencing a productivity gap where the chip complexity that can be handled by current design teams falls short of the possibilities offered by technological advances Together with growing time to market pressures this drives the need for innovative measures to increase design productivity by orders of magnitude It is commonly agreed that the solutions for achieving such a leap in design productivity lie in a shift of the focus of the design process to higher levels of abstraction on the one hand and in the massive reuse of predesigned complex system components intellectual property IP on the other hand In order to be successful both concepts eventually require the adoption of new languages and methodologies for system design backed up by the availability of a corresponding set of system level design automation tools This book presents the SpecC system level design language SLDL and the corresponding SpecC design methodology The SpecC language is intended for specification and design of SOCs or embedded systems including software and hardware whether using fixed platforms integrating systems from different IPs or synthesizing the system blocks from programming or hardware description languages SpecC Specification Language and Methodology describes the SpecC methodology that leads designers from an executable specification to an RTL implementation through a well defined sequence of steps Each model is described and guidelines are given for generating these models from executable specifications Finally the SpecC methodology is demonstrated on an industrial size example The design community is now entering the system level of abstraction era and SpecC is the enabling element to achieve a paradigm shift in design culture needed for system product design and manufacturing SpecC Specification Language and Methodology will be of interest to researchers designers and managers dealing with system level design design flows and methodologies as well as students learning system specification modeling and design A Methodology, Specification Language, and Automated Support Environment for Computer-aided Design Systems Duane Robert Worley, Rand Corporation, 1986 Computer based systems are becoming increasingly complex and expensive and there is a need for extensive design prior to embarking upon costly implementation phases Many design tools and design techniques have been proposed and some have been built to stand alone They are at best loosely integrated with other tools They have much in common notably the construction and manipulation of models of the complex system under design and a high degree of interactivity A detailed study of areas of commonality guides the requirements definition of a computer aided design of computer systems CADOCS support nucleus The nucleus provides an integrated set of primitives A specification language targeted to the support nucleus allows description of the design model and human model interaction This paper hypothesizes

that it is possible to define a methodology a specification language and an automated support environment suitable to the systematic development and execution of CADOCS systems The methodology is applied to UCLA's Systems Architect's Apprentice SARA system the test bed and the resulting SARA IDEAS system serves as an existence proof for the hypothesis Structured Object-Oriented Formal Language and Method Shaoying Liu, 2013-08-04 This book constitutes the thoroughly refereed post conference proceedings of the Second International Workshop on Structured Object Oriented Formal Language SOFL 2012 held in Kyoto Japan in November 2012 The 10 full papers presented were carefully reviewed and selected for inclusion in this book and address the following topics of interest testing and tools tools for specification model checking and application and prototyping Semantics of Specification Languages (SoSL) Derek J. Andrews, Jan F. Groote, C.A. Middelburg, 2012-12-06 SoSL was the first International Workshop on Semantics of Specification Languages held from 25 27 October 1993 in Utrecht the Netherlands The workshop was organized by the Department of Philosophy of Utrecht University with financial support from the Nationale Faciliteit Informatica of the Nederlandse Organisatie voor Wetenschappelijk Onderzoek NWO and under the auspices of the British Computer Society S specialist group in Formal Aspects of Computing Science BCS FACS The concern of the workshop was the semantics of specification languages and the issues closely related to this area such as type checking and the justification of proof rules and proof obligations Its aim was the exchange of problems and ideas in this field of formal methods and the identification of common programs of work for further investigation The program of SoSL consisted of 3 invited lectures presenting the developments of the semantics of 3 major specification languages Furthermore there were 16 presentations of submitted papers This volume provides a direct account of the workshop It contains 3 papers that match the invited lectures and the 16 selected papers The editors want to thank all those who have contributed to the workshop the Program Committee and the referees for selecting the contributed papers the invited speakers for their interesting talks the Organizing Committee for all their efforts and of course the participants We have the feeling that the workshop was worthwhile and should be repeated Specification of Software Systems V.S. Alagar, K. Periyasamy, 2011-03-19 This extensively revised and updated new edition of Specification of Software Systems builds upon the original focus on software specification with added emphasis on the practice of formal methods for specification and verification activities for different types of software systems and at different stages of developing software systems Topics and features provides a wide coverage of formal specification techniques and a clear writing style supported by end of chapter bibliographic notes for further reading presents a logical structure with sections devoted to specification fundamentals basics of formalism logic set theory and relations property oriented specification methods and model based specification techniques contains end of chapter exercises and numerous case studies with potential course outlines suggested in the Preface covers Object Z B Method and Calculus of Communicating Systems offers material that can be taught with tool supported laboratory projects NASA Formal Methods Alwyn Goodloe, Suzette Person, 2012-03-30 This

book constitutes the refereed proceedings of the Fourth International Symposium on NASA Formal Methods NFM 2012 held in Norfolk VA USA in April 2012 The 36 revised regular papers presented together with 10 short papers 3 invited talks were carefully reviewed and selected from 93 submissions The topics are organized in topical sections on theorem proving symbolic execution model based engineering real time and stochastic systems model checking abstraction and abstraction refinement compositional verification techniques static and dynamic analysis techniques fault protection cyber security specification formalisms requirements analysis and applications of formal techniques Logics of Specification Languages Dines Bjørner, Martin C. Henson, 2007-12-05 This book presents comprehensive studies on nine specification languages and their logics of reasoning The editors and authors are authorities on these specification languages and their application In a unique feature the book closes with short commentaries on the specification languages written by researchers closely associated with their original development The book contains extensive references and pointers to future developments

Technical Reports Awareness Circular: TRAC., 1987-10 FM'99 - Formal Methods Jeannette M. Wing, Jim Woodcook, Jim Davies, 1999-09-13 Formal methods are coming of age Mathematical techniques and tools are now regarded as an important part of the development process in a wide range of industrial and governmental organisations A transfer of technology into the mainstream of systems development is slowly but surely taking place FM 99 the First World Congress on Formal Methods in the Development of Computing Systems is a result and a measure of this new found maturity It brings an impressive array of industrial and applications oriented papers that show how formal methods have been used to tackle real problems These proceedings are a record of the technical symposium of FM 99 alo side the papers describingapplicationsofformalmethods youwill ndtechnical reports papers and abstracts detailing new advances in formaltechniques from mathematical foundations to practical tools The World Congress is the successor to the four Formal Methods Europe Symposia which in turn succeeded the four VDM Europe Symposia This s cession re ects an increasing openness within the international community of researchers and practitioners papers were submitted covering a wide variety of formal methods and application areas The programmecommittee re ects the Congress s international nature with a membership of 84 leading researchersfrom 38 di erent countries The comm tee was divided into 19 tracks each with its own chair to oversee the reviewing process Our collective task was a di cult one there were 259 high quality s missions from 35 di Guidelines for Formal Verification Systems, 1989 This document explains the requirements for formal erent countries verification systems that are candidates for the NCSC s Endorsed Tools List ETL This document is primarily intended for developers of verification systems to use in the development of production quality formal verification systems It explains the requirements and the process used to evaluate formal verification systems submitted to the NCSC for endorsement DTIC

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