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Special Computer Architectures for Pattern Processing

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Special Computer Architectures For Pattern Processing

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Special Computer Architectures For Pattern Processing:

Special Computer Architectures for Pattern Processing King-Sun Fu, 2018-05-04 It has been recognized for a long time that a conventional sequential processor is inefficient for operations on pictorial data where relatively simple operations need to be performed on a large number of data elements pixels Though many parallel processing architectures for picture processing have been proposed in the past very few have actually been implemented due to the costs involved With LSI technology it is becoming possible to realize parallel architectures at a modest cost In the following the authors review some of the proposed architectures for pattern recognition and image processing

Computer Architectures for Spatially Distributed Data Herbert Freeman, G.G. Pieroni, 2013-11-09 These are the proceedings of a NATO Advanced Study Institute ASI held in Cetraro Italy during 6-17 June 1983 The title of the ASI was Computer Architectures for Spatially Distributed Data and it brought together some 60 participants from Europe and America Presented here are 21 of the lectures that were delivered The articles cover a wide spectrum of topics related to computer architectures specially oriented toward the fast processing of spatial data and represent an excellent review of the state of the art of this topic For more than 20 years now researchers in pattern recognition image processing meteorology remote sensing and computer engineering have been looking toward new forms of computer architectures to speed the processing of data from two and three dimensional processes The work can be said to have commenced with the landmark article by Steve Unger in 1958 and it received a strong forward push with the development of the ILIAC III and IV computers at the University of Illinois during the 1960s One clear obstacle faced by the computer designers in those days was the limitation of the state of the art of hardware when the only switching devices available to them were discrete transistors As a result parallel processing was generally considered to be impractical and relatively little progress was made

VLSI for Pattern Recognition and Image Processing K.S. Fu, 2013-03-08 During the past two decades there has been a considerable growth in interest in problems of pattern recognition and image processing PRIP This interest has created an increasing need for methods and techniques for the design of PRIP systems PRIP involves analysis classification and interpretation of data Practical applications of PRIP include character recognition remote sensing analysis of medical signals and images fingerprint and face identification target recognition and speech understanding One difficulty in making PRIP systems practically feasible and hence more popularly used is the requirement of computer time and storage This situation is particularly serious when the patterns to be analyzed are quite complex Thus it is of the utmost importance to investigate special computer architectures and their implementations for PRIP Since the advent of VLSI technology it is possible to put thousands of components on one chip This reduces the cost of processors and increases the processing speed VLSI algorithms and their implementations have been recently developed for PRIP This book is intended to document the recent major progress in VLSI system design for PRIP applications

Multicomputers and Image Processing Kendall Preston, 1982-01-01 Multicomputers and Image Processing

Algorithms and Programs is the second of a set presenting papers from the third meeting held in Madison Wisconsin on May 27 30 1981 The workshop explores the large and powerful multicomputer arrays and networks with particular emphasis on the related aspects of developing algorithms and programs for multicomputer architectures Separating 33 papers into chapters this book reflects the three major aspects of the problem user algorithms and programs higher level languages and multicomputer architectures The first chapters present specific larger structure as well as whole program algorithms and their respective applications Other chapters describe the important high level programming of Fortran based language for the massively parallel processors and the set theory based language for expressing the structural image processing and perceptual operations effected by Cytocomputer and other cellular array motivated architectures The concluding chapters examine several major types of computer architectures that are being developed for large parallel serial multicomputer systems This book is of great value to computer programmers and scientists

Morphological Image Processing: Architecture and VLSI design P.P. Jonker, 2012-12-06 Summary Based on the experiences of past designs and the outcome of recent studies in the comparisons of low level image processing architectures a pipelined system for real time low image processing has been designed and realized in CMOS technology To minimize design pitfalls a study was performed to the details of the design solutions that have been found in embodimentsof the three main architectural groups of image processing the Square Processor Arrays the Linear Processor Arrays and the Pipelines This is reflected in a theoretical model As the design is based on bitplane wise processing of images research was performed on the principles of Cellular Logic Processing of two dimensional images of binary A methodology has been developed that is based on the transformation images using sets of Hit or Miss masks This method appeared to be extendable to higher dimensional images A theoretical model for the generation of break point conditions in high dimensional images has been developed and applied up to dimension three

Fundamentals in Computer Vision O. D. Faugeras, 1983-07-28 **VLSI Design Methodologies for Digital Signal Processing Architectures** Magdy A. Bayoumi, 2012-12-06 Designing VLSI systems represents a challenging task It is a transfonnation among different specifications corresponding to different levels of design abstraction behavioral stntctural and physical The behavioral level describes the functionality of the design It consists of two components static and dynamic The static component describes operations whereas the dynamic component describes sequencing and timing The structural level contains infonnation about components control and connectivity The physical level describes the constraints that should be imposed on the floor plan the placement of components and the geometry of the design Constraints of area speed and power are also applied at this level To implement such multilevel transfonnation a design methodology should be devised taking into consideration the constraints limitations and properties of each level The mapping process between any of these domains is non isomorphic A single behavioral component may be transfonned into more than one structural component Design methodologies are the most recent evolution in the design automation era which started off with the

introduction and subsequent usage of module generation especially for regular structures such as PLA s and memories A design methodology should offer an integrated design system rather than a set of separate unrelated routines and tools A general outline of a desired integrated design system is as follows Decide on a certain unified framework for all design levels Derive a design method based on this framework Create a design environment to implement this design method

Handbook of Artificial Intelligence Applications for Industrial Sustainability Vikas Garg, Richa Goel, Pooja Tiwari, Esra S. Döngül, 2024-01-30 The subject of Artificial Intelligence AI is continuing on its journey of affecting each and every individual and will keep on this path in the times to come This handbook is a collection of topics on the application of artificial intelligence applications for sustainability in different areas It provides an insight into the various uses of concepts and practical examples for different domains all in one place which makes it unique and important for the potential reader Handbook of Artificial Intelligence Applications for Industrial Sustainability Concepts and Practical Examples examines the influence of AI and how it can be used in several industries to improve corporate performance reduce security concerns improve customer experience and ultimately generate value for customers and maximize profits The handbook offers practical examples concepts and applications that provide an easy understanding and implementation process It provides AI applications in many fields such as sustainable credit decisions cyber security and fraud prevention warehouse management and much more This handbook will provide insight to customers managers professionals engineers researchers and students on the various uses of AI and sustainability in different domains All of this needed information compiled into one handbook makes it unique and important for the engineering business and computer science communities *Image Sequence*

Processing and Dynamic Scene Analysis T. S. Huang, 2012-12-06 This volume contains the proceedings of the NATO Advanced Study Institute on Image Sequence Processing and Dynamic Scene Analysis held 21 June 2 July 1982 in Hotel Maritim Braunlage Harz Federal Republic of Germany The organizing committee of the institute consists of T S Huang Director H G Musmann Co Director H H Nagel Consultant and C E Liedtke and W Geuen Local arrangement This Institute was devoted to the rapidly emerging field of image sequence processing and dynamic scene analysis which has many important applications including target tracking television bandwidth compression highway traffic monitoring and analysis of heart wall motion for medical diagnosis The lectures and discussions in this Institute fell into three overlapping categories Motion estimation pattern recognition and artificial intelligence techniques in dynamic scene analysis and applications 1 Motion estimation One of the most important problems in image sequence analysis and dynamic scene analysis is displacement and motion estimation For example in interframe coding using temporal DPCM displacement estimation and compensation can improve efficiency significantly Also estimated motion parameters can be powerful cues in target segmentation detection and classification In this Institute a number of recently developed techniques for displacement and motion estimation were discussed Real-Time Object Measurement and Classification Anil K. Jain, 2012-12-06 This book

contains papers presented at the NATO Advanced Research Workshop on Real time Object and Environment Measurement and Classification held in Hotel Villa del Mare Maratea Italy August 31 September 3 1987 This workshop was organized under the NATO Special Programme on Sensory Systems for Robotic Control Professor Eric Backer Delft University of Technology The Netherlands and Professor Erdal Panayirci Technical University of Istanbul Turkey were the members of the organizing committee for this workshop There were four major themes of this workshop Real time Requirements Feature Measurement Object Representation and Recognition and Architecture for Measurement and Classification A total of twenty five technical presentations were made These talks covered a wide spectrum of topics including hardware implementation of specific vision algorithms a complete vision system for object tracking and inspection using three cameras trinocular stereo for feature measurement neural network for object recognition integration of CAD Computer Aided Design and vision systems and the use of pyramid architectures for solving various computer vision problems

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