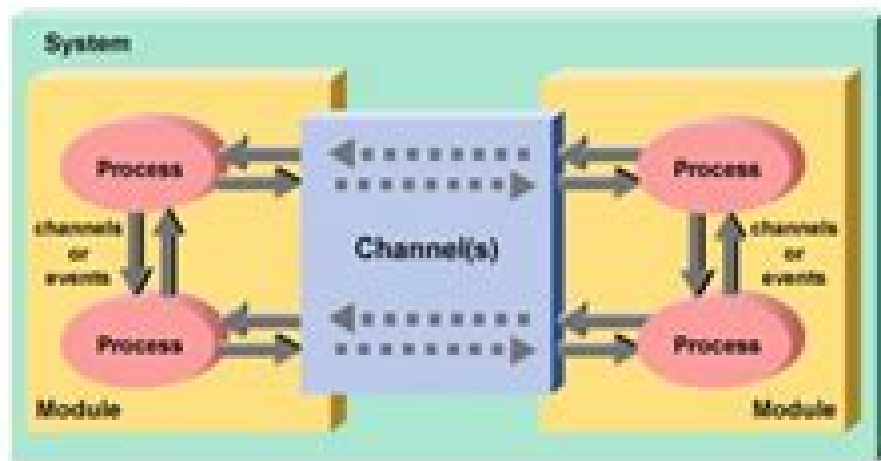


System-level Synthesis Data Model

▼ SSDM (System-level Synthesis Data Model)

- Hierarchical netlist of concurrent processes and communication channels



- Each leaf process contains a sequential program which is represented by an extended LLVM IR with hardware-specific semantics
 - Port / IO interfaces, bit-vector manipulations, cycle-level notations

System Level Synthesis

F Rizvi



System Level Synthesis:

System-Level Synthesis Ahmed Amine Jerraya, Jean Mermet, 2012-12-06 System Level Synthesis deals with the concurrent design of electronic applications including both hardware and software. The issue has become the bottleneck in the design of electronic systems including both hardware and software in several major industrial fields including telecommunications, automotive and aerospace engineering. The major difficulty with the subject is that it demands contributions from several research fields including system specification, system architecture, hardware design and software design. Most existing books cover well only a few aspects of system level synthesis. The present volume presents a comprehensive discussion of all the aspects of system level synthesis. Each topic is covered by a contribution written by an international authority on the subject.

System-level Modelling and Design Space Exploration for Multiprocessor Embedded System-on-chip Architectures Cagkan Erbas, 2006 Modern embedded systems come with contradictory design constraints. On one hand, these systems often target mass production and battery-based devices and therefore should be cheap and power efficient. On the other hand, they still need to show high, sometimes real-time performance and often support multiple applications and standards which requires high programmability. This wide spectrum of design requirements leads to complex heterogeneous System on Chip (SoC) architectures consisting of several types of processors from fully programmable microprocessors to configurable processing cores and customized hardware components integrated on a single chip. This study targets such multiprocessor embedded systems and strives to develop algorithms, methods and tools to deal with a number of fundamental problems which are encountered by the system designers during the early design stages.

System-Level Design Techniques for Energy-Efficient Embedded Systems Marcus T. Schmitz, Bashir M. Al-Hashimi, Petru Eles, 2006-01-16 System Level Design Techniques for Energy Efficient Embedded Systems addresses the development and validation of co-synthesis techniques that allow an effective design of embedded systems with low energy dissipation. The book provides an overview of a system level co-design flow illustrating through examples how system performance is influenced at various steps of the flow including allocation, mapping and scheduling. The book places special emphasis upon system level co-synthesis techniques for architectures that contain voltage-scalable processors which can dynamically trade off between computational performance and power consumption. Throughout the book, the introduced co-synthesis techniques which target both single mode systems and emerging multi mode applications are applied to numerous benchmarks and real life examples including a realistic smart phone.

System Level Design with .Net Technology El Mostapha Aboulhamid, Frederic Rousseau, 2018-10-03 The first book to harness the power of .NET for system design. System Level Design with .NET Technology constitutes a software-based approach to design modeling, verification and simulation. World class developers who have been at the forefront of system design for decades explain how to tap into the power of this dynamic programming environment for more effective and efficient management of metadata and introspection and

interoperability between tools Using readily available technology the text details how to capture constraints and requirements at high levels and describes how to percolate them during the refinement process Departing from proprietary environments built around System Verilog and VHDL this cutting edge reference includes an open source environment ESys NET that readers can use to experiment with new ideas algorithms and design methods and to expand the capabilities of their current tools It also covers Modeling and simulation including requirements specification IP reuse and applications of design patterns to hardware software systems Simulation and validation including transaction based models accurate simulation at cycle and transaction levels cosimulation and acceleration technique as well as timing specification and validation Practical use of the ESys NET environment Worked examples end of chapter references and the ESys NET implementation test bed make this the ideal resource for system engineers and students looking to maximize their embedded system designs

System Design Automation Renate Merker,Wolfgang Schwarz,2013-03-09 Design automation of electronic and hybrid systems is a steadily growing field of interest and a permanent challenge for researchers in Electronics Computer Engineering and Computer Science System Design Automation presents some recent results in design automation of different types of electronic and mechatronic systems It deals with various topics of design automation ranging from high level digital system synthesis through analogue and heterogeneous system analysis and design up to system modeling and simulation Design automation is treated from the aspects of its theoretical fundamentals its basic approach and its methods and tools Several application cases are presented in detail The book consists of three chapters High Level System Synthesis Digital Hardware Software Systems Here embedded systems distributed systems and processor arrays as well as hardware software codesign are treated Also three special application cases are discussed in detail Analog and Heterogeneous System Design System Approach and Methodology This chapter copes with the analysis and design of hybrid systems comprised of analog and digital electronic and mechanical components System Simulation and Evaluation Methods and Tools In this chapter object oriented Modelling analog system simulation including fault simulation parameter optimization and system validation are regarded The contents of the book are based on material presented at the Workshop System Design Automation SDA 2000 organised by the Sonderforschungsbereich 358 of the Deutsche Forschungsgemeinschaft at TU Dresden

Models in System Design Jean-Michel Bergé,Oz Levia,Jacques Rouillard,2012-12-06 Models in System Design tracks the general trend in electronics in terms of size complexity and difficulty of maintenance System design is by nature combined with prototyping mixed domain design and verification and it is no surprise that today s modeling and models are used in various levels of system design and verification In order to deal with constraints induced by volume and complexity new methods and techniques have been defined Models in System Design provides an overview of the latest modeling techniques for use by system designers The first part of the book considers system level design discussing such issues as abstraction performance and trade offs There is also a section on automating system design The second part of the book

deals with some of the newest aspects of embedded system design These include co verification and prototyping Finally the book includes a section on the use of the MCSE methodology for hardware software co design Models in System Design will help designers and researchers to understand these latest techniques in system design and as such will be of interest to all involved in embedded system design

Electronic System-Level HW/SW Co-Design of Heterogeneous Multi-Processor Embedded Systems Luigi Pomante,2022-09-01 Modern electronic systems consist of a fairly heterogeneous set of components Today a single system can be constituted by a hardware platform frequently composed of a mix of analog and digital components and by several software application layers The hardware can include several heterogeneous microprocessors e g GPP DSP GPU etc dedicated ICs ASICs and or FPGAs memories a set of local connections between the system components and some interfaces between the system and the environment sensors actuators etc Therefore on the one hand multi processor embedded systems are capable of meeting the demand of processing power and flexibility of complex applications On the other hand such systems are very complex to design and optimize so that the design methodology plays a major role in determining the success of the products For these reasons to cope with the increasing system complexity the approaches typically used today are oriented towards co design methodologies working at the higher levels of abstraction Unfortunately such methodologies are typically customized for the specific application suffer of a lack of generality and still need a considerable effort when real size project are envisioned Therefore there is still the need for a general methodology able to support the designer during the high level steps of a co design flow enabling an effective design space exploration before tackling the low level steps and thus committing to the final technology This should prevent costly redesign loops In such a context the work described in this book composed of two parts aims at providing models methodologies and tools to support each step of the co design flow of embedded systems implemented by exploiting heterogeneous multi processor architectures mapped on distributed systems as well as fully integrated onto a single chip

Computer Hardware Description Languages and their Applications D. Agnew,L. Claesen,R. Camposano,2014-05-21 Hardware description languages HDLs have established themselves as one of the principal means of designing electronic systems The interest in and usage of HDLs continues to spread rapidly driven by the increasing complexity of systems the growth of HDL driven synthesis the research on formal design methods and many other related advances This research oriented publication aims to make a strong contribution to further developments in the field The following topics are explored in depth BDD based system design and analysis system level formal verification formal reasoning on hardware languages for protocol specification VHDL HDL based design methods high level synthesis and text graphical HDLs There are short papers covering advanced design capture and recent work in high level synthesis and formal verification In addition several invited presentations on key issues discuss and summarize recent advances in real time system design automatic verification of sequential circuits and languages for protocol specification

Automating the Design of Computer Systems William P.

Birmingham, Anurag P. Gupta, Daniel P. Siewiorek, 1992-12-18 Addressing the issues of engineering design in computer architecture this book describes the design and implementation of MICON a system for automating the synthesis of small computers

AI-Enabled Electronic Circuit and System Design Ali Iranmanesh, Hossein Sayadi, 2025-01-27 As our world becomes increasingly digital electronics underpin nearly every industry Understanding how AI enhances this foundational technology can unlock innovations from smarter homes to more powerful gadgets offering vast opportunities for businesses and consumers alike This book demystifies how AI streamlines the creation of electronic systems making them smarter and more efficient With AI's transformative impact on various engineering fields this resource provides an up to date exploration of these advancements authored by experts actively engaged in this dynamic field Stay ahead in the rapidly evolving landscape of AI in engineering with AI Enabled Electronic Circuit and System Design From Ideation to Utilization your essential guide to the future of electronic systems endif A transformative guide describing how revolutionizes electronic design through AI integration Highlighting trends challenges and opportunities Demystifies complex AI applications in electronic design for practical use Leading insights authored by top experts actively engaged in the field Offers a current relevant exploration of significant topics in AI's role in electronic circuit and system design Editor's bios Dr Ali A Iranmanesh is the founder and CEO of Silicon Valley Polytechnic Institute He has received his Bachelor of Science in Electrical Engineering from Sharif University of Technology SUT Tehran Iran and both his master's and Ph D degrees in Electrical Engineering and Physics from Stanford University in Stanford CA He additionally holds a master's degree in business administration MBA from San Jose State University in San Jose CA Dr Iranmanesh is the founder and chairman of the International Society for Quality Electronic Design ISQED Currently he serves as the CEO of Innovotek Dr Iranmanesh has been instrumental in advancing semiconductor technologies innovative design methodologies and engineering education He holds nearly 100 US and international patents reflecting his significant contributions to the field Dr Iranmanesh is the Senior life members of IEEE senior member of the American Society for Quality co founder and Chair Emeritus of the IEEE Education Society of Silicon Valley Vice Chair Emeritus of the IEEE PV chapter and recipient of IEEE Outstanding Educator Award Dr Hossein Sayadi is a Tenure Track Assistant Professor and Associate Chair in the Department of Computer Engineering and Computer Science at California State University Long Beach CSULB He earned his Ph D in Electrical and Computer Engineering from George Mason University in Fairfax Virginia and an M Sc in Computer Engineering from Sharif University of Technology in Tehran Iran As a recognized researcher with over 14 years of research experience Dr Sayadi is the founder and director of the Intelligent Secure and Energy Efficient Computing iSEC Lab at CSULB His research focuses on advancing hardware security and trust AI and machine learning cybersecurity and energy efficient computing addressing critical challenges in modern computing and cyber physical systems He has authored over 75 peer reviewed publications in leading conferences and journals Dr Sayadi is the CSU STEM NET Faculty Fellow with his research supported by multiple

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