SystemC

Methodologies and Applications

Edited by W. Müller, W. Rosenstiel and J. Ruf

Foreword by Grant Martin and Thorsten Grötker



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Sandro Rigo, Rodolfo Azevedo, Luiz Santos

Systemc Methodologies And Applications:

SystemC Wolfgang Müller, Wolfgang Rosenstiel, Jürgen Ruf, 2003-05-31 This comprehensive survey on the state of the art of SystemC in industry and research is organised into 11 self contained chapters Selected SystemC experts present their approaches in the domains of modelling analysis and synthesis ranging from mixed signal and discrete system to embedded Design Methods and Applications for Distributed Embedded Systems Bernd Kleinjohann, Guang R. Gao, Hermann Kopetz, Lisa Kleinjohann, Achim Rettberg, 2006-04-11 The IFIP TC 10 Working Conference on Distributed and Parallel Embedded Systems DIPES 2004 brings together experts from industry and academia to discuss recent developments in this important and growing field in the splendid city of Toulouse France The ever decreasing price performance ratio of microcontrollers makes it economically attractive to replace more and more conventional mechanical or electronic control systems within many products by embedded real time computer systems An embedded real time computer system is always part of a well specified larger system which we call an intelligent product Although most intelligent products start out as stand alone units many of them are required to interact with other systems at a later stage At present many industries are in the middle of this transition from stand alone products to networked embedded systems This transition requires reflection and architecting The complexity of the evolving distributed artifact can only be controlled if careful planning and principled design methods replace the hoc engineering of the first version of many standalone embedded products **Simulation and** Modeling Methodologies, Technologies and Applications Mohammad S. Obaidat, Tuncer Ören, Floriano De Rango, 2018-11-20 This book highlights a set of selected revised and extended papers from the 7th International Conference on Simulation and Modeling Methodologies Technologies and Applications SIMULTECH 2017 held in Madrid Spain on July 26 to 28 2017 The conference brought together researchers engineers and practitioners whose work involves methodologies in and applications of modeling and simulation The papers showcased here represent the very best papers from the Conference and report on a broad range of new and innovative solutions Real-Time Simulation Technologies: Principles, Methodologies, and Applications Katalin Popovici, Pieter Mosterman, 2017-12-19 Real Time Simulation Technologies Principles Methodologies and Applications is an edited compilation of work that explores fundamental concepts and basic techniques of real time simulation for complex and diverse systems across a broad spectrum Useful for both new entrants and experienced experts in the field this book integrates coverage of detailed theory acclaimed methodological approaches entrenched technologies and high value applications of real time simulation all from the unique perspectives of renowned international contributors Because it offers an accurate and otherwise unattainable assessment of how a system will behave over a particular time frame real time simulation is increasingly critical to the optimization of dynamic processes and adaptive systems in a variety of enterprises These range in scope from the maintenance of the national power grid to space exploration to the development of virtual reality programs and cyber physical systems This book outlines how for these and

other undertakings engineers must assimilate real time data with computational tools for rapid decision making under uncertainty Clarifying the central concepts behind real time simulation tools and techniques this one of a kind resource Discusses the state of the art important challenges and high impact developments in simulation technologies Provides a basis for the study of real time simulation as a fundamental and foundational technology Helps readers develop and refine principles that are applicable across a wide variety of application domains As science moves toward more advanced technologies unconventional design approaches and unproven regions of the design space simulation tools are increasingly critical to successful design and operation of technical systems in a growing number of application domains This must have resource presents detailed coverage of real time simulation for system design parallel and distributed simulations industry tools and a large set of applications SystemC Kernel Extensions for Heterogeneous System Modeling Hiren Patel, Sandeep Kumar Shukla, 2006-01-16 System C Kernel Extensions for Heterogeneous System Modeling is a result of an almost two year endeavour on our part to understand how SystemC can be made useful for system level modeling at higher levels of abstraction Making it a truly heterogeneous modeling language and platform for hardware software co design as well as complex embedded hardware designs has been our focus in the work reported in this book their Application Brian Bailey, Grant Martin, 2009-12-15 This book arises from experience the authors have gained from years of work as industry practitioners in the field of Electronic System Level design ESL At the heart of all things related to Electronic Design Automation EDA the core issue is one of models what are the models used for what should the models contain and how should they be written and distributed Issues such as interoperability and tool transportability become central factors that may decide which ones are successful and those that cannot get sufficient traction in the industry to survive Through a set of real examples taken from recent industry experience this book will distill the state of the art in terms of System Level Design models and provide practical guidance to readers that can be put into use This book is an invaluable tool that will aid readers in their own designs reduce risk in development projects expand the scope of design projects and improve developmental processes and project planning Formal Methods: Applications and Technology Lubos Brim, Boudewijn Haverkort, Martin Leucker, Jaco van de Pol, 2007-06-27 This book constitutes the thoroughly refereed joint post proceedings of the two International Workshops on Formal Methods for Industrial Critical Systems FMICS 2006 and on Parallel and Distributed Methods in Verification PDMC 2006 held in Bonn Germany in August 2006 in the course of the 17th International Conference on Concurrency Theory CONCUR 2006 Electronic System Level Design Sandro Rigo, Rodolfo Azevedo, Luiz Santos, 2011-04-28 Electronic System Level Design an Open Source Approach is based on the successful experience acquired with the conception of the ADL ArchC the development of its underlying tool suite and the building of its platform modeling infrastructure With more than 10000 accesses per year since 2004 the dissemination of ArchC models reached not only students in quest of proper infrastructure to develop their research projects but also some

companies in need of processor models to build virtual platforms using SystemC The need to anticipate the development of hardware dependent software and to build virtual prototypes gave rise to Transaction Level Modeling TLM Since SystemC provided the elements and the adequate abstraction level for supporting TLM their relation has grown so strong that OSCI created a TLM Working Group whose effort resulted in the recently released TLM 2 0 standard which is also covered in this **ESL Design and Verification** Grant Martin, Brian Bailey, Andrew Piziali, 2010-07-27 Visit the authors companion site http www electronicsystemlevel com Includes interactive forum with the authors Electronic System Level ESL design has mainstreamed it is now an established approach at most of the world's leading system on chip SoC design companies and is being used increasingly in system design From its genesis as an algorithm modeling methodology with no links to implementation ESL is evolving into a set of complementary methodologies that enable embedded system design verification and debug through to the hardware and software implementation of custom SoC system on FPGA system on board and entire multi board systems. This book arises from experience the authors have gained from years of work as industry practitioners in the Electronic System Level design area they have seen SLD or ESL go through many stages and false starts and have observed that the shift in design methodologies to ESL is finally occurring This is partly because of ESL technologies themselves are stabilizing on a useful set of languages being standardized SystemC is the most notable and use models are being identified that are beginning to get real adoption ESL DESIGN VERIFICATION offers a true prescriptive guide to ESL that reviews its past and outlines the best practices of today Table of ContentsCHAPTER 1 WHAT IS ESL CHAPTER 2 TAXONOMY AND DEFINITIONS FOR THE ELECTRONIC SYSTEM LEVEL CHAPTER 3 EVOLUTION OF ESL DEVELOPMENT CHAPTER 4 WHAT ARE THE ENABLERS OF ESL CHAPTER 5 ESL FLOW CHAPTER 6 SPECIFICATIONS AND MODELING CHAPTER 7 PRE PARTITIONING ANALYSIS CHAPTER 8 PARTITIONING CHAPTER 9 POST PARTITIONING ANALYSIS AND DEBUG CHAPTER 10 POST PARTITIONING VERIFICATION CHAPTER 11 HARDWARE IMPLEMENTATION CHAPTER 12 SOFTWARE IMPLEMENTATION CHAPTER 13 USE OF ESL FOR IMPLEMENTATION VERIFICATION CHAPTER 14 RESEARCH EMERGING AND FUTURE PROSPECTS APPENDIX LIST OF ACRONYMS Provides broad comprehensive coverage not available in any other such book Massive global appeal with an internationally recognised author team Crammed full of state of the art content from notable industry experts Essential Issues in SOC Design Youn-Long Steve Lin, 2007-05-31 This book originated from a workshop held at the DATE 2005 conference namely Designing Complex SOCs State of the art in issues related to System on Chip SoC design by leading experts in the fields covers IP development verification integration chip implementation testing and software SOC design is fast becoming the key area of focus that engineers and researchers from the Electronic Design Automation field are focusing on in their quest to further develop Integrated Circuit technology The more systems and even networks that we can integrate on one piece of silicon the faster cheaper more powerful and efficient the technology will become Essential Issues in SOC Design contains valuable

academic and industrial examples for those involved with the design of complex SOCs all contributors are selected from a region of the world that is generally known to lead the SOC Revolution namely Asia

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