SystemC Kernel Extensions for Heterogeneous System Modeling

A Framework for Multi-MoC Modeling & Simulation

By
Hiren D. Patel and Sandeep K. Shukla

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SystemC Kernel Extensions for Heterogeneous System Modeling Hiren Patel, Sandeep Kumar Shukla, 2006-01-16
SystemC Kernel Extensions for Heterogeneous System Modeling is a result of an almost two year endeavour on our part to understand how SystemC can be made useful for system level modeling at higher levels of abstraction Making it a truly heterogeneous modeling language and platform for hardware software co design as well as complex embedded hardware designs has been our focus in the work reported in this book SystemC Kernel Extensions for Heterogeneous System Modeling Hiren Patel, Sandeep Kumar Shukla, 2008-11-01 SystemC Kernel Extensions for Heterogeneous System Modeling is a result of an almost two year endeavour on our part to understand how SystemC can be made useful for system level modeling at higher levels of abstraction Making it a truly heterogeneous modeling language and platform for hardware software co design as well as complex embedded hardware designs has been our focus in the work reported in this book

Synthesis of Embedded Software Sandeep Kumar Shukla, Jean-Pierre Talpin, 2010-08-05 Embedded software is ubiquitous today There are millions of lines of embedded code in smart phones and even more in systems responsible for automotive control avionics control weapons control and space missions Some of these are safety critical systems whose correctness timely response and reliability are of paramount importance. These requirement pose new challenges to system designers This necessitates that a proper design science based on constructive correctness be developed Correct by construction design and synthesis of embedded software is done in a way so that post development verification is minimized and correct operation of embedded systems is maximized This book presents the state of the art in the design of safety critical embedded software It introduced readers to three major approaches to specification driven embedded software synthesis construction synchronous programming based approaches models of computation based approaches and an approach based on concurrent programming with a co design focused language It is an invaluable reference for practitioners and researchers concerned with improving the product development life cycle Embedded Systems Specification and Design Languages Eugenio Villar, 2008-05-15 This book is the latest contribution to the Chip Design Languages series and it consists of selected papers presented at the Forum on Specifications and Design Languages FDL 07 in September 2007 The book represents the state of the art in research and practice and it identifies new research directions It highlights the role of specification and modelling languages and presents practical experiences with specification and modelling languages Model Driven Engineering Languages and Systems Dorina C. Petriu, Nicolas Rouquette, Oystein Haugen, 2010-09-22 The MODELS series of conferences is the premier venue for the exchange of novative technical ideas and experiences focusing on a very important new te nical discipline model driven software and systems engineering The expansion ofthisdisciplineisadirectconsequenceoftheincreasingsigni canceandsuccess of model based methods in practice Numerous e orts resulted in the invention of concepts languages and tools for the de nition analysis transformation and veri

cationofdomain speci cmodelinglanguagesandgeneral purposemodeling language standards as well as their use for software and systems engineering MODELS 2010 the 13th edition of the conference series took place in Oslo Norway October 3 8 2010 along with numerous satellite workshops symposia and tutorials. The conference was fortunate to have three prominent keynote speakers Ole Lehrmann Madsen Aarhus University Denmark Edward A Lee UC Berkeley USA and Pamela Zave AT T Laboratories USA To provide a broader forum for reporting on scienti c progress as well as on experience stemming from practical applications of model based methods the 2010 conference accepted submissions in two distinct tracks Foundations and Applications The primary objective of the rst track is to present new research results dedicated to advancing the state of the art of the discipline whereas the second aims to provide a realistic and veri able picture of the current state the practice of model based engineering so that the broader community could be better informed of the capabilities and successes of this relatively young discipline This volume contains the nal version of the papers accepted for presentation at the conference from both tracks Embedded Systems Design Based on Formal Models of Computation Ivan Radojevic, Zoran Salcic, 2011-06-15 Models of Computation for Heterogeneous Embedded Systems presents a model of computation for heterogeneous embedded systems called DFCharts It targets heterogeneous systems by combining finite state machines FSM with synchronous dataflow graphs SDFG FSMs are connected in the same way as in Argos a Statecharts variant with purely synchronous semantics using three operators synchronous parallel refinement and hiding The fourth operator called asynchronous parallel is introduced in DFCharts to connect FSMs with SDFGs In the formal semantics of DFCharts the operation of an SDFG is represented as an FSM Using this representation SDFGs are merged with FSMs so that the behaviour of a complete DFCharts specification can be expressed as a single flat FSM This allows system properties to be verified globally The practical application of DFCharts has been demonstrated by linking it to widely used system level Metamodeling-driven IP Reuse for SoC Integration and Microprocessor Design languages Java Esterel and SystemC Deepak A. Mathaikutty, Sandeep Shukla, 2009 This cutting edge resource offers you an in depth understanding of metamodeling approaches for the reuse of intellectual properties IPs in the form of reusable design or verification components The book covers the essential issues associated with fast and effective integration of reusable design components into a system on a chip SoC to achieve faster design turn around time Moreover it addresses key factors related to the use of reusable verification IPs for a write once use many times verification strategy another effective approach that can attain a faster product design cycle **Languages for Embedded Systems and their Applications** Martin Radetzki, 2009-05-24 Embedded systems take over complex control and data processing tasks in diverse application elds such as automotive avionics consumer products and telec munications They are the primary driver for improving overall system safety ef ciency and comfort The demand for further improvement in these aspects can only be satis ed by designing embedded systems of increasing complexity which in turn necessitates the development of new system design methodologies

based on speci cation design and veri cation languages The objective of the book at hand is to provide researchers and designers with an overview of current research trends results and application experiences in c puter languages for embedded systems The book builds upon the most relevant contributions to the 2008 conference Forum on Design Languages FDL the p mier international conference specializing in this eld These contributions have been selected based on the results of reviews provided by leading experts from search and industry In many cases the authors have improved their original work by adding breadth depth or explanation **ESL Models and their Application** Brian Bailey, Grant Martin, 2009-12-15 This book arises from experience the authors have gained from years of work as industry practitioners in the field of Electronic System Level design ESL At the heart of all things related to Electronic Design Automation EDA the core issue is one of models what are the models used for what should the models contain and how should they be written and distributed Issues such as interoperability and tool transportability become central factors that may decide which ones are successful and those that cannot get sufficient traction in the industry to survive Through a set of real examples taken from recent industry experience this book will distill the state of the art in terms of System Level Design models and provide practical guidance to readers that can be put into use This book is an invaluable tool that will aid readers in their own designs reduce risk in development projects expand the scope of design projects and improve developmental processes and project planning Embedded and Real-Time Systems Engineering for Communication Virtanen, Seppo, 2012-04-30 This book has collected the latest research within the field of real time systems engineering and will serve as a vital reference compendium for practitioners and academics Provided by publisher

Systemc Kernel Extensions For Heterogenous System Modeling Book Review: Unveiling the Power of Words

In some sort of driven by information and connectivity, the ability of words has be more evident than ever. They have the capacity to inspire, provoke, and ignite change. Such could be the essence of the book **Systemc Kernel Extensions For Heterogenous System Modeling**, a literary masterpiece that delves deep into the significance of words and their affect our lives. Compiled by a renowned author, this captivating work takes readers on a transformative journey, unraveling the secrets and potential behind every word. In this review, we shall explore the book is key themes, examine its writing style, and analyze its overall affect readers.

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