

Why SystemC?

SystemC describes overall system

- System level design
- Describing hardware architectures
- Describing software algorithms
- Verification
- IP exchange

System Design With Systemc

Renate Merker, Wolfgang Schwarz



System Design With SystemC:

System Design with SystemC Thorsten Grötter, Stan Liao, Grant Martin, Stuart Swan, 2007-05-08 I am honored and delighted to write the foreword to this very first book about SystemC. It is now an excellent time to summarize what SystemC really is and what it can be used for. The main message in the area of design in the 2001 International Technology Roadmap for Semiconductors (ITRS) is that cost of design is the greatest threat to the continuation of the semiconductor roadmap. This recent revision of the ITRS describes the major productivity improvements of the last few years as small block reuse, large block reuse, and IC implementation tools. In order to continue to reduce design cost, the required future solutions will be intelligent test benches and embedded system level methodology. As the new system level specification and design language, SystemC directly contributes to these two solutions. These will have the biggest impact on future design technology and will reduce system implementation cost. It took SystemC less than two years to emerge as the leader among the many new and well discussed system level design languages. In my opinion, this is due to the fact that SystemC adopted object oriented system level design, the most promising method already applied by the majority of firms during the last couple of years. Even before the introduction of SystemC, many system designers have attempted to develop executable specifications in C. These executable functional specifications are then refined to the well known transaction level to model the communication of system level processes.

Formal Methods and Models for System Design Rajesh Gupta, Paul Le Guernic, Sandeep Kumar Shukla, Jean-Pierre Talpin, 2004-10-01 Perhaps nothing characterizes the inherent heterogeneity in embedded systems than the ability to choose between hardware and software implementations of a given system function. Indeed, most embedded systems at their core represent a careful division and design of hardware and software parts of the system. To do this task effectively, models and methods are necessary: functionality to capture application behavior, needs, and system implementation constraints. Formal modeling can be valuable in addressing these tasks. As with most engineering domains, co-design practice defines the state of the art; it seeks to add new capabilities in system conceptualization, model transformation, optimization, and implementation. These advances, particularly those related to synthesis and verification tasks, directly depend upon formal understanding of system behavior and performance measures. Current practice in system modeling relies upon exploiting high level programming frameworks such as SystemC Esterel to capture design at increasingly higher levels of abstraction and attempts to reduce the system implementation task. While raising the abstraction levels for design and verification tasks to be really useful, these approaches must also provide for reuse/adaptation of the existing intellectual property (IP) blocks. *SystemC* Wolfgang Müller, Wolfgang Rosenstiel, Jürgen Ruf, 2007-05-08 SystemC has received a wide acceptance by users and tool vendors as the next generation system description language in order to deal with higher levels of abstraction for complex SoC designs. *SystemC Methodologies and Applications* gives a comprehensive survey on the state of the art of SystemC in industry and research. Organised into 11 self contained chapters, selected SystemC experts present their approaches in the domains of

modelling analysis synthesis Their contributions range from mixed signal and discrete system to embedded software The chapters give a broad overview of recent advances in SystemC methodologies and applications and are mainly based on presentations given at the European SystemC User Group meetings Design Principles for Embedded Systems KCS Murti, 2021-09-20 The book is designed to serve as a textbook for courses offered to graduate and undergraduate students enrolled in electronics and electrical engineering and computer science This book attempts to bridge the gap between electronics and computer science students providing complementary knowledge that is essential for designing an embedded system The book covers key concepts tailored for embedded system design in one place The topics covered in this book are models and architectures Executable Specific Languages SystemC Unified Modeling Language real time systems real time operating systems networked embedded systems Embedded Processor architectures and platforms that are secured and energy efficient A major segment of embedded systems needs hard real time requirements This textbook includes real time concepts including algorithms and real time operating system standards like POSIX threads Embedded systems are mostly distributed and networked for deterministic responses The book covers how to design networked embedded systems with appropriate protocols for real time requirements Each chapter contains 2-3 solved case studies and 10 real world problems as exercises to provide detailed coverage and essential pedagogical tools that make this an ideal textbook for students enrolled in electrical and electronics engineering and computer science programs *Formal and Practical Techniques for the Complex System Design Process using Virtual Prototypes* Pascal Pieper, Rolf Drechsler, 2024-03-25 This book deals with formal and practical approaches for early fast modeling and verification of complex digital processor hardware and software using SystemC based virtual prototypes As a special focus modeling approaches of instruction level behavior of System on Chips and the connected off chip digital devices are addressed Featured verification approaches are based on symbolic execution of simulated hardware devices or on classical discrete execution of the whole system with dynamic data flow tracking The approaches are accompanied by Case Studies that develop and build on top of an open source RISC V SoC simulation In Particular this book *System Design Automation* Renate Merker, Wolfgang Schwarz, 2013-03-09 Design automation of electronic and hybrid systems is a steadily growing field of interest and a permanent challenge for researchers in Electronics Computer Engineering and Computer Science System Design Automation presents some recent results in design automation of different types of electronic and mechatronic systems It deals with various topics of design automation ranging from high level digital system synthesis through analogue and heterogeneous system analysis and design up to system modeling and simulation Design automation is treated from the aspects of its theoretical fundamentals its basic approach and its methods and tools Several application cases are presented in detail The book consists of three chapters High Level System Synthesis Digital Hardware Software Systems Here embedded systems distributed systems and processor arrays as well as hardware software codesign are treated Also three special application cases are discussed in detail Analog

and Heterogeneous System Design System Approach and Methodology This chapter copes with the analysis and design of hybrid systems comprised of analog and digital electronic and mechanical components System Simulation and Evaluation Methods and Tools In this chapter object oriented Modelling analog system simulation including fault simulation parameter optimization and system validation are regarded The contents of the book are based on material presented at the Workshop System Design Automation SDA 2000 organised by the Sonderforschungsbereich 358 of the Deutsche Forschungsgemeinschaft at TU Dresden

Quality-Driven SystemC Design Daniel Große,Rolf Drechsler,2009-12-02 A quality driven design and verification flow for digital systems is developed and presented in Quality Driven SystemC Design Two major enhancements characterize the new flow First dedicated verification techniques are integrated which target the different levels of abstraction Second each verification technique is complemented by an approach to measure the achieved verification quality The new flow distinguishes three levels of abstraction namely system level top level and block level and can be incorporated in existing approaches After reviewing the preliminary concepts in the following chapters the three levels for modeling and verification are considered in detail At each level the verification quality is measured In summary following the new design and verification flow a high overall quality results

Languages, Design Methods, and Tools for Electronic System Design Marie-Minerve Louërat,Torsten Maehne,2014-08-21 This book brings together a selection of the best papers from the sixteenth edition of the Forum on specification and Design Languages Conference FDL which was held in September 2013 in Paris France FDL is a well established international forum devoted to dissemination of research results practical experiences and new ideas in the application of specification design and verification languages to the design modeling and verification of integrated circuits complex hardware software embedded systems and mixed technology systems

Design and Test Technology for Dependable Systems-on-Chip Ubar, Raimund,Raik, Jaan,Vierhaus, Heinrich Theodor,2010-12-31 This book covers aspects of system design and efficient modelling and also introduces various fault models and fault mechanisms associated with digital circuits integrated into System on Chip SoC Multi Processor System on Chip MPSoC or Network on Chip NoC

EDA for IC System Design, Verification, and Testing Louis Scheffer,Luciano Lavagno,Grant Martin,2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The first volume EDA for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logical verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for IC designs design and verification languages digital simulation hardware acceleration and emulation and much more Save on the complete set

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