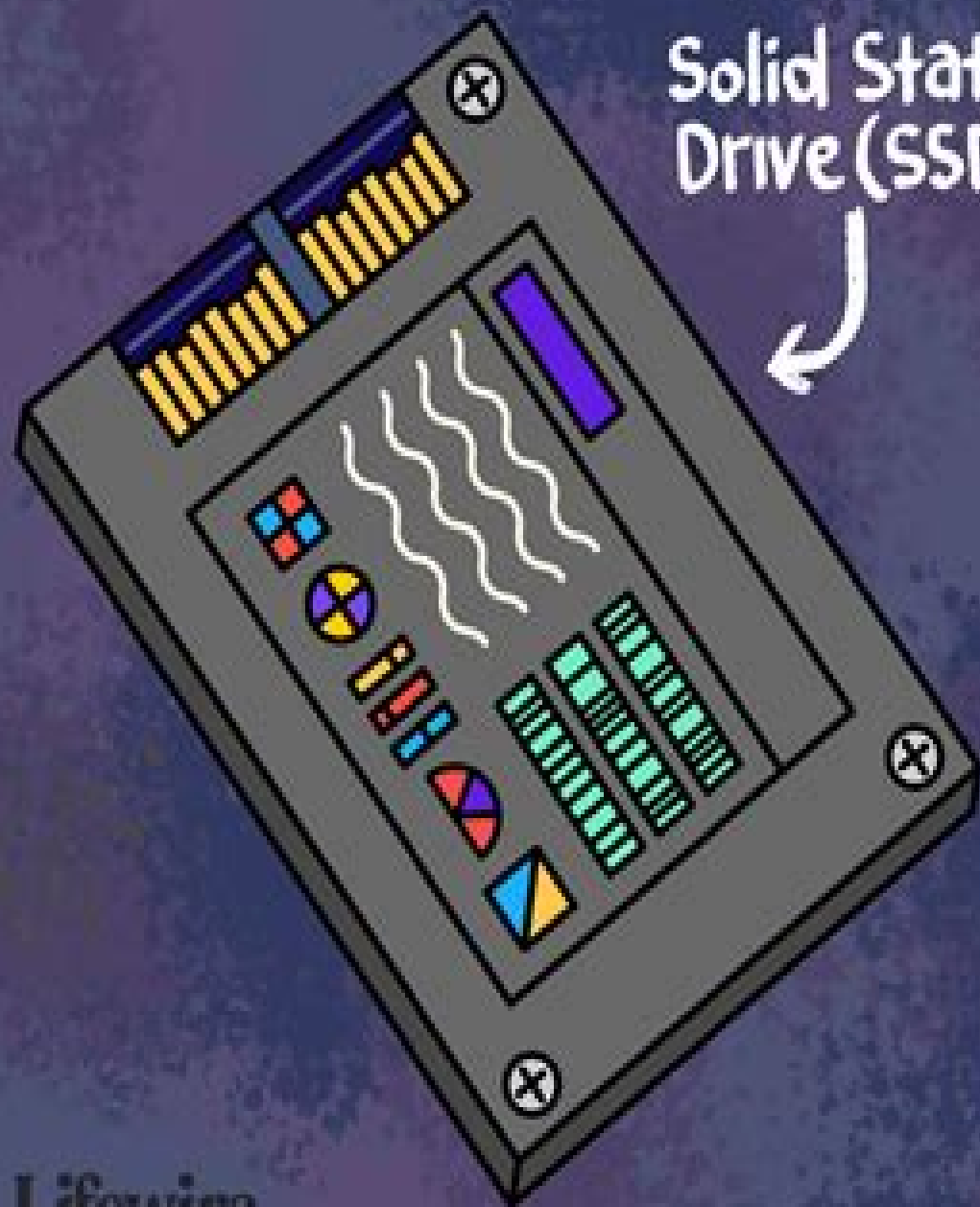
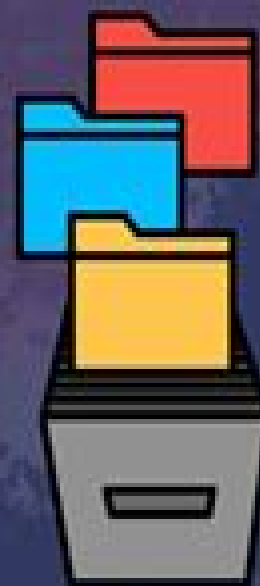


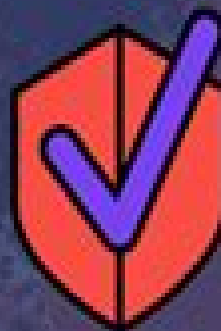


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Advanced MOS Device Physics Norman Einspruch, 2012-12-02 VLSI Electronics Microstructure Science Volume 18 Advanced MOS Device Physics explores several device physics topics related to metal oxide semiconductor MOS technology The emphasis is on physical description modeling and technological implications rather than on the formal aspects of device theory Special attention is paid to the reliability physics of small geometry MOSFETs Comprised of eight chapters this volume begins with a general picture of MOS technology development from the device and processing points of view The critical issue of hot carrier effects is discussed along with the device engineering aspects of this problem the emerging low temperature MOS technology and the problem of latchup in scaled MOS circuits Several device models that are suitable for use in circuit simulators are also described The last chapter examines novel electron transport effects observed in ultra small MOS structures This book should prove useful to semiconductor engineers involved in different aspects of MOS technology development as well as for researchers in this field and students of the corresponding disciplines III-V Semiconductor Materials and Devices R.J. Malik, 2012-12-02 The main emphasis of this volume is on III V semiconductor epitaxial and bulk

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Levy, 2012-12-06 The primary thrust of very large scale integration VLS is the miniaturization of devices to increase packing density achieve higher speed and consume lower power The fabrication of integrated circuits containing in excess of four million components per chip with design rules in the submicron range has now been made possible by the introduction of innovative circuit designs and the development of new microelectronic materials and processes This book addresses the latter challenge by assessing the current status of the science and technology associated with the production of VLSI silicon circuits It represents the cumulative effort of experts from academia and industry who have come together to blend their expertise into a tutorial overview and cohesive update of this rapidly expanding field A balance of fundamental and applied contributions cover the basics of microelectronics materials and process engineering Subjects in materials science include silicon silicides resists dielectrics and interconnect metallization Subjects in process engineering include crystal growth epitaxy oxidation thin film deposition fine line lithography dry etching ion implantation and diffusion Other related topics such as process simulation defects phenomena and diagnostic techniques are also included This book is the result of a NATO sponsored Advanced Study Institute AS held in Castelvecchio Pascoli Italy Invited speakers at this institute provided manuscripts which were edited updated and integrated with other contributions solicited from non participants to this AS

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