Supercompilers for Parallel and Vector Computers

Hans Zima

with
Barbara Chapman

Supercompilers For Parallel And Vector Computers

Günter Böckle

Supercompilers For Parallel And Vector Computers:

Supercompilers for Parallel and Vector Computers Hans Zima,1990 **Languages and Compilers for Parallel Computing** Keshav Pingali,1995-01-26 This volume presents revised versions of the 32 papers accepted for the Seventh Annual Workshop on Languages and Compilers for Parallel Computing held in Ithaca NY in August 1994 The 32 papers presented report on the leading research activities in languages and compilers for parallel computing and thus reflect the state of the art in the field The volume is organized in sections on fine grain parallelism align ment and distribution postlinear loop transformation parallel structures program analysis computer communication automatic parallelization languages for parallelism scheduling and program optimization and program evaluation **Compiling Parallel Loops for High Performance Computers** David E. Hudak,Santosh G. Abraham,2012-12-06 4 2 Code Segments 96 4 3 Determining Communication Parameters 99 4 4 Multicast Communication Overhead 103 4 5 Partitioning 103 4 6 Experimental Results 117 4 7 Conclusion 121 5 COLLECTIVE PARTITIONING AND REMAPPING FOR MULTIPLE LOOP NESTS 125 5 1 Introduction 125 5 2 Program Enclosure Trees 128 5 3 The CPR Algorithm 132 5 4 Experimental Results 141 5 5 Conclusion 146 BIBLIOGRAPHY 149 INDEX 157 LIST OF FIGURES Figure 1 1 The Butterfly Architecture 5 1 2 Example of an iterative data parallel loop 7 1 3 Contiguous tiling and assignment of an iteration space 13 2 1 Communication along a line segment 24 2 2 Access pattern for the access offset 3 2 25 2 3 Decomposing an access vector along an orthogonal basis set of vectors 26 2 4 An analysis of communication patterns 29 2 5 Decomposing a vector along two separate basis sets of vectors 31 2 6 Cache lines aligning with borders 33 2 7 Cache lines not aligned with borders 34 2 8 nh is the difference of nd and nb 42 2 9 nh is the sum of nd and nb 42 2 10 The ADAPT system 44 2 11 Code segment used in experiments 46 2 12 Execution rates for various partitions 47 2 13 Execution time of partitions on Multimax 48 2 14 Performance increase as processing power increases 49 2 15 Percentage miss ratios for various aspect ratios and line sizes **Languages and Compilers for Parallel Computing** Utpal Banerjee,David Gelernter,Alex Nicolau,David A. Padua,1992 This volume contains the proceedings of the Fourth Workshop on Languages andCompilers for Parallel Computing held in Santa Clara California in August1991 The purpose of the workshop held every year since 1988 is to bring together the leading researchers on parallel programming language designand compilation techniques for parallel computers The papers in this book cover several important topics including 1 languages and structures to represent programs internally in the compiler 2 techniques to analyzeand manipulate sequential loops in order to generate a parallel version 3 techniques to detect and extract fine grain parallelism 4 scheduling and memory management issues in automatically generated parallel programs 5 parallel programming language designs and 6 compilation of explicitly parallel programs Together the papers give a good overview of the research projects underway in 1991 in this field **Languages and Compilers for Parallel Computing** Chua-Huang Huang,1996-01-24 This book presents the refereed proceedings of the Eighth Annual Workshop on Languages and Compilers for Parallel Computing held

in Columbus Ohio in August 1995 The 38 full revised papers presented were carefully selected for inclusion in the proceedings and reflect the state of the art of research and advanced applications in parallel languages restructuring compilers and runtime systems The papers are organized in sections on fine grain parallelism interprocedural analysis program analysis Fortran 90 and HPF loop parallelization for HPF compilers tools and libraries loop level optimization automatic data distribution compiler models irregular computation object oriented and functional parallelism <u>Parallel Computing and Transputer Applications</u> Mateo Valero,1992 Mathematics of Computing Parallelism **Optimizing Supercompilers for Supercomputers** Michael Joseph Wolfe,1989 Effective use of a supercomputer requires users to have a good algorithm and to express this algorithm in an appropriate language and requires compilers to generate efficient code This book investigates several problems facing compiler design for supercomputers including building efficient and comprehensive data dependence graphs recurrence relations the management of compiler temporary variables and WHILE loops The book first proposes an efficient means of representing the flow of data in a program by labeling the arcs in a data dependence graph with direction vectors to show how the flow of data corresponds to the loop structure of the program These data dependence direction vectors are then used in several high level compiler loop optimizations loop vectorization loop concurrentization loop fusion and loop interchanging The book shows how to perform these transformations and how to use them to optimize programs for a wide range of supercomputers The problems of recurrence relations studied include arithmetic recurrences with IF statements and recurrences involving both data and control dependence relations in a cycle The wavefront method of solving recurrences is also treated The book discusses ways to make the problem of managing temporary arrays more tractable It concludes by offering several methods for executing WHILE loops and describes a general structure of an optimizing compiler for supercomputers developed from the author s experience with a test bed compiler Michael Wolfe is Associate Professor in the Computer Science and Engineering Department at the Oregon Graduate Center Optimizing Supercompilers for Supercomputers is included in the series ResearchMonographs in Parallel Computing Copublished with Pitman Publishing *Parallel Computing Technologies* Victor Malyshkin,1997-08-06 This book constitutes the refereed proceedings of the Fourth International Conference on Parallel Computing Technologies PaCT 97 held in Yaroslavl Russia in September 1997 The volume presents a total of 54 contributions 21 full papers 20 short papers 10 posters and three tutorials All papers were selected for inclusion in the proceedings from numerous submissions on the basis of three independent reviews The volume covers all current topics in parallel processing it is divided into sections on theory software hardware and architecture applications posters and tutorials *Low-Power Processors and Systems on Chips* Christian Piguet,2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet s recently published Low Power Electronics Design this volume addresses the design of low power microprocessors in deep submicron technologies It provides a focused reference for

specialists involved in systems on chips from low power microprocessors to DSP cores reconfigurable processors memories ad hoc networks and embedded software Low Power Processors and Systems on Chips is organized into three broad sections for convenient access The first section examines the design of digital signal processors for embedded applications and techniques for reducing dynamic and static power at the electrical and system levels The second part describes several aspects of low power systems on chips including hardware and embedded software aspects efficient data storage networks on chips and applications such as routing strategies in wireless RF sensing and actuating devices The final section discusses embedded software issues including details on compilers retargetable compilers and coverification tools Providing detailed examinations contributed by leading experts Low Power Processors and Systems on Chips supplies authoritative information on how to maintain high performance while lowering power consumption in modern processors and SoCs It is a must read for anyone designing modern computers or embedded systems *Exploitation of Fine-Grain Parallelism* Günter Böckle,1995-07-18 Many parallel computer architectures are especially suited for particular classes of applications However there are only a few parallel architectures equally well suited for standard programs Much effort is invested into research in compiler techniques to make programming parallel machines easier This book presents methods for automatic parallelization so that programs need not to be tailored for specific architectures here the focus is on fine grain parallelism offered by most new microprocessor architectures The book addresses compiler writers computer architects and students by demonstrating the manifold complex relationships between architecture and compiler technology **Intelligent Agents** Michael J. Wooldridge,1995-01-26 This volume coherently present 24 thoroughly revised full papers accepted for the ECAI 94 Workshop on Agent Theories Architectures and Languages There is currently considerable interest from both the AI and the mainstream CS communities in conceptualizing and building complex computer systems as collections of intelligent agents This book is devoted to theoretical and practical aspects of architectural and language related design and implementation issues of software agents Particularly interesting is the comprehensive survey by the volume editors which outlines the key issues and indicates via a comprehensive bibliography topics for further reading In addition a glossary of key terms in this emerging field and a comprehensive subject index is included **Languages, Compilers and Run-time Environments for Distributed Memory Machines** J. Saltz,P. Mehrotra,2014-06-28 Papers presented within this volume cover a wide range of topics related to programming distributed memory machines Distributed memory architectures although having the potential to supply the very high levels of performance required to support future computing needs present awkward programming problems The major issue is to design methods which enable compilers to generate efficient distributed memory programs from relatively machine independent program specifications This book is the compilation of papers describing a wide range of research efforts aimed at easing the task of programming distributed memory machines *High Performance Algorithms for Structured Matrix Problems* Peter Arbenz,1998 Comprises 10 contributions that summarize the

state of the art in the areas of high performance solutions of structured linear systems and structured eigenvalue and singular value problems Topics covered range from parallel solvers for sparse or banded linear systems to parallel computation of eigenvalues and singular values of tridiagonal and bidiagonal matrices Specific paper topics include the stable parallel solution of general narrow banded linear systems efficient algorithms for reducing banded matrices to bidiagonal and tridiagonal form a numerical comparison of look ahead Levinson and Schur algorithms for non Hermitian Toeplitz systems and parallel CG methods automatically optimized for PC and workstation clusters Annotation copyrighted by Book News Inc Portland OR   Supercomputing in Engineering Analysis Hojjat Adeli,2020-08-13 The first volume in this new series has a companion in volume 2 unseen Parallel processing in computational mechanics The first six contributions present general aspects of supercomputing from both hardware and software engineering points of view Subsequent chapters discuss homotopy algorithms   **Computer Vision Systems** Henrik I. Christensen,2003-06-29 Computer Vision has now reached a level of maturity that allows us not only to perform research on individual methods but also to build fully integrated computer vision systems of a signi cant complexity This opens up a number of new problems related to architectures systems integration validation of stems using benchmarking techniques and so on So far the majority of vision conferences have focused on component technologies which has motivated the organization of the First International Conference on Computer Vision Systems ICVS It is our hope that the conference will allow us not only to see a number of interesting new vision techniques and systems but hopefully also to de ne the research issues that need to be addressed to pave the way for more wide scale use of computer vision in a diverse set of real world applications ICVS is organized as a single track conference consisting of high quality p viously unpublished contributed papers on new and original research on c puter vision systems All contributions will be presented orally A total of 65 papers were submitted for consideration by the conference All papers were viewed by three reviewers from the program committee Thirty two of the papers were selected for presentation ICVS 99 is being held at the Alfredo Kraus Auditorium and Convention Centre in Las Palmas on the lovely Canary Islands Spain The setting is spri like which seems only appropriate as the basis for a new conference   *Languages and Compilers for Parallel Computing* Barbara Chapman,José Moreira,2022-02-15 This book constitutes the thoroughly refereed post conference proceedings of the 33rd International Workshop on Languages and Compilers for Parallel Computing LCPC 2020 held in Stony Brook NY USA in October 2020 Due to COVID 19 pandemic the conference was held virtually The 15 revised full papers were carefully reviewed and selected from 19 submissions The contributions were organized in topical sections named as follows Code and Data Transformations OpenMP and Fortran Domain Specific Compilation Machine Language and Quantum Computing Performance Analysis Code Generation   *A Programmer's Companion to Algorithm Analysis* Ernst L. Leiss,2006-09-26 Until now no other book examined the gap between the theory of algorithms and the production of software programs Focusing on practical issues A Programmer s Companion to Algorithm

Analysis carefully details the transition from the design and analysis of an algorithm to the resulting software program Consisting of two main complementary *Languages and Compilers for Parallel Computing* Vikram Adve,María Jesús Garzarán,2008-08-04 This book constitutes the thoroughly refereed post conference proceedings of the 20th International Workshop on Languages and Compilers for Parallel Computing LCPC 2007 held in Urbana IL USA in October 2007 The 23 revised full papers presented were carefully reviewed and selected from 49 submissions The papers are organized in topical sections on reliability languages parallel compiler technology libraries run time systems and performance analysis and general compiler techniques **Speculative Execution in High Performance Computer Architectures** David Kaeli,Pen-Chung Yew,2005-05-26 Until now there were few textbooks that focused on the dynamic subject of speculative execution a topic that is crucial to the development of high performance computer architectures Speculative Execution in High Performance Computer Architectures describes many recent advances in speculative execution techniques It covers cutting edge research **Computer and Information Sciences - ISCIS 2005** Pinar Yolum,2005-10-17 This book constitutes the refereed proceedings of the 20th International Symposium on Computer and Information Sciences ISCIS 2005 held in Istanbul Turkey in October 2005 The 92 revised full papers presented together with 4 invited talks were carefully reviewed and selected from 491 submissions The papers are organized in topical sections on computer networks sensor and satellite networks security and cryptography performance evaluation e commerce and Web services multiagent systems machine learning information retrieval and natural language processing image and speech processing algorithms and database systems as well as theory of computing

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web logit of the cumulative probabilities lump together and identify various portions of an otherwise continuous variable let t be the underlying continuous variable and suppose that $y = r$ if $r - 1 < t$ for some $1 \leq 0 \leq 1 \leq m$ let $x = 1$ consider the regression model $t = k' x_i \theta_0 + \theta_i x + e$ where $\theta_0 \ldots \theta_1 \ldots \theta_m$ are regression parameters and e is the

how to perform logistic regression in sas statology - Jul 15 2023

web jan 5 2022   logistic regression uses a method known as maximum likelihood estimation to find an equation of the following form log p x 1 p x β0 β1x1 β2x2 βpxp where xj the jth predictor variable βj the coefficient estimate for

different ways of performing logistic regression in sas - Jan 09 2023

web logistic regression is a statistical technique that estimates the natural base logarithm of the probability of one discrete event e g passing occurring as opposed to another event failing or more other events the log odds of the event broadly referred to as the logit here are the predicted values

logistic regression in sas enterprise guide sas communities - Feb 27 2022

web jul 3 2019   hello i ve been using an existing sas eg job to process some survey information and am having a problem with a part of the job that does logistic regression i have used the in built logistic regression wizard menu in sas eg and put the results of question 1 in as the primary dependent i t

chapter 7 50 most asked logistic regression interview questions - Mar 31 2022

web apr 4 2023   802 why is logistic regression called regression if it does the job of classification 803 what is logistic regression 804 what is the similarity between linear regression and logistic regression 805 explain the mechanism of logistic regression 806 what are the applications of logistic regression 807

different outputs in sas and r for factorial logistic regression - Jan 29 2022

web oct 22 2018   i m trying to do these factorial logistic regressions both in sas and r but i obtain different results in dry rt chi ur why my data id dry rt chi ur 1 1 0 1 2 0 0 0 3 0 0

sample questions - Oct 06 2022

web question 8 you perform a logistic regression on a multinomial response variable in sas visual statistics that has 3 levels small medium large large is specified as the event which statement is true a the other levels are grouped into one non event b an ordinal logistic regression is performed c a multinomial logistic regression is

sas how to make prediction to new data using a trained logistic - Apr 12 2023

web jan 7 2023   let s look at a really simple example using sashelp class data train prod set sashelp class if n le 15 then output train else output prod run proc logistic data train outmodel sasuser logmodel model sex age height weight run we ve saved our model into sasuser logmodel

logit regression sas data analysis examples oarc stats - Aug 16 2023

web logistic regression also called a logit model is used to model dichotomous outcome variables in the logit model the log odds of the outcome is modeled as a linear combination of the predictor variables please note the purpose of this page is to show how to use various data analysis commands

logistic regression sas support - Nov 07 2022

web figure 11 14 logistic regression model dialog model tab figure 11 14 displays the model dialog with the terms age ecg sex and their interactions selected as effects in the model note that you can build specific models with the add cross and factorial buttons or you can select a model by clicking on the standard models button and making a

predictive modeling with logistic regression using sas - Feb 10 2023

web this course covers predictive modeling using sas stat software with emphasis on the logistic procedure this course also discusses selecting variables and interactions recoding categorical variables based on the smooth weight of evidence assessing models treating missing values and using efficiency techniques for massive data sets

questions about the logistic regression sas support communities - Aug 04 2022

web questions about the logistic regression sas support communities sas software for learning community sas tips from the community