

# Assembler Macros

- Tired of writing `segment`, and `ends`? The `.model small` allows defaults and abbreviations
- Macros make program source more readable, easier to maintain
- Macro can be defined anywhere in assembler source
- Introduced by user defined name and `macro` keyword
- Terminated by `endm` keyword
- Macros may have 0 or more parameters, to be used and expanded in place inside macro body
- Note: When you dis-assemble (C++ compiler option) you do not see any macro; all is expanded

# Small Assembler A Macro Assembler Written In Small C

**Lingsheng Yao**



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In a fast-paced world fueled by information and interconnectivity, the spellbinding force of linguistics has acquired newfound prominence. Its capacity to evoke emotions, stimulate contemplation, and stimulate metamorphosis is really astonishing. Within the pages of "**Small Assembler A Macro Assembler Written In Small C**," an enthralling opus penned by a very acclaimed wordsmith, readers set about an immersive expedition to unravel the intricate significance of language and its indelible imprint on our lives. Throughout this assessment, we shall delve in to the book is central motifs, appraise its distinctive narrative style, and gauge its overarching influence on the minds of its readers.

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