Solid State Devices, 1977

Nine invoted papers presented at the Seventh European Solid State Device Research Confreence (ESSE/ERC) held at the University of Sussex, Beighton, 12-15 September 1977

Edited by E A Ash

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E. A. Ash

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wet processes of lithography Lithography as adopted by the semiconductor industry is the process of drawing or printing the pattern of an integrated circuit in a resist material The pattern is formed and overlayed to a previous circuit layer as many as 30 times in the manufacture of logic and memory devices With the resist pattern acting as a mask a permanent device structure is formed by subtractive removal etching or by additive deposition of metals or insulators Each process step in lithography uses inorganic or organic materials to physically transform semiconductors of silicon insulators of oxides nitrides and organic polymers and metals into useful electronic devices All forms of electromagnetic radiation are used in the processing Lithography is a mUltidisciplinary science of materials processes and equipment interacting to produce three dimensional structures Many aspects of chemistry electrical engineering materials science and physics are involved The purpose of this book is to bring together the work of many scientists and engineers over the last 10 years and focus upon the basic resist materials the lithographic processes and the fundamental principles behind each lithographic process

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