Designer's Handbook

Solid-State Power Circuits

A comprehensive text on basic theory, technology, and circuit applications of power transistors, thyristors, silicon rectifiers, and power hybrid circuits.

Solid State Power Circuits Designer Handbook

RCA Corporation. Solid State Division

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Design Marc Thompson, 2013-11-12 Intuitive Analog Circuit Design outlines ways of thinking about analog circuits and systems that let you develop a feel for what a good working analog circuit design should be This book reflects author Marc Thompson s 30 years of experience designing analog and power electronics circuits and teaching graduate level analog circuit design and is the ideal reference for anyone who needs a straightforward introduction to the subject In this book Dr Thompson describes intuitive and back of the envelope techniques for designing and analyzing analog circuits including transistor amplifiers CMOS IFET and bipolar transistor switching noise in analog circuits thermal circuit design magnetic circuit design and control systems. The application of some simple rules of thumb and design techniques is the first step in developing an intuitive understanding of the behavior of complex electrical systems Introducing analog circuit design with a minimum of mathematics this book uses numerous real world examples to help you make the transition to analog design The second edition is an ideal introductory text for anyone new to the area of analog circuit design LTSPICE files and PowerPoint files available online to assist readers and instructors in simulating circuits found in the text Design examples are used throughout the text along with end of chapter examples Covers real world parasitic elements in circuit design and their Modeling in Analog Design Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2012-12-06 Modeling in Analog effects Design highlights some of the most pressing issues in the use of modeling techniques for design of analogue circuits Using models for circuit design gives designers the power to express directly the behaviour of parts of a circuit in addition to using other pre defined components There are numerous advantages to this new category of analog behavioral language In the short term by favouring the top down design and raising the level of description abstraction this approach provides greater freedom of implementation and a higher degree of technology independence In the longer term analog synthesis and formal optimisation are targeted Modeling in Analog Design introduces the reader to two main language standards VHDL A and MHDL It goes on to provide in depth examples of the use of these languages to model analog devices The final part is devoted to the very important topic of modeling the thermal and electrothermal aspects of devices This book is essential reading for analog designers using behavioral languages and analog CAD tool development environments who have to provide the tools used by the designers 5G and E-Band Communication Circuits in Deep-Scaled CMOS Marco Vigilante, Patrick Reynaert, 2018-02-07 This book discusses design techniques layout details and measurements of several key analog building blocks that currently limit the performance of 5G and E Band transceivers implemented in deep scaled CMOS The authors present recent developments in low noise quadrature VCOs and tunable inductor less frequency dividers Moreover the design of low loss broadband transformer based filters that realize inter stage matching power division combining and impedance transformation is discussed in great detail The design and measurements of a low noise amplifier a downconverter and a highly linear power amplifier that leverage the proposed techniques are shown All the prototypes were realized in advanced nanometer scaled CMOS technologies without RF thick to metal option **Reference Data for**

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with an established track record in these branches need to adapt their skills whereas companies that want to enter the miniaturization market need to establish an entirely new set of capabil ities. The same can be said of engineers with classical training who will also need to be educated toward their future professional activity in the MEMS field. Here are some questions that a company or technologist may ask I have an existing product with miniaturization market potential Which technology should I adopt. What are the manufacturing options available for miniaturization What are the qualitative differences. How do we maintain market lead for products based on MEMS. Is there CAD support Can we outsource manufacturing. Which skills in our current capability need only adaptation. What skills need to be added Professors Jan Korvink and Oliver Paul have set out to answer these questions in a form that addresses the needs of companies commercial practitioners and technologists.

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