


**WHAT IS SOLID-STATE
TECHNOLOGY, AND HOW
DID IT REVOLUTIONIZE
ELECTRONIC DEVICES?
EXPLAINED!**

Solid State Devices 1983

**Conference on Solid State Devices and
Materials**



Solid State Devices 1983:

Solid State Devices ,1984 Solid State Devices 1983 E. H. Rhoderick,1984 Solid State Devices ,1974 Contains invited papers presented at the European Solid State Device Research Conference **Solid State Chemical Sensors** Jiri Janata,2012-12-02 Solid State Chemical Sensors reviews the basic chemical and physical principles involved in the construction and operation of solid state sensors A major portion of the book is devoted to explanation of the basic mechanism of operation and the many actual and potential applications of field effect transistors for gas and solution sensing This text is comprised of four chapters the first of which describes the basics of device fabrication Emphasis is placed on the physical description of semiconductor devices with catalytic metal gates along with their drawbacks and their promise The behavior of hydrogen in the Pd SiO₂ system is also considered and some applications of hydrogen sensitive transistors such as smoke detection and biochemical reaction monitoring are described The second chapter focuses on chemically sensitive field effect transistors and their thermodynamics while the third chapter explains the general fabrication procedure for solid state chemical sensors The final chapter introduces the reader to piezoelectric and pyroelectric chemical sensors paying particular attention to the sensor nature of piezoelectricity the piezoelectric gravimetric sensor and pyroelectric gas analysis This book is intended to assist electrical engineers in understanding the chemistry involved in the construction and operation of solid state sensors and to educate chemists in solid state science Compound and Josephson High-Speed Devices Takahiko Misugi,Akihiro Shibatomi,2013-06-29 In recent years III V devices integrated circuits and superconducting integrated circuits have emerged as leading contenders for high frequency and ultrahigh speed applications GaAs MESFETs have been applied in microwave systems as low noise and high power amplifiers since the early 1970s replacing silicon devices The heterojunction high electron mobility transistor HEMT invented in 1980 has become a key component for satellite broadcasting receiver systems serving as the ultra low noise device at 12 GHz Furthermore the heterojunction bipolar transistor HBT has been considered as having the highest switching speed and cutoff frequency in the semiconductor device field Initially most of these devices were used for analog high frequency applications but there is also a strong need to develop high speed III V digital devices for computer telecom munication and instrumentation systems to replace silicon high speed devices because of the switching speed and power dissipation limitations of silicon The potential high speed and low power dissipation of digital integrated circuits using GaAs MESFET HEMT HBT and superconducting Josephson junction devices has evoked tremendous competition in the race to develop such technology A technology review shows that Japanese research institutes and companies have taken the lead in the development of these devices and some integrated circuits have already been applied to supercomputers in Japan The activities of Japanese research institutes and companies in the III V and superconducting device fields have been superior for three reasons First bulk crystal growth epitaxial growth process and design technology were developed at the same time **Extended abstracts of the 15th Conference on Solid State**

Devices and Materials Conference on Solid State Devices and Materials (15, 1983, Tōkyō), 1983 **Custom-Specific Integrated Circuits** Hurst, 1985-03-27 In the semiconductor industry cutting basic design time of microelectronics is by far the most cost effective measure for keeping production budgets in line Custom Specific Integrated Circuits thoroughly considers the various methods available to reduce the design time of a microelectronic circuit to fit a specialized requirement This important work explores the principles of both bipolar and MOS technologies and provides in depth coverage of the many avenues which enable system designers to incorporate specific needs into an integrated circuit form Comprehensive and up to date this reference compares and contrasts all the techniques of custom and semicustom design and fabrication including programmable arrays masterslice arrays cell libraries and full custom examines the principles of placement and routing of regular structures presents convenient chapter summaries for quick review of essential material and offers physics fundamentals for basic understanding while concentrating on practical system design Ideal for both the practicing engineer and graduate level engineering student this outstanding book gives electrical electronic design computer mechanical and control engineers as well as electrical electronic and computer science engineering students the contemporary hands on coverage needed to master Custom Specific Integrated Circuits Book jacket **Microelectronic Materials and Processes**

R.A. Levy, 2012-12-06 The primary thrust of very large scale integration VLSI is the miniaturization of devices to increase packing density achieve higher speed and consume lower power The fabrication of integrated circuits containing in excess of four million components per chip with design rules in the submicron range has now been made possible by the introduction of innovative circuit designs and the development of new microelectronic materials and processes This book addresses the latter challenge by assessing the current status of the science and technology associated with the production of VLSI silicon circuits It represents the cumulative effort of experts from academia and industry who have come together to blend their expertise into a tutorial overview and cohesive update of this rapidly expanding field A balance of fundamental and applied contributions cover the basics of microelectronics materials and process engineering Subjects in materials science include silicon silicides resists dielectrics and interconnect metallization Subjects in process engineering include crystal growth epitaxy oxidation thin film deposition fine line lithography dry etching ion implantation and diffusion Other related topics such as process simulation defects phenomena and diagnostic techniques are also included This book is the result of a NATO sponsored Advanced Study Institute ASI held in Castelvecchio Pascoli Italy Invited speakers at this institute provided manuscripts which were edited updated and integrated with other contributions solicited from non participants to this ASI

GaAs Devices and Circuits Michael S. Shur, 2013-11-21 GaAs devices and integrated circuits have emerged as leading contenders for ultra high speed applications This book is intended to be a reference for a rapidly growing GaAs community of researchers and graduate students It was written over several years and parts of it were used for courses on GaAs devices and integrated circuits and on heterojunction GaAs devices developed and taught at the University of Minnesota Many people

helped me in writing this book I would like to express my deep gratitude to Professor Lester Eastman of Cornell University whose ideas and thoughts inspired me and helped to determine the direction of my research work for many years I also benefited from numerous discussions with his students and associates and from the very atmosphere of the pursuit of excellence which exists in his group I would like to thank my former and present co workers and colleagues Drs Levinstein and Gelmont of the A F Ioffe Institute of Physics and Technology Professor Melvin Shaw of Wayne State University Dr Kastalsky of Bell Communications Professor Gary Robinson of Colorado State University Professor Tony Valois and Dr Tim Drummond of Sandia Labs for their contributions to our joint research and for valuable discussions My special thanks to Professor Morko for his help his ideas and the example set by his pioneering work Since 1978 I have been working with engineers from Honeywell Inc Drs

Physics of Nanostructured Solid State Devices Supriyo

Bandyopadhyay,2012-02-17 Physics of Nanostructured Solid State Devices introduces readers to theories and concepts such as semi classical and quantum mechanical descriptions of electron transport methods for calculations of band structures in solids with applications in calculation of optical constants and other advanced concepts The information presented here will equip readers with the necessary tools to carry out cutting edge research in modern solid state nanodevices Scientific

and Technical Aerospace Reports ,1984-04

Silicon Molecular Beam Epitaxy

E. Kasper,2018-05-04 This subject is divided into two volumes Volume I is on homoepitaxy with the necessary systems techniques and models for growth and dopant incorporation Three chapters on homoepitaxy are followed by two chapters describing the different ways in which MBE may be applied to create insulator Si stackings which may be used for three dimensional circuits The two remaining chapters in Volume I are devoted to device applications The first three chapters of Volume II treat all aspects of heteroepitaxy with the exception of the epitaxial insulator Si structures already treated in volume I

III-V

Semiconductor Materials and Devices R.J. Malik,2012-12-02 The main emphasis of this volume is on III V semiconductor epitaxial and bulk crystal growth techniques Chapters are also included on material characterization and ion implantation In order to put these growth techniques into perspective a thorough review of the physics and technology of III V devices is presented This is the first book of its kind to discuss the theory of the various crystal growth techniques in relation to their advantages and limitations for use in III V semiconductor devices

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Semiconductor Lithography Wayne M. Moreau,2012-12-06

Semiconductor lithography is one of the key steps in the manufacturing of integrated silicon based circuits In fabricating a semiconductor device such as a transistor a series of hot processes consisting of vacuum film deposition oxidations and dopant implantation are all patterned into microscopic circuits by the wet processes of lithography Lithography as adopted

by the semiconductor industry is the process of drawing or printing the pattern of an integrated circuit in a resist material. The pattern is formed and overlayed to a previous circuit layer as many as 30 times in the manufacture of logic and memory devices. With the resist pattern acting as a mask, a permanent device structure is formed by subtractive removal etching or by additive deposition of metals or insulators. Each process step in lithography uses inorganic or organic materials to physically transform semiconductors of silicon, insulators of oxides, nitrides, and organic polymers and metals into useful electronic devices. All forms of electromagnetic radiation are used in the processing. Lithography is a multidisciplinary science of materials, processes, and equipment interacting to produce three-dimensional structures. Many aspects of chemistry, electrical engineering, materials science, and physics are involved. The purpose of this book is to bring together the work of many scientists and engineers over the last 10 years and focus upon the basic resist materials, the lithographic processes, and the fundamental principles behind each lithographic process. *The 15th Conference on Solid State Devices and Materials*

Conference on Solid State Devices and Materials, 1983 *Monthly Catalogue, United States Public Documents*, 1983

Advanced MOS Device Physics Norman Einspruch, 2012-12-02 VLSI Electronics Microstructure Science Volume 18. *Advanced MOS Device Physics* explores several device physics topics related to metal oxide semiconductor MOS technology. The emphasis is on physical description, modeling, and technological implications rather than on the formal aspects of device theory. Special attention is paid to the reliability physics of small geometry MOSFETs. Comprised of eight chapters, this volume begins with a general picture of MOS technology development from the device and processing points of view. The critical issue of hot carrier effects is discussed along with the device engineering aspects of this problem, the emerging low temperature MOS technology, and the problem of latchup in scaled MOS circuits. Several device models that are suitable for use in circuit simulators are also described. The last chapter examines novel electron transport effects observed in ultra small MOS structures. This book should prove useful to semiconductor engineers involved in different aspects of MOS technology development as well as for researchers in this field and students of the corresponding disciplines. *Semiconductors and Semimetals*, 1990-05-23 *Semiconductors and Semimetals* 〇〇〇〇〇〇〇〇〇〇〇〇〇〇〇〇〇〇〇〇〇〇 〇〇〇〇〇〇〇 (Japan), 1990

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