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would lead to its association with multimedia technologies namely for the production of courseware Parallel to the improvement of the didactic potential and quality which results from this association it nevertheless brought along a natural array of difficulties some old some new in the conception and use of hypermedia products Today there is agreement that one of the most promising technological advances for education is represented by the use of text sound and images based on nonlinear techniques of information handling and searching of hypermedia architectures The problem of hypermedia is fundamentally one of communication this leads to an attempt at defining a language for hypermedia

Embedded Processor Design Challenges Ed F. Deprettere, Stamatis Vassiliadis, 2003-07-31 This textbook is intended to give an introduction to and an overview of state of the art techniques in the design of complex embedded systems The book title is SAMOS for two major reasons First it tries to focus on the actual distinct yet important problems of System Level design of embedded systems including mapping techniques and synthesis Architectural design Modeling issues such as specification languages formal models and finally Simulation The second reason is that the volume includes a number of papers presented at a workshop with the same name on the Island of Samos Greece in July 2001 In order to receive international attention a number of reputed researchers were invited to this workshop to present their current work Participation was by invitation only For the volume presented here a number of additional papers were selected based on a call for papers All contributions were refereed This volume presents a selection of 18 of the refereed papers including 2 invited papers The textbook is organized according to four topics The first is A System Level Design and Simulation In this section we present a collection of papers that give an overview of the challenging goal to design and explore alternatives of embedded system implementations at the system level One paper gives an overview of models and tools used in system level design The other papers present new models to describe applications provide models for refinement and design space exploration and for tradeoff analysis between cost and flexibility of an implementation

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