DONALD D. WEINER JOHN E SPENA

SINUSOIDAL ANALYSIS AND MODELING OF WEAKLY NONLINEAR CIRCUITS

With Application to Nonlinear Interference Effects.

SJ Ball

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Interpolatory Methods for Model Reduction A. C. Antoulas, C. A. Beattie, S. Gü?ercin, 2020-01-13 Dynamical systems are a principal tool in the modeling prediction and control of a wide range of complex phenomena As the need for improved accuracy leads to larger and more complex dynamical systems direct simulation often becomes the only available strategy for accurate prediction or control inevitably creating a considerable burden on computational resources. This is the main context where one considers model reduction seeking to replace large systems of coupled differential and algebraic equations that constitute high fidelity system models with substantially fewer equations that are crafted to control the loss of fidelity that order reduction may induce in the system response Interpolatory methods are among the most widely used model reduction techniques and Interpolatory Methods for Model Reduction is the first comprehensive analysis of this approach available in a single extensive resource. It introduces state of the art methods reflecting significant developments over the past two decades covering both classical projection frameworks for model reduction and data driven nonintrusive frameworks. This textbook is appropriate for a wide audience of engineers and other scientists working in the general areas of large scale dynamical systems and data driven modeling of dynamics.

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Substrate Noise Coupling in Mixed-Signal ASICs Stéphane Donnay, Georges Gielen, 2006-05-31 This book is the first in a series of three dedicated to advanced topics in Mixed Signal IC design methodologies It is one of the results achieved by the Mixed Signal Design Cluster an initiative launched in 1998 as part of the TARDIS project funded by the European Commission within the ESPRIT IV Framework This initiative aims to promote the development of new design and test methodologies for Mixed Signal ICs and to accelerate their adoption by industrial users As Microelectronics evolves Mixed Signal techniques are gaining a significant importance due to the wide spread of applications where an analog front end is needed to drive a complex digital processing subsystem In this sense Analog and Mixed Signal circuits are recognized as a bottleneck for the market acceptance of Systems On Chip because of the inherent difficulties involved in the design and test of these circuits Specially problems arising from the use of a common substrate for analog and digital components are a main limiting factor The Mixed Signal Cluster has been formed by a group of 11 Research and Development projects plus a specific action to promote the dissemination of design methodologies techniques and supporting tools developed within the Cluster projects The whole action ending in July 2002 has been assigned an overall budget of more than 8 million EURO

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