RF and microwave solid-state power amplifiers design requires specialised engineering

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In the world of RF and microwave engineering, the design and development of solid-state amplifilers is a speciality. It has always required many years of specialised engineering experience and a suitable collection of test and measurement equipment. While these will always be necessary, to be successful in the marketplace today, it is also essential to use a combination of specialised and general CAD sools.

The RF and microwave design software we use has removed much of the risk and guess work from creating a new amplifier design. For us, the software design tools have transformed the process of designing an amplifier in terms of speed, substantially shortening the product design cycle and massively improving the probability that the new device will perform an specified at the first attempt.

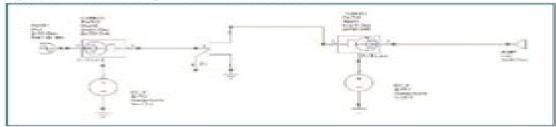
In the last 10 years or so wide bandgap transistors (SIC MESFETs and GaN HEMTs) have appeared on the market for high power RF/microwave transistors. They offer higher power density and higher voltage operation, which in turn are associated with much lower parasitic capacitances and much higher load-line. dynamic resistance, and hence wider bandwidth applications. Of the two kinds the GaN HEMTs. offer higher gain performance and became dominant on the market. However, the much wider bandwidth matching networks could not be designed optimally with the traditional Smith Chart and optimisation techniques [1]. The new requirements for broadband high power and high efficiency performance require new and more sophisticated matching networks synthesis. techniques such as the real frequency technique TALETE.

The design software for the RF and microwave amplitiers

The most important part of the design relies upon extensive use of two RF/microwave software programs which are used in tandem [5]. These are the MultiMatch Amplifier Design Witzed and the general simulator/optimizer Microwave Office 171, 181.

In MultiMarch the designer uses the powerful real frequencies without technique for lossy and lossless matching network design to achieve the optimum performance from the RE/microwave transistors [4], [7]. The designer can also use the new power parameters (a properly and fully defined load-line approach) to design power amplifiers [2], [3], [4], [6]. MultiMatch is like a massive amplifier design template where the creation of the schematics

Figure 1: Extraction of design data in Microwave Office.



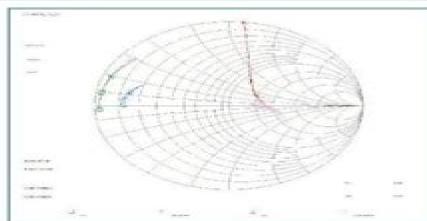


Figure 2: Fitting linear model to the S-parameters in MultiMatch.



Figure 3: MultiMatch layout.

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Solid State Electronic Amplifiers An Empirical Approach

Jose Solé,Luisa Bausa,Daniel Jaque

Solid State Electronic Amplifiers An Empirical Approach:

Handbook of Optoelectronic Device Modeling and Simulation Joachim Piprek, 2017-10-10 Optoelectronic devices are now ubiquitous in our daily lives from light emitting diodes LEDs in many household appliances to solar cells for energy This handbook shows how we can probe the underlying and highly complex physical processes using modern mathematical models and numerical simulation for optoelectronic device design analysis and performance optimization It reflects the wide availability of powerful computers and advanced commercial software which have opened the door for non specialists to perform sophisticated modeling and simulation tasks The chapters comprise the know how of more than a hundred experts from all over the world The handbook is an ideal starting point for beginners but also gives experienced researchers the opportunity to renew and broaden their knowledge in this expanding field **U.S. Government Research Reports**, 1964

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