SYNTHESIS OF FINITE STATE MACHINES:
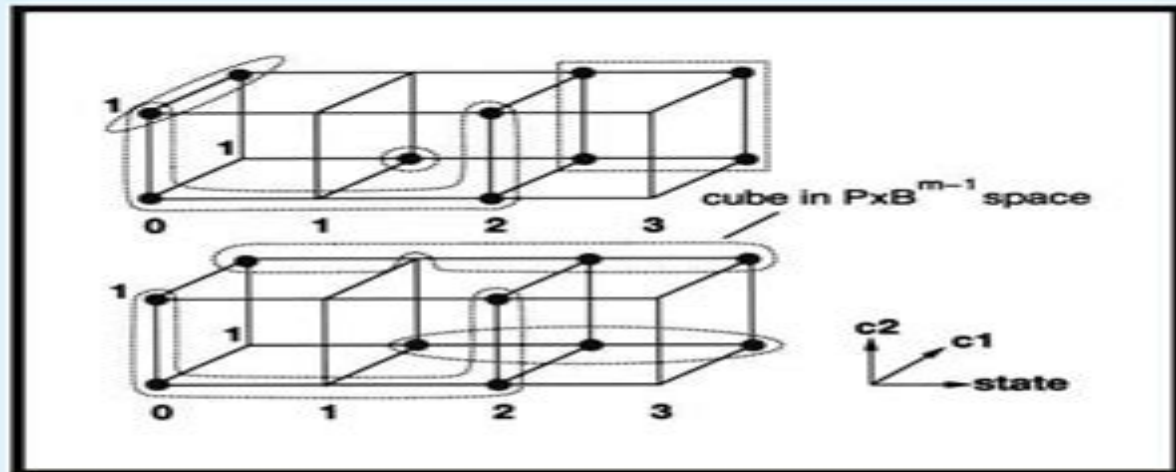## Logic Optimization

Tiziano Villa
Timothy Kam
Robert K. Brayton
Alberto Sangiovanni-Vincentelli

cube in PxB^{m-1} space

Synthesis Of Finite State Machines Logic Optimization

Richard C. Dorf

Synthesis Of Finite State Machines Logic Optimization:

Synthesis of Finite State Machines Tiziano Villa,Timothy Kam,Robert K. Brayton,Alberto L. Sangiovanni-Vincentelli,2012-12-06 Synthesis of Finite State Machines Logic Optimization is the second in a set of two monographs devoted to the synthesis of Finite State Machines FSMs The first volume Synthesis of Finite State Machines Functional Optimization addresses functional optimization whereas this one addresses logic optimization The result of functional optimization is a symbolic description of an FSM which represents a sequential function chosen from a collection of permissible candidates Logic optimization is the body of techniques for converting a symbolic description of an FSM into a hardware implementation The mapping of a given symbolic representation into a two valued logic implementation is called state encoding or state assignment and it impacts heavily area speed testability and power consumption of the realized circuit The first part of the book introduces the relevant background presents results previously scattered in the literature on the computational complexity of encoding problems and surveys in depth old and new approaches to encoding in logic synthesis The second part of the book presents two main results about symbolic minimization a new procedure to find minimal two level symbolic covers under face dominance and disjunctive constraints and a unified frame to check encodability of encoding constraints and find codes of minimum length that satisfy them The third part of the book introduces generalized prime implicants GPIs which are the counterpart in symbolic minimization of two level logic to prime implicants in two valued two level minimization GPIs enable the design of an exact procedure for two level symbolic minimization based on a covering step which is complicated by the need to guarantee encodability of the final cover A new efficient algorithm to verify encodability of a selected cover is presented If a cover is not encodable it is shown how to augment itminimally until an encodable superset of GPIs is determined To handle encodability the authors have extended the frame to satisfy encoding constraints presented in the second part The covering problems generated in the minimization of GPIs tend to be very large Recently large covering problems have been attacked successfully by representing the covering table with binary decision diagrams BDD In the fourth part of the book the authors introduce such techniques and extend them to the case of the implicit minimization of GPIs where the encodability and augmentation steps are also performed implicitly Synthesis of Finite State Machines Logic Optimization will be of interest to researchers and professional engineers who work in the area of computer aided design of integrated circuits　　**Synthesis of Finite State Machines** Timothy Kam,Tiziano Villa,Robert K. Brayton,Alberto Sangiovanni-Vincentelli,1996-12-31 Synthesis of Finite State Machines Functional Optimization is one of two monographs devoted to the synthesis of Finite State Machines FSMs This volume addresses functional optimization whereas the second addresses logic optimization By functional optimization here we mean the body of techniques that compute all permissible sequential functions for a given topology of interconnected FSMs and select a best sequential function out of the permissible ones The result is a symbolic description of the FSM representing the chosen sequential function By logic

optimization here we mean the steps that convert a symbolic description of an FSM into a hardware implementation with the goal to optimize objectives like area testability performance and so on Synthesis of Finite State Machines Functional Optimization is divided into three parts The first part presents some preliminary definitions theories and techniques related to the exploration of behaviors of FSMs The second part presents an implicit algorithm for exact state minimization of incompletely specified finite state machines ISFSMs and an exhaustive presentation of explicit and implicit algorithms for the binate covering problem The third part addresses the computation of permissible behaviors at a node of a network of FSMs and the related minimization problems of non deterministic finite state machines NDFSMs Key themes running through the book are the exploration of behaviors contained in a non deterministic FSM NDFSM and the representation of combinatorial problems arising in FSM synthesis by means of Binary Decision Diagrams BDDs Synthesis of Finite State Machines Functional Optimization will be of interest to researchers and designers in logic synthesis CAD and design automation

Synthesis of Finite State Machines Tiziano Villa,Timothy Kam,Robert K Brayton,1997-04-01 **Synthesis of Finite State Machines** Timothy Kam,Tiziano Villa,Robert K. Brayton,2014-01-15 *Logic Synthesis for FPGA-Based Finite State Machines* Alexander Barkalov,Larysa Titarenko,Malgorzata Kolopienczyk,Kamil Mielcarek,Grzegorz Bazydlo,2015-10-15 This book discusses control units represented by the model of a finite state machine FSM It contains various original methods and takes into account the peculiarities of field programmable gate arrays FPGA chips and a FSM model It shows that one of the peculiarities of FPGA chips is the existence of embedded memory blocks EMB The book is devoted to the solution of problems of logic synthesis and reduction of hardware amount in control units The book will be interesting and useful for researchers and PhD students in the area of Electrical Engineering and Computer Science as well as for designers of modern digital systems **Logic Synthesis for VLSI-Based Combined Finite State Machines** Alexander Barkalov,Larysa Titarenko,Kamil Mielcarek,Małgorzata Mazurkiewicz,Elżbieta Kawecka,2022-11-24 The book is devoted to design and optimization of control units represented by combined finite state machines CFSMs The CFSMs combine features of both Mealy and Moore FSMs Having states of Moore FSM they produce output signals of both Mealy and Moore types To optimize the circuits of CFSMs we propose to use optimization methods targeting both Mealy and Moore FSMs The book contains some original synthesis and optimization methods targeting hardware reduction in VLSI based CFSM circuits These methods take into account the peculiarities of both a CFSM model and a VLSI chip in use The optimization is achieved due to combining classical optimization methods with new methods proposed in this book These new methods are a mixed encoding of collections of microoperations and a twofold state assignment in CFSMs All proposed methods target reducing the numbers of arguments in systems of Boolean functions representing CFSM circuits Also we propose to use classes of pseudoequivalent states of Moore FSMs to reduce the number of product terms in these systems The book includes a lot of examples which contributes to a better understanding of the features of the synthesis methods under consideration This is

the first book entirely devoted to the problems associated with synthesis and optimization of VLSI based CFSMs We hope that the book will be interesting and useful for students and PhD students in the area of Computer Science as well as for designers of various digital systems We think that proposed CFSM models enlarge the class of models applied for implementation of control units with modern VLSI chips   *Foundations of Embedded Systems* Alexander Barkalov,Larysa Titarenko,Małgorzata Mazurkiewicz,2019-02-04 This book is devoted to embedded systems ESs which can now be found in practically all fields of human activity Embedded systems are essentially a special class of computing systems designed for monitoring and controlling objects of the physical world The book begins by discussing the distinctive features of ESs above all their cybernetic physical character and how they can be designed to deliver the required performance with a minimum amount of hardware In turn it presents a range of design methodologies Considerable attention is paid to the hardware implementation of computational algorithms It is shown that different parts of complex ESs could be implemented using models of finite state machines FSMs Also field programmable gate arrays FPGAs are very often used to implement different hardware accelerators in ESs The book pays considerable attention to design methods for FPGA based FSMs before the closing section turns to programmable logic controllers widely used in industry This book will be interesting and useful for students and postgraduates in the area of Computer Science as well as for designers of embedded systems In addition it offers a good point of departure for creating embedded systems for various spheres of human activity   **Algorithms for VLSI Design Automation** Sabih H. Gerez,1999-01-05 Modern microprocessors such as Intel s Pentium chip typically contain many millions of transistors They are known generically as Very Large Scale Integrated VLSI systems and their sheer scale and complexity has necessitated the development of CAD tools to automate their design This book focuses on the algorithms which are the building blocks of the design automation software which generates the layout of VLSI circuits Courses on this area are typically elective courses taken at senior undergrad or graduate level by students of Electrical and Electronic Engineering and sometimes in Computer Science or Computer Engineering   **Intelligent Information Processing and Web Mining** Mieczyslaw A. Klopotek,Slawomir T. Wierzchon,Krzysztof Trojanowski,2013-06-05 This volume contains articles accepted for presentation during The Intel ligent Information Processing and Web Mining Conference IIS IIPWM 04 which was held in Zakopane Poland on May 17 20 2004 This conference is a continuation of a series of 13 successful symposia and conferrences on Intel ligent Information Systems organized by the Institute of Computer Science of Polish Academy of Sciences devoted to new trends in broadly understood Artificial Intelligence The idea of organizing such meetings dates back to 1992 Our main in tention guided the first rather small audience workshop in the series was to resume the results gained in Polish scientific centers as well as contrast them with the research performed by Polish scientists working at the universities in Europe and USA and their foreign collaborators This idea proved to be attractive enough that we decided to continue such meetings As the years went by the workshops has transformed into

regular symposia devoted to such fields like Machine Learning Knowledge Discovery Natural Language Processing Knowledge Based Systems and Reasoning and Soft Computing i e Fuzzy and Rough Sets Bayesian Networks Neural Networks and Evo lutionary Algorithms At present about 50 papers prepared by researches from Poland and other countries are usually presented This year conference devotes much more attention to the newest devel opments in the area of Artificial Intelligence related to broadly understood Web mining *Compiler Construction ,* RTL Hardware Design Using VHDL Pong P. Chu,2006-04-20 The skills and guidance needed to master RTL hardware design This book teaches readers how to systematically design efficient portable and scalable Register Transfer Level RTL digital circuits using the VHDL hardware description language and synthesis software Focusing on the module level design which is composed of functional units routing circuit and storage the book illustrates the relationship between the VHDL constructs and the underlying hardware components and shows how to develop codes that faithfully reflect the module level design and can be synthesized into efficient gate level implementation Several unique features distinguish the book Coding style that shows a clear relationship between VHDL constructs and hardware components Conceptual diagrams that illustrate the realization of VHDL codes Emphasis on the code reuse Practical examples that demonstrate and reinforce design concepts procedures and techniques Two chapters on realizing sequential algorithms in hardware Two chapters on scalable and parameterized designs and coding One chapter covering the synchronization and interface between multiple clock domains Although the focus of the book is RTL synthesis it also examines the synthesis task from the perspective of the overall development process Readers learn good design practices and guidelines to ensure that an RTL design can accommodate future simulation verification and testing needs and can be easily incorporated into a larger system or reused Discussion is independent of technology and can be applied to both ASIC and FPGA devices With a balanced presentation of fundamentals and practical examples this is an excellent textbook for upper level undergraduate or graduate courses in advanced digital logic Engineers who need to make effective use of today s synthesis software and FPGA devices should also refer to this book *Design and Implementation of Compiler* Ravendra Singh,Vivek Sharma,Manish Varsheny,2009 About the Book This well organized text provides the design techniques of complier in a simple and straightforward manner It describes the complete development of various phases of complier with their imitation of C language in order to have an understanding of their application Primarily designed as a text for undergraduate students of Computer Science and Information Technology and postgraduate students of MCA Key Features Chapter1 covers all formal languages with their properties More illustration on parsing to offer enhanced perspective of parser and also more examples in e **Intelligent Information Systems 2002** Mieczyslaw A. Klopotek,Slawomir Wierzchon,Maciej Michalewicz,2013-11-11 This volume contains articles accepted for presentation during The Intelligent Information Systems Symposium IIS 2002 which was held in Sopot Poland on June 3 6 2002 This is eleventh in the order symposium organized by the Institute of Computer Science of Polish Academy of Sciences and devoted to new

trends in broadly understood ArtificialIntelligence The meetings started back to 1992 With small initial audience workshops in the series grew to an important meeting of Polish and foreign scientists working at the universities in Europe Asia and the Northern America Over years the workshops transformed into regular symposia devoted to latest trends in such fields like Machine Learning Knowledge Discovery Natural Language Processing Knowledge Based Systems and Reasoning and Soft Computing i e Fuzzy and Rough Sets Bayesian Networks Neural Networks and Evolutionary Algorithms At present about 50 60 papers are accepted each year Besides for several years now the symposia are accompanied by a number of tutorials given by the outstanding scientists in their domain The main topics of this year symposium included decision trees and other classifier systems neural network and biologiccally motivated systems clustering methods handling imprecision and uncertainty deductive distributed and agent based systems We were pleased to see the continuation of the last year trend towards an increase in the number of co operative contributions and in the number and diversity of practical applications of theoretical research **Memory, Microprocessor, and ASIC** Wai-Kai Chen,2003-03-26 Timing memory power dissipation testing and testability are all crucial elements of VLSI circuit design In this volume culled from the popular VLSI Handbook experts from around the world provide in depth discussions on these and related topics Stacked gate embedded and flash memory all receive detailed treatment including their power cons Computer Information Systems and Industrial Management Khalid Saeed,Rituparna Chaki,Valentina Janev,2019-09-12 This book constitutes the proceedings of the 18th International Conference on Computer Information Systems and Industrial Management Applications CISIM 2019 held in Belgrade Serbia in September 2019 The 43 full papers presented together with 3 abstracts of keynotes were carefully reviewed and selected from 70 submissions The main topics covered by the chapters in this book are biometrics security systems multimedia classification and clustering industrial management Besides these the reader will find interesting papers on computer information systems as applied to wireless networks computer graphics and intelligent systems The papers are organized in the following topical sections biometrics and pattern recognition applications computer information systems industrial management and other applications machine learning and high performance computing modelling and optimization various aspects of computer security Introduction to VLSI Design Flow Saurabh,2023 **Low Power Design in Deep Submicron Electronics** W. Nebel,Jean Mermet,1997-06-30 Decreasing power dissipation per logic function has become a primary concern in virtually all CMOS system chips designed today as a result of the relentless progress in processing technology that has led us into the deep submicron age Evolution from 1 micron to 0 1 micron lithography in the next decade will not be possible without a change in the way we design CMOS systems But power reduction requires an overall optimisation ranging from software compilation over instruction set design down to the introduction of much more parallelism in the architecture the optimal use of memory hierarchy new clocking strategies use of asynchronous techniques new CMOS circuit techniques and management of leakage currents in new low power

technologies Moreover performance and power dissipation will come to be dominated by interconnect and thus completely new floor planning and place and route strategies are emerging The chapters in this book present a systematic coverage of deep submicron CMOS digital system design for low power from process technology all the way up to software design and embedded software systems Audience An excellent guide for the practising engineer researcher and student interested in this crucial aspect of actual CMOS design   **Springer Handbook of Automation** Shimon Y. Nof,2009-07-16 Automation is undergoing a major transformation in scope and dimension and plays an increasingly important role in the global economy and in our daily lives Engineers combine automated devices with mathematical and organizational tools to create complex systems for a rapidly expanding range of applications and human activities This handbook incorporates these new developments and presents a widespread and well structured conglomeration of new emerging application areas of automation Besides manufacturing as a primary application of automation the handbook contains new application areas such as medical systems and health transportation security and maintenance service construction and retail as well as production or logistics This Springer Handbook is not only an ideal resource for automation experts but also for people new to this expanding field such as engineers medical doctors computer scientists designers It is edited by an internationally renowned and experienced expert   Digital Design and Fabrication Vojin G. Oklobdzija,2017-12-19 In response to tremendous growth and new technologies in the semiconductor industry this volume is organized into five information rich sections Digital Design and Fabrication surveys the latest advances in computer architecture and design as well as the technologies used to manufacture and test them Featuring contributions from leading experts the book also includes a new section on memory and storage in addition to a new chapter on nonvolatile memory technologies Developing advanced concepts this sharply focused book Describes new technologies that have become driving factors for the electronic industry Includes new information on semiconductor memory circuits whose development best illustrates the phenomenal progress encountered by the fabrication and technology sector Contains a section dedicated to issues related to system power consumption Describes reliability and testability of computer systems Pinpoints trends and state of the art advances in fabrication and CMOS technologies Describes performance evaluation measures which are the bottom line from the user s point of view Discusses design techniques used to create modern computer systems including high speed computer arithmetic and high frequency design timing and clocking and PLL and DLL design   **The Electrical Engineering Handbook - Six Volume Set** Richard C. Dorf,2018-12-14 In two editions spanning more than a decade The Electrical Engineering Handbook stands as the definitive reference to the multidisciplinary field of electrical engineering Our knowledge continues to grow and so does the Handbook For the third edition it has grown into a set of six books carefully focused on specialized areas or fields of study Each one represents a concise yet definitive collection of key concepts models and equations in its respective domain thoughtfully gathered for convenient access Combined they constitute the most comprehensive authoritative resource

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