

THE SYNTHESIS APPROACH TO DIGITAL SYSTEM DESIGN

Petra Michel
Ulrich Lauther
Peter Duzy



Springer

Synthesis Approach To Digital System Design

Vojin G. Oklobdzija



Synthesis Approach To Digital System Design:

The Synthesis Approach to Digital System Design Petra Michel,Ulrich Lauther,Peter Duzy,1992-03-31 Over the past decade there has been a dramatic change in the role played by design automation for electronic systems Ten years ago integrated circuit IC designers were content to use the computer for circuit logic and limited amounts of high level simulation as well as for capturing the digitized mask layouts used for IC manufacture The tools were only aids to design the designer could always find a way to implement the chip or board manually if the tools failed or if they did not give acceptable results Today however design technology plays an indispensable role in the design of electronic systems and is critical to achieving time to market cost and performance targets In less than ten years designers have come to rely on automatic or semi automatic CAD systems for the physical design of complex ICs containing over a million transistors In the past three years practical logic synthesis systems that take into account both cost and performance have become a commercial reality and many designers have already relinquished control of the logic netlist level of design to automatic computer aids To date only in certain well defined areas especially digital signal processing and telecommunications have higher level design methods and tools found significant success However the forces of time to market and growing system complexity will demand the broad based adoption of high level automated methods and tools over the next few years The Synthesis

Approach to Digital System Design Petra Michel,Ulrich Lauther,Peter Duzy,2012-12-06 Over the past decade there has been a dramatic change in the role played by design automation for electronic systems Ten years ago integrated circuit IC designers were content to use the computer for circuit logic and limited amounts of high level simulation as well as for capturing the digitized mask layouts used for IC manufacture The tools were only aids to design the designer could always find a way to implement the chip or board manually if the tools failed or if they did not give acceptable results Today however design technology plays an indispensable role in the design of electronic systems and is critical to achieving time to market cost and performance targets In less than ten years designers have come to rely on automatic or semi automatic CAD systems for the physical design of complex ICs containing over a million transistors In the past three years practical logic synthesis systems that take into account both cost and performance have become a commercial reality and many designers have already relinquished control of the logic netlist level of design to automatic computer aids To date only in certain well defined areas especially digital signal processing and telecommunications have higher level design methods and tools found significant success However the forces of time to market and growing system complexity will demand the broad based adoption of high level automated methods and tools over the next few years *Digital Systems Design with VHDL and*

Synthesis Kou-Chuan Chang,2005 **Introduction to VLSI Design Flow** Sneh Saurabh,2023-06-15 Chip designing is a complex task that requires an in depth understanding of VLSI design flow skills to employ sophisticated design tools and keeping pace with the bleeding edge semiconductor technologies This lucid textbook is focused on fulfilling these

requirements for students as well as a refresher for professionals in the industry It helps the user develop a holistic view of the design flow through a well sequenced set of chapters on logic synthesis verification physical design and testing Illustrations and pictorial representations have been used liberally to simplify the explanation Additionally each chapter has a set of activities that can be performed using freely available tools and provide hands on experience with the design tools Review questions and problems are given at the end of each chapter to revise the concepts Recent trends and references are listed at the end of each chapter for further reading

Digital System Design using FSMs Peter D. Minns, 2021-06-23

DIGITAL SYSTEM DESIGN USING FSMS Explore this concise guide perfect for digital designers and students of electronic engineering who work in or study embedded systems Digital System Design using FSMs A Practical Learning Approach delivers a thorough update on the author's earlier work FSM Based Digital Design using Verilog HDL The new book retains the foundational content from the first book while including refreshed content to cover the design of Finite State Machines delivered in a linear programmed learning format The author describes a different form of State Machines based on Toggle Flip Flops and Data Flip Flops The book includes many figures of which 15 are Verilog HDL simulations that readers can use to test out the design methods described in the book as well as 19 Logisim simulation files with figures Additional circuits are also contained within the Wiley web folder It has tutorials and exercises including comprehensive coverage of real world examples demonstrated alongside the frame by frame presentations of the techniques used In addition to covering the necessary Boolean algebra in sufficient detail for the reader to implement the FSM based systems used in the book readers will also benefit from the inclusion of A thorough introduction to finite state machines and state diagrams for the design of electronic circuits and systems An exploration of using state diagrams to control external hardware subsystems Discussions of synthesizing hardware from a state diagram synchronous and asynchronous finite state machine designs and testing finite state machines using a test bench module A treatment of the One Hot Technique in finite state machine design An examination of Verilog HDL including its elements An analysis of Petri Nets including both sequential and parallel system design Suitable for design engineers and senior technicians seeking to enhance their skills in developing digital systems

Digital System Design using FSMs A Practical Learning Approach will also earn a place in the libraries of undergraduate and graduate electrical and electronic engineering students and researchers

Digital Design and Fabrication Vojin G. Oklobdzija, 2017-12-19 In response to tremendous growth and new technologies in the semiconductor industry this volume is organized into five information rich sections Digital Design and Fabrication surveys the latest advances in computer architecture and design as well as the technologies used to manufacture and test them Featuring contributions from leading experts the book also includes a new section on memory and storage in addition to a new chapter on nonvolatile memory technologies Developing advanced concepts this sharply focused book Describes new technologies that have become driving factors for the electronic industry Includes new information on semiconductor memory circuits whose development best

illustrates the phenomenal progress encountered by the fabrication and technology sector Contains a section dedicated to issues related to system power consumption Describes reliability and testability of computer systems Pinpoints trends and state of the art advances in fabrication and CMOS technologies Describes performance evaluation measures which are the bottom line from the user's point of view Discusses design techniques used to create modern computer systems including high speed computer arithmetic and high frequency design timing and clocking and PLL and DLL design

The Computer Engineering Handbook Vojin G. Oklobdzija, 2001-12-26 There is arguably no field in greater need of a comprehensive handbook than computer engineering The unparalleled rate of technological advancement the explosion of computer applications and the now in progress migration to a wireless world have made it difficult for engineers to keep up with all the developments in specialties outside their own

The SECD Microprocessor Brian T. Graham, 2012-12-06 This is a milestone in machine assisted microprocessor verification Gordon 20 and Hunt 32 led the way with their verifications of simple designs Cohn 12 13 followed this with the verification of parts of the VIPER microprocessor This work illustrates how much these and other pioneers achieved in developing tractable models scalable tools and a robust methodology A condensed review of previous research emphasising the behavioural model underlying this style of verification is followed by a careful and remarkably readable account of the SECD architecture its formalisation and a report on the organisation and execution of the automated correctness proof in HOL This monograph reports on Graham's MSc project demonstrating that in the right hands the tools and methodology for formal verification can and therefore should now be applied by someone with little previous expertise in formal methods to verify a non trivial microprocessor in a limited timescale This is not to belittle Graham's achievement the production of this proof working as Graham did from the previous literature goes well beyond a typical MSc project The achievement is that with this exposition to hand an engineer tackling the verification of similar microprocessor designs will have a clear view of the milestones that must be passed on the way and of the methods to be applied to achieve them

Correct Hardware Design and Verification Methods Laurence Pierre, Thomas Kropf, 2003-07-31 CHARME 99 is the tenth in a series of working conferences devoted to the development and use of leading edge formal techniques and tools for the design and verification of hardware and systems Previous conferences have been held in Darmstadt 1984 Edinburgh 1985 Grenoble 1986 Glasgow 1988 Leuven 1989 Torino 1991 Arles 1993 Frankfurt 1995 and Montreal 1997 This workshop and conference series has been organized in cooperation with IFIP WG 10.5 It is now the biannual counterpart of FMCAD which takes place every even numbered year in the USA The 1999 event took place in Bad Herrenalb a resort village located in the Black Forest close to the city of Karlsruhe The validation of functional and timing behavior is a major bottleneck in current VLSI design systems A predominantly academic area of study until a few years ago formal design and verification techniques are now migrating into industrial use The aim of CHARME 99 is to bring together researchers and users from academia and industry working in this active area of research Two invited talks illustrate major

current trends the presentation by Gerard Berry Ecole des Mines de Paris Sophia Antipolis France is concerned with the use of synchronous languages in circuit design and the talk given by Peter Jansen BMW Munich Germany demonstrates an application of formal methods in an industrial environment The program also includes 20 regular presentations and 12 short presentations poster exhibitions that have been selected from the 48 submitted papers

Digital Control Engineering M. Sami Fadali, Antonio Visioli, 2012-08-21 Digital controllers are part of nearly all modern personal industrial and transportation systems Every senior or graduate student of electrical chemical or mechanical engineering should therefore be familiar with the basic theory of digital controllers This new text covers the fundamental principles and applications of digital control engineering with emphasis on engineering design Fadali and Visioli cover analysis and design of digitally controlled systems and describe applications of digital controls in a wide range of fields With worked examples and Matlab applications in every chapter and many end of chapter assignments this text provides both theory and practice for those coming to digital control engineering for the first time whether as a student or practicing engineer Extensive Use of computational tools Matlab sections at end of each chapter show how to implement concepts from the chapter Frees the student from the drudgery of mundane calculations and allows him to consider more subtle aspects of control system analysis and design An engineering approach to digital controls emphasis throughout the book is on design of control systems Mathematics is used to help explain concepts but throughout the text discussion is tied to design and implementation For example coverage of analog controls in chapter 5 is not simply a review but is used to show how analog control systems map to digital control systems Review of Background Material contains review material to aid understanding of digital control analysis and design Examples include discussion of discrete time systems in time domain and frequency domain reviewed from linear systems course and root locus design in s domain and z domain reviewed from feedback control course Inclusion of Advanced Topics In addition to the basic topics required for a one semester senior graduate class the text includes some advanced material to make it suitable for an introductory graduate level class or for two quarters at the senior graduate level Examples of optional topics are state space methods which may receive brief coverage in a one semester course and nonlinear discrete time systems Minimal Mathematics Prerequisites The mathematics background required for understanding most of the book is based on what can be reasonably expected from the average electrical chemical or mechanical engineering senior This background includes three semesters of calculus differential equations and basic linear algebra Some texts on digital control require more

Computer Literature Bibliography United States. National Bureau of Standards, 1965

Readings in Hardware/Software Co-Design Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2002 This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s

Digital Systems Design and Prototyping Zoran Salcic, Asim Smailagic, 2007-05-08 Digital Systems Design and Prototyping Using Field Programmable Logic and Hardware Description Languages Second Edition covers the subject of digital systems

design using two important technologies Field Programmable Logic Devices FPLDs and Hardware Description Languages HDLs These two technologies are combined to aid in the design prototyping and implementation of a whole range of digital systems from very simple ones replacing traditional glue logic to very complex ones customized as the applications require Three HDLs are presented VHDL and Verilog the widely used standard languages and the proprietary Altera HDL AHDL The chapters on these languages serve as tutorials and comparisons are made that show the strengths and weaknesses of each language A large number of examples are used in the description of each language providing insight for the design and implementation of FPLDs With the addition of the Altera UP 1 prototyping board all examples can be tested and verified in a real FPLD Digital Systems Design and Prototyping Using Field Programmable Logic and Hardware Description Languages Second Edition is designed as an advanced level textbook as well as a reference for the professional engineer Fault Covering Problems in Reconfigurable VLSI Systems Ran Libeskind-Hadas,Nany Hasan,Jingsheng Jason Cong,Philip McKinley,C.L. Liu,2012-12-06 Fault Covering Problems in Reconfigurable VLSI Systems describes the authors recent research on reconfiguration problems for fault tolerance in VLSI and WSI Systems The book examines solutions to a number of reconfiguration problems Efficient algorithms are given for tractable covering problems and general techniques are given for dealing with a large number of intractable covering problems The book begins with an investigation of algorithms for the reconfiguration of large redundant memories Next a number of more general covering problems are considered and the complexity of these problems is analyzed Finally a general and uniform approach is proposed for solving a wide class of covering problems The results and techniques described here will be useful to researchers and students working in this area As such the book serves as an excellent reference and may be used as the text for an advanced course on the topic

Principles of Testing Electronic Systems Samiha Mourad,Yervant Zorian,2000-07-25 A pragmatic approach to testing electronic systems As we move ahead in the electronic age rapid changes in technology pose an ever increasing number of challenges in testing electronic products Many practicing engineers are involved in this arena but few have a chance to study the field in a systematic way learning takes place on the job By covering the fundamental disciplines in detail Principles of Testing Electronic Systems provides design engineers with the much needed knowledge base Divided into five major parts this highly useful reference relates design and tests to the development of reliable electronic products shows the main vehicles for design verification examines designs that facilitate testing and investigates how testing is applied to random logic memories FPGAs and microprocessors Finally the last part offers coverage of advanced test solutions for today s very deep submicron designs The authors take a phenomenological approach to the subject matter while providing readers with plenty of opportunities to explore the foundation in detail Special features include An explanation of where a test belongs in the design flow Detailed discussion of scan path and ordering of scan chains BIST solutions for embedded logic and memory blocks Test methodologies for FPGAs A chapter on testing system on a chip Numerous references *Advances in Computers*

,1993-09-14 Advances in Computers Digital Systems Design With Vhdl And Synthesis: An Integrated Approach
Chang,2007-01-10 This book presents an integrated approach to digital design principles processes and implementations to help the reader design increasingly complex systems within shorter design cycles It also introduces digital design concepts VHDL coding VHDL simulation synthesis commands and strategies together VHDL and Digital Circuit Primitives VHDL Simulation and Synthesis Environment and Design Process Basic Combinational Circuits Basic Binary Arithmetic Circuits Basic Sequential Circuits Registers Clock and Reset Circuits Dual Port RAM FIFO and DRAM Modeling A Design Case Study Finite Impulse Response Filter ASIC Design A Design Case Study A Microprogram Controller Design Error Detection and Correction Fixed Point Multiplication Fixed Point Division Floating Point Arithmetic *EDA for IC System Design, Verification, and Testing* Louis Scheffer,Luciano Lavagno,Grant Martin,2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The first volume EDA for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logical verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for IC designs design and verification languages digital simulation hardware acceleration and emulation and much more Save on the complete set Electronic CAD Frameworks Timothy J. Barnes,David Harrison,A. Richard Newton,Rick L. Spickelmier,2012-12-06 When it comes to frameworks the familiar story of the elephant and the six blind philosophers seems to apply As each philosopher encountered a separate part of the elephant each pronounced his considered but flawed judgement One blind philosopher felt a leg and thought it a tree Another felt the tail and thought he held a rope Another felt the elephant s flank and thought he stood before a wall We re supposed to learn about snap judgements from this allegory but its author might well have been describing design automation frameworks For in the reality of today s product development requirements a framework must be many things to many people xiv CAD Frameworks Integration Technology for CAD As the authors of this book note framework design is an optimization problem Somehow it has to be both a superior rope for one and a tremendous tree for another Somehow it needs to provide a standard environment for exploiting the full potential of computer aided engineering tools And somehow it has to make real such abstractions as interoperability and interchangeability For years we ve talked about a framework as something that provides application oriented services just as an operating system provides system level support And for years that simple statement has hid the tremendous complexity of actually providing those services **Design of Digital Systems and Devices** Marian Adamski,Alexander Barkalov,Marek Wegrzyn,2011-02-04 Logic design of digital devices is a very important part of the Computer Science It deals with design and testing of logic circuits for both data path and control unit of a digital system Design methods depend strongly on logic elements using for implementation of logic circuits Different programmable

logic devices are wide used for implementation of logic circuits Nowadays we witness the rapid growth of new and new chips but there is a strong lack of new design methods This book includes a variety of design and test methods targeted on different digital devices It covers methods of digital system design the development of theoretical base for construction and designing of the PLD based devices application of UML for digital design A considerable part of the book is devoted to design methods oriented on implementing control units using FPGA and CPLD chips Such important issues as design of reliable FSMs automatic design of concurrent logic controllers the models and methods for creating infrastructure IP services for the SoCs are also presented The editors of the book hope that it will be interesting and useful for experts in Computer Science and Electronics as well as for students who are viewed as designers of future digital devices and systems

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