

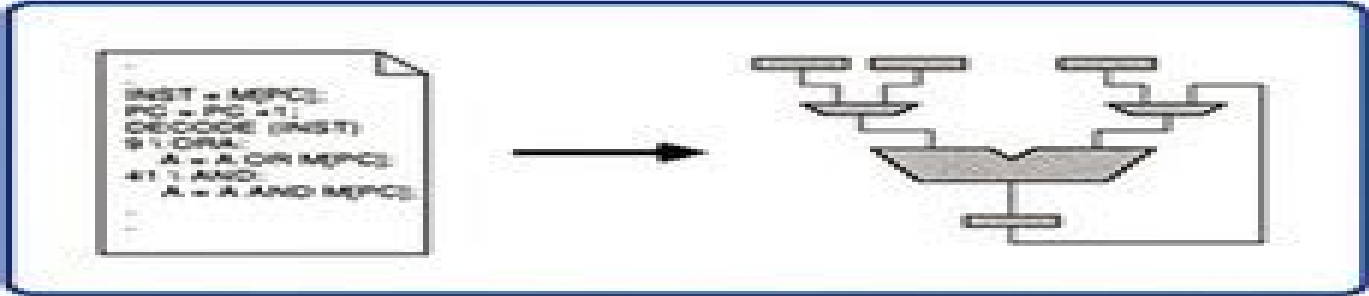
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# A Survey of High-Level Synthesis Systems

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edited by

**Robert A. Walker**  
**Raul Camposano**



The diagram is enclosed in a blue rectangular border. On the left, a document icon contains the following code:

```
-  
INST = MPCL;  
PC = PC + 1;  
DECODE (INST)  
if OPCL  
  A = A OR MPCL;  
if 1 AND  
  A = A AND MPCL;  
-
```

An arrow points from this code to a logic circuit on the right. The circuit features a central shaded trapezoidal block with multiple inputs and outputs, some of which are connected to rectangular logic gates above and below it.

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# Survey Of High Level Synthesis Systems

**Xiaolong Qi**



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