Assembler Macros

- Tired of writing segment, and ends? The .model small allows defaults and abbreviations
- Macros make program source more readable, easier to maintain
- Macro can be defined anywhere in assembler source
- Introduced by user defined name and macro keyword
- Terminated by endm keyword
- Macros may have 0 or more parameters, to be used and expanded in place inside macro body
- Note: When you dis-assemble (C++ compiler option) you do not see any macro; all is expanded

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