COMPUTER SCIENCE SERIES

THE ANATOMY OF A COMPILER SECOND EDITION

JOHN A. N. LEE

The Anatomy Of A Compiler Computer Science Series

NARAYAN CHANGDER

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matter can exist in both physical and abstract realms An artifact such as software is both tangible and not and must be classified as something in between or liminal The study and production of liminal artifacts allows for creative possibilities that are and have been possible only in computer science In It Began with Babbage computer scientist and writer Subrata Dasgupta examines the distinct history of computer science in terms of its creative innovations reaching back to Charles Babbage in 1819 Since all artifacts of computer science are conceived with a use in mind the computer scientist is not concerned with the natural laws that govern disciplines like physics or chemistry instead the field is more concerned with the concept of purpose This requirement lends itself to a type of creative thinking that as Dasgupta shows us has exhibited itself throughout the history of computer science More than any other computer science is the science of the artificial and has a unique history to accompany its unique focus The book traces a path from Babbage's Difference Engine in the early 19th century to the end of the 1960s by when a new academic discipline named computer science had come into being Along the way we meet characters like Babbage and Ada Lovelace Turing and von Neumann Shannon and Chomsky and a host of other people from a variety of backgrounds who collectively created this new science of the artificial And in the end we see how and why computer science acquired a nature and history all of its own A Formal Approach to Hardware Design Jørgen Staunstrup, 2012-12-06 A Formal Approach to Hardware Design discusses designing computations to be realised by application specific hardware It introduces a formal design approach based on a high level design language called Synchronized Transitions The models created using Synchronized Transitions enable the designer to perform different kinds of analysis and verification based on descriptions in a single language It is for example possible to use exactly the same design description both for mechanically supported verification and synthesis Synchronized Transitions is supported by a collection of public domain CAD tools These tools can be used with the book in presenting a course on the subject A Formal Approach to Hardware Design illustrates the benefits to be gained from adopting such techniques but it does so without assuming prior knowledge of formal design methods The book is thus not only an excellent reference it is also suitable for use by students and practitioners Circuit Synthesis with VHDL Roland Airiau, Jean-Michel Bergé, Vincent Olive, 2012-12-06 One of the main applications of VHDL is the synthesis of electronic circuits Circuit Synthesis with VHDL is an introduction to the use of VHDL logic RTL synthesis tools in circuit design The modeling styles proposed are independent of specific market tools and focus on constructs widely recognized as synthesizable by synthesis tools A statement of the prerequisites for synthesis is followed by a short introduction to the VHDL concepts used in synthesis Circuit Synthesis with VHDL presents two possible approaches to synthesis the first starts with VHDL features and derives hardware counterparts the second starts from a given hardware component and derives several description styles. The book also describes how to introduce the synthesis design cycle into existing design methodologies and the standard synthesis environment Circuit Synthesis with VHDL concludes with a case study providing a realistic example of the design flow from behavioral

description down to the synthesized level Circuit Synthesis with VHDL is essential reading for all students researchers design engineers and managers working with VHDL in a synthesis environment VHDL'92 Jean-Michel Bergé, Alain Fonkoua, Serge Maginot, Jacques Rouillard, 2012-12-06 An open process of restandardization conducted by the IEEE has led to the definitions of the new VHDL standard The changes make VHDL safer more portable and more powerful VHDL also becomes bigger and more complete The canonical simulator of VHDL is enriched by new mechanisms the predefined environment is more complete and the syntax is more regular and flexible Discrepancies and known bugs of VHDL 87 have been fixed However the new VHDL 92 is compatible with VHDL 87 with some minor exceptions This book presents the new VHDL 92 for the VHDL designer New features ar explained and classified Examples are provided each new feature is given a rationale and its impact on design methodology and performance is analysed Where appropriate pitfalls and traps are explained The VHDL designer will quickly be able to find the feature needed to evaluate the benefits it brings to modify previous VHDL 87 code to make it more efficient more portable and more flexible VHDL 92 is the essential update for all VHDL designers and managers involved in electronic design Application-Driven Architecture Synthesis Francky Catthoor, Lars-Gunnar Svensson, 2012-12-06 Application Driven Architecture Synthesis describes the state of the art of architectural synthesis for complex real time processing In order to deal with the stringent timing requirements and the intricacies of complex real time signal and data processing target architecture styles and target application domains have been adopted to make the synthesis approach feasible These approaches are also heavily application driven which is illustrated by many realistic demonstrations used as examples in the book The focus is on domains where application specific solutions are attractive such as significant parts of audio telecom instrumentation speech robotics medical and automotive processing image and video processing TV multi media radar sonar Application Driven Architecture Synthesis is of interest to both academics and senior design engineers and CAD managers in industry It provides an excellent overview of what capabilities to expect from future practical design tools and includes an extensive bibliography Encyclopedia of Microcomputers Allen Kent, James G. Williams, 1995-10-13 Strategies in the Microprocessor Industry to Teaching Critical Thinking and Problem Solving Computer Science & Perl Programming Jon Orwant, 2002-11-04 In its first five years of existence The Perl Journal ran 247 articles by over 120 authors Every serious Perl programmer subscribed to it and every notable Perl guru jumped at the opportunity to write for it TPJ explained critical topics such as regular expressions databases and object oriented programming and demonstrated Perl s utility for fields as diverse as astronomy biology economics AI and games The magazine gave birth to both the Obfuscated Perl Contest and the Perl Poetry contest and remains a proud and timeless achievement of Perl during one of its most exciting periods of development Computer Science and Perl Programming is the first volume of The Best of the Perl Journal compiled and re edited by the original editor and publisher of The Perl Journal Jon Orwant In this series we ve taken the very best and still relevant articles published in TPJ over its 5

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APL User's Guide Harry Katzan, 1971

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Motion Analysis and Image Sequence Processing M. Ibrahim Sezan, Reginald L. Lagendijk, 2012-12-06 An image or video sequence is a series of two dimensional 2 D images sequen tially ordered in time Image sequences can be acquired for instance by video motion picture X ray or acoustic cameras or they can be synthetically gen erated by sequentially ordering 2 D still images as in computer graphics and animation The use of image sequences in areas such as entertainment visual communications multimedia education medicine surveillance remote control and scientific research is constantly growing as the use of television and video systems are becoming more and more common The boosted interest in digital video for both consumer and professional products along with the availability of fast processors and memory at reasonable costs has been a major driving force behind this growth Before we elaborate on the two major terms that appear in the title of this book namely motion analysis and image sequence processing we like to place them in their proper contexts within the range of possible operations that involve image sequences In this book we choose to classify these operations into three major categories namely i image sequence processing ii image sequence analysis and iii visualization The interrelationship among these three categories is pictorially described in Figure 1 below in the form of an image sequence triangle *Algorithms for* Synthesis and Testing of Asynchronous Circuits Luciano Lavagno, Alberto L. Sangiovanni-Vincentelli, 2012-12-06 Since the second half of the 1980s asynchronous circuits have been the subject of a great deal of research following a period of relative oblivion The lack of interest in asynchronous techniques was motivated by the progressive shift towards synchronous design techniques that had much more structure and were much easier to verify and synthesize System design requirements made it impossible to eliminate totally the use of asynchronous circuits Given the objective difficulty encountered by designers the asynchronous components of electronic systems such as interfaces became a serious bottleneck in the design process The use of new models and some theoretical breakthroughs made it possible to develop asynchronous design techniques that were reliable and effective This book describes a variety of mathematical models and of algorithms that form the backbone and the body of a new design methodology for asyn chronous design The book is intended for asynchronous hardware

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