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Editors

Faizatul Akmar Abdul Nila, Chong Khai Lin and Azham Hussain

AIP | Conference Proceedings

The Physics Of Vlsi Aip Conference Proceedings No 1

Rachel Sandford



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The Physics of VLSI, Xerox, Palo Alto, 1984 J. C. Knights, 1984 **Energy Research Abstracts**, 1986 **VLSI Artificial Neural Networks Engineering** Mohamed I. Elmasry, 2012-12-06 Engineers have long been fascinated by how efficient and how fast biological neural networks are capable of performing such complex tasks as recognition Such networks are capable of recognizing input data from any of the five senses with the necessary accuracy and speed to allow living creatures to survive Machines which perform such complex tasks as recognition with similar accuracy and speed were difficult to implement until the technological advances of VLSI circuits and systems in the late 1980 s Since then the field of VLSI Artificial Neural Networks ANNs have witnessed an exponential growth and a new engineering discipline was born Today many engineering curriculums have included a course or more on the subject at the graduate or senior under graduate levels Since the pioneering book by Carver Mead Analog VLSI and Neural Systems Addison Wesley 1989 there were a number of excellent text and reference books on the subject each dealing with one or two topics This book attempts to present an integrated approach of a single research team to VLSI ANNs Engineering **Physics Briefs**, 1990

High-Speed Electronics Bengt Källbäck, Heinz Beneking, 2013-03-08 In the past a number of Satellite Conferences have been held in connection with the International Conference on Physics of Semiconductors covering selected fields of interest In 1986 when the main conference was held in Stockholm Sweden new phenomena had to be discussed super lattices hot electron phenomena and new device structures for high speed applications The aim was to select topics which would be of interest to physicists as well as to electronics engineers Therefore a Satellite Conference on High Speed Electronics Basic Physical Phenomena and Device Principles was arranged at Saltjobaden a coastal resort near Stockholm An organizing committee was established after the first suggestion made by Professor Grimmeiss from the University of Lund Sweden and some preliminary discussions on the Conference format A Program Committee was established to be responsible for the further selection of the invited talks the regular papers and poster presentation The aim was to have a broad spectrum of contributions to attract physicists as well as device oriented engineers and to stimulate discussions among the participants These Proceedings contain all oral and poster presentations with emphasis on the invited talks which give a competent overview of the field The fast publication by Springer Verlag has permitted the presentation of an up to date survey of the principles of high speed electronics Incorporation in the Springer Series in Electronics and Photonics will enable the book to be distributed worldwide and to reach all interested scientists **Handbook of Pattern Recognition and Computer**

Vision C. H. Chen, L. F. Pau, P. S. Wang, 1993-08 The book provides an up to date and authoritative treatment of pattern recognition and computer vision with chapters written by leaders in the field On the basic methods in pattern recognition and computer vision topics range from statistical pattern recognition to array grammars to projective geometry to skeletonization and shape and texture measures **BOOK JACKET** *Reliability of high-k / metal gate field-effect transistors considering*

circuit operational constraints Steve Kupke, 2016-06-06 After many decades the scaling of silicon dioxide based field effect transistors has reached insurmountable physical limits due to unintentional high gate leakage currents for gate oxide thicknesses below 2 nm. The introduction of high k metal gate stacks guaranteed the trend towards smaller transistor dimensions. The implementation of HfO₂ as high k dielectric also led to a substantial number of manufacturing and reliability challenges. The deterioration of the gate oxide properties under thermal and electric stress jeopardizes the circuit operation and hence needs to be comprehensively understood. As a starting point, 6T static random access memory cells were used to identify the different single device operating conditions. The strongest deterioration of the gate stack was found for nMOS devices under positive bias temperature instability (PBTI) stress, resulting in a severe threshold voltage shift and increased gate leakage current. A detailed investigation of physical origin and temperature and voltage dependency was done. The reliability issues were caused by the electron trapping into already existing HfO₂ oxygen vacancies. The oxygen vacancies reside in different charge states depending on applied stress voltages. This in return also resulted in a strong threshold voltage and gate current relaxation after stress was cut off. The reliability assessment using constant voltage stress does not reflect realistic circuit operation, which can result in a changed degradation behaviour. Therefore, the constant voltage stress measurements were extended by considering CMOS operational constraints, where it was found that the supply voltage frequently switches between the gate and drain terminal. The additional drain off state bias led to an increased V_t relaxation in comparison to zero bias voltage. The off state influence strongly depended on the gate length and became significant for short channel devices. The influence of the off state bias on the dielectric breakdown was studied and compared to the standard assessment methods. Different wear out mechanisms for drain only and alternating gate and drain stress were verified. Under drain only stress, the dielectric breakdown was caused by hot carrier degradation. The lifetime was correlated with the device length and amount of subthreshold leakage. The gate oxide breakdown under alternating gate and off state stress was caused by the continuous trapping and detrapping behaviour of high k metal gate devices. **ISTFA 2007**

Proceedings of the 33rd International Symposium for Testing and Failure Analysis ASM International, 2007-01-01
Printed by Springer. Der kan printes 10 sider ad gangen og max 40 sider pr session. *Principles of Extreme Mechanics (XM) in Design for Reliability (DfR)* Arief Suriadi Budiman, 2021-11-13 This book addresses issues pertinent to mechanics and stress generation especially in recent advanced cases of technology developments spanning from micrometer interconnects in solar photovoltaics, PV next gen energy storage devices to multilayers of nano scale composites enabling novel stretchable flexible conductor technologies. In these cases, the mechanics of materials have been pushed to the extreme edges of human knowledge to enable cutting edge unprecedented functionalities and technological innovations. Synchrotron X ray diffraction in situ small scale mechanical testing combined with physics based computational modeling simulation has been widely used approaches to probe these mechanics of the materials at their extreme limits due to their recently discovered distinct

advantages The techniques discussed in this manuscript are highlights specially curated from the broad body of work recently reported in the literature especially ones that the author had led the pursuits at the frontier himself Extreme stress generation in these advanced material leads to often new failure modes and hence the reliability of the final product is directly affected From the recent topics and various advanced case studies covered in this book the reader gets an updated knowledge of how new mechanics can and has been applied in Design for Reliability DfR for some of the latest technological innovations known in our modern world Further this also helps in building better designs which may avoid the pitfalls of the current practiced trends *The Journal of Chemical Physics* ,1987

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