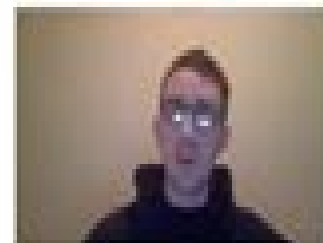


Deterministic vs.

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Stochastic Deterministic Averaging Processors

Warren J. Gross, Vincent C. Gaudet



Stochastic Deterministic Averaging Processors:

Stochastic and Deterministic Averaging Processors P. Mars, Wolfgang J. Poppelbaum, 1981 **Stochastic Computing: Techniques and Applications** Warren J. Gross, Vincent C. Gaudet, 2019-02-18 This book covers the history and recent developments of stochastic computing Stochastic computing SC was first introduced in the 1960s for logic circuit design but its origin can be traced back to von Neumann's work on probabilistic logic In SC real numbers are encoded by random binary bit streams and information is carried on the statistics of the binary streams SC offers advantages such as hardware simplicity and fault tolerance Its promise in data processing has been shown in applications including neural computation decoding of error correcting codes image processing spectral transforms and reliability analysis There are three main parts to this book The first part comprising Chapters 1 and 2 provides a history of the technical developments in stochastic computing and a tutorial overview of the field for both novice and seasoned stochastic computing researchers In the second part comprising Chapters 3 to 8 we review both well established and emerging design approaches for stochastic computing systems with a focus on accuracy correlation sequence generation and synthesis The last part comprising Chapters 9 and 10 provides insights into applications in machine learning and error control coding **Advances in Computers**, 1987-06-01 **Advances in Computers** **50 Years in the Semiconductor Underground** David K. Ferry, 2015-05-27 This book was derived from a talk that the author gave at the International Conference on Advanced Nanodevices and Nanotechnology in Hawaii The book is about science and engineering but is not on science and engineering It is not a textbook which develops the understanding of a small part of the field but a book about random encounters and about **Languages, Compilers, and Tools for Embedded Systems** Jack Davidson, Sang Lyul Min, 2001-03-07 This book constitutes the thoroughly refereed post proceedings of the ACM SIGPLAN Workshop on Languages Compilers and Tools for Embedded Systems LCTES 2000 held in Vancouver Canada in June 2000 The 12 revised full papers presented together with five posters were carefully reviewed and selected from a total of 43 submissions The book presents topical sections on formal methods and databases compilers tools hardware and work in process *Computational Intelligence in Telecommunications Networks* Witold Pedrycz, Athanasios Vasilakos, 2018-10-03 Telecommunications has evolved and grown at an explosive rate in recent years and will undoubtedly continue to do so As its functions applications and technology grow it becomes increasingly complex and difficult if not impossible to meet the demands of a global network using conventional computing technologies Computational intelligence CI is the technology of the future and the future is now Computational Intelligence in Telecommunications Networks offers an in depth look at the rapid progress of CI technology and shows its importance in solving the crucial problems of future telecommunications networks It covers a broad range of topics from Call Admission Control congestion control and QoS routing for ATM networks to network design and management optical mobile and active networks and Intelligent Mobile Agents Today's telecommunications professionals need a working knowledge of CI to exploit

its potential to overcome emerging challenges The CI community must become acquainted with those challenges to take advantage of the enormous opportunities the telecommunications field offers This text meets both those needs clearly concisely and with a depth certain to inspire further theoretical and practical advances Parallel Processing in Neural Systems and Computers Rolf Eckmiller, Georg Hartmann, Gert Hauske, 1990 The 119 contributions in this book cover a range of topics including parallel computing parallel processing in biological neural systems simulators for artificial neural networks neural networks for visual and auditory pattern recognition as well as for motor control AI and examples of optical and molecular computing The book may be regarded as a state of the art report and at the same time as an Interdisciplinary Reference Source for parallel processing It should catalyze international and interdisciplinary cooperation among computer scientists neuroscientists physicists and engineers in the attempt to 1 decipher parallel information processes in biology physics and chemistry 2 design conceptually similar technical parallel information processors **Advanced Signal-processing Algorithms, Architectures, and Implementations** ,1990 **Advanced Signal-processing Algorithms, Architectures, and Implementations** Franklin T. Luk, 1990 *IEEE International Conference on Artificial Neural Networks* ,1989

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