

Edition

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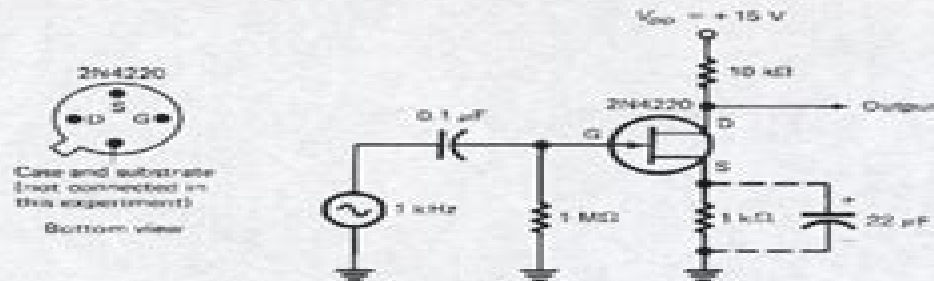


Fig. 7-7 Common-source amplifier.

- What is the voltage drop across the source resistor?
 - What is the voltage across the gate and source terminals?
 - What is the polarity of the gate with respect to the source?
 - What is this type of bias called?
- Increase the amplitude of the signal generator until the amplifier output signal measures 8 V peak-to-peak on your oscilloscope. Set up the oscilloscope to compare phase at the input and output of the amplifier. Use dual-trace operation or external triggering. Consult your oscilloscope manual for information.
 - With the amplifier output at 8 V peak-to-peak, what is the amplitude of the input signal?
 - What is the voltage gain of the amplifier?
 - What is the phase relationship of the input and output signals?
 - Add the source bypass capacitor as shown in Fig. 7-7. Adjust the amplitude of the signal generator so that the output signal from the amplifier is once again 8 V peak-to-peak.
 - What is the amplitude of the input signal?
 - What is the voltage gain of the amplifier?
 - The voltage gain of a common-source FET amplifier is set by the transistor's forward transfer admittance and the load resistor. If $Y_{fs} = 2.2 \text{ mS}$, calculate the voltage gain for the amplifier of Fig. 7-7:

$$A_v = Y_{fs} \times R_L$$
 - Does the voltage gain of step 3b agree reasonably well with the gain of step 3c?
 - Note the setting of the amplitude control on the signal generator. Disconnect one end of the gate resistor (the 1-MΩ resistor). Monitor the output signal of the amplifier with the oscilloscope. Turn the amplitude of the signal generator all the way up and then return it to its original setting.
 - What happens to the output signal?
 - Reconnect the gate resistor. Is the output signal restored?
 - What has happened?

PROCEDURE

Part 4

- Build the circuit shown in Fig. 7-8 on page 144. Do not connect the feedback path until you are instructed to do so. Monitor the output signal from

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Joacim Rocklöv



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