Condition Codes

Table 7. Hardware Communication Task Condition Codes (continued)

Reason Code	Error String	Error Description
002	ING_origuser_invalid	The userid and output correlator passed to INGHWCOM is not valid.
003	ING_interface_invalid	The hardware interface name passed to INGHWCOM is not valid. Allowed interface names are INTERNAL, SNMP, or SNA.
004	ING_ieterface_missing	No hardware interface name is passed to INGHWCOM.
005	ING_tgt_length-error	Parsing Error: The target object name (processor or image name) has an invalid length. It must be 1 to 8 characters.
006	ING_tgt_missing	Parsing Error: The target object name (processor or image name) is not specified.
007	ING_cpc_length_error	Parsing Error: The CPC address specification netid.nau has an invalid length. It must not exceed 17 characters.
008	ING_cpc_missing	Parsing Error: The CPC address specification, which is a required parameter for the request, is missing.
009	ING_imgname_length_err.	Parsing Error: The image name (Lpar name) parameter has an invalid length.
00A	ING_imgname_missing	Parsing Error: The image name (Lpar name) is a required parameter for the request, but has not been specified.
00B	ING_force_invalid	Parsing Error: The FORCE option is specified in the request but is not supported for the HW function. The following HW functions allow the FORCE option: ACTIVATE, DEACTIVATE, SYSRESET, LOAD
00C	ING_force_missing	Parsing Error: The FORCE option is required for the request, but has not been specified.
00D	ING_auth_missing	Parsing Error: The AUTHENTICATION specification that is required for each request is missing.
00E	ING_timeout_missing	Parsing Error: The required TIMEOUT parameter is missing in the request.
00F	ING_OCFCMD_truncated	Parsing Error: The HW function (OCFCMD) exceeds the maximum allowed length, which is 40 characters.
010	ING_OCFCMD_missing	Parsing Error: No HW function (OCFCMD) was specified in the request.
020	ING_SNMP_noIP_address	Parameter Resolution Error: The SNMP interface was specified for the HW request, but no IP address information is available.
021	ING_OCF_resolve_failed	Parameter Resolution Error: No HW function name to resolve, same as error 010.
022	ING_OCF_not_resolved	Parameter Resolution Error: An invalid HW function name was detected.
023	ING_BCP_null_cmd	Parameter Resolution Error: The BCP command retrieved from NetView Cglobal ING.xxxx.CMDTXT was empty.
024	ING_BCP_cmd_acc	Parameter Resolution Error: The NetView Cglobal variable containing the BCP command could not be retrieved.
025	ING_session_type_missing_or_invalid	Parameter Resolution Error: Session type values SYNC or ASYNC missing, or an invalid session type value was passed.
026	ING_active_session_type_mismatch	Parameter Resolution Error: A session was requested having a different session type SYNC/ASYNC than the active session. Reissue the INITCOM request with the correct session type or terminate the active session prior to requesting a new one.

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, **Designing Digital Computer Systems with Verilog** David J. Lilja,Sachin S. Sapatnekar,2004-12-02 This book serves both as an introduction to computer architecture and as a guide to using a hardware description language HDL to design model and simulate real digital systems The book starts with an introduction to Verilog the HDL chosen for the book since it is widely used in industry and straightforward to learn Next the instruction set architecture ISA for the simple VeSPA Very Small Processor Architecture processor is defined this is a real working device that has been built and tested at the University of Minnesota by the authors The VeSPA ISA is used throughout the remainder of the book to demonstrate how behavioural and structural models can be developed and intermingled in Verilog Although Verilog is used throughout the lessons learned will be equally applicable to other HDLs Written for senior and graduate students this book is also an ideal introduction to Verilog for practising engineers **Official Gazette of the United States Patent and Trademark Office** ,1997 *Itanium Architecture for Programmers* James S. Evans,Gregory L. Trimper,2003 Step by step guide to assembly language for the 64 bit Itanium processors with extensive examples Details of Explicitly Parallel Instruction Computing EPIC Instruction set addressing register stack engine predication I O procedure calls floating point operations and more Learn how to comprehend and optimize open source Intel and HP UX compiler output Understand the full power of 64 bit Itanium EPIC processorsItaniumreg Architecture for Programmersis a comprehensive introduction to the breakthrough capabilities of the new 64 bit Itanium architecture Using standard command line tools and extensive examples the authors illuminate the Itanium design within the broader context of contemporary computer architecture via a step by step investigation of Itanium assembly language Coverage includes The potential of Explicitly Parallel Instruction Computing EPIC Itanium instruction formats and addressing modes Innovations such as the register stack engine RSE and extensive predication Procedure calls and procedure calling mechanisms Floating point operations I O techniques from simple debugging to the use of files Optimization of output from open source Intel and HP UX compilers An essential resource for both computing professionals and students of architecture or assembly language Itanium Architecture for Programmers includes extensive printed and Web based references plus many numeric essay and programming exercises for each chapter **Quality Control Depot Storage Standards** United States. Defense Logistics Agency,1988 *Supply operations manual* United States. Defense Logistics Agency,1965 **Operator, Organizational, DS, and GS Maintenance Manual** ,1979 *Aviation Storekeeper 3 & 2* United States. Bureau of Naval Personnel,1959 **Computer Organization and Design MIPS Edition** David A. Patterson,John L. Hennessy,2013-09-30 Computer Organization and Design Fifth Edition is the latest update to the classic introduction to computer organization The text now contains new examples and material highlighting the emergence of mobile computing and the cloud It explores this generational change with updated content featuring tablet computers cloud infrastructure and the ARM mobile computing devices and x86 cloud computing architectures The book uses a MIPS

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safety the design of Internet of Things devices and systems and wireless communications standards like Bluetooth and ZigBee Uses real processors to demonstrate both technology and techniques Shows readers how to apply principles to actual design practice Stresses necessary fundamentals that can be applied to evolving technologies and helps readers gain facility to design large complex embedded systems Covers the design of Internet of Things IoT devices and systems including applications devices and communication systems and databases Introduces concepts of safety and security in embedded systems Includes new chapter on Automotive and Aerospace Systems Describes wireless communication standards such as Bluetooth and ZigBee *Maintenance of NAS Enroute Stage A, Air Traffic Control System* United States. Federal Aviation Administration,1968 **Virtual Machines** Jim Smith,Ravi Nair,2005-07-12 Virtual Machine technology applies the concept of virtualization to an entire machine circumventing real machine compatibility constraints and hardware resource constraints to enable a higher degree of software portability and flexibility Virtual machines are rapidly becoming an essential element in computer system design They provide system security flexibility cross platform compatibility reliability and resource efficiency Designed to solve problems in combining and using major computer system components virtual machine technologies play a key role in many disciplines including operating systems programming languages and computer architecture For example at the process level virtualizing technologies support dynamic program translation and platform independent network computing At the system level they support multiple operating system environments on the same hardware platform and in servers Historically individual virtual machine techniques have been developed within the specific disciplines that employ them in some cases they aren t even referred to as virtual machines making it difficult to see their common underlying relationships in a cohesive way In this text Smith and Nair take a new approach by examining virtual machines as a unified discipline Pulling together cross cutting technologies allows virtual machine implementations to be studied and engineered in a well structured manner Topics include instruction set emulation dynamic program translation and optimization high level virtual machines including Java and CLI and system virtual machines for both single user systems and servers Examines virtual machine technologies across the disciplines that use them operating systems programming languages and computer architecture defining a new and unified discipline Reviewed by principle researchers at Microsoft HP and by other industry research groups Written by two authors who combine several decades of expertise in computer system research and development both in academia and industry **Materiel Quality Control Storage Standards** United States. Defense Logistics Agency,1993 **Essentials of Computer Architecture** Douglas Comer,2017-01-06 This easy to read textbook provides an introduction to computer architecture while focusing on the essential aspects of hardware that programmers need to know The topics are explained from a programmer s point of view and the text emphasizes consequences for programmers Divided in five parts the book covers the basics of digital logic gates and data paths as well as the three primary aspects of architecture processors memories and I O systems The book also covers advanced topics of

parallelism pipelining power and energy and performance A hands on lab is also included The second edition contains three new chapters as well as changes and updates throughout **Computer Organization and Design** David A. Patterson,John L. Hennessy,2012 Rev ed of Computer organization and design John L Hennessy David A Patterson 1998 **Munitions systems specialist (AFSC 46150).** William J. Richard,1984 **The Best of Kinks and Hints** Allan Colombo,1997-12-23 The Best of Kinks and Hints is a compilation of over 100 articles from SDM Magazine about trouble shooting alarm system problems This completely revised edition retains the excellent composition of the original Kinks Hints for the Alarm Installer while increasing coverage on important topics such as access control burglar alarms and CCTV Here in a straight forward easy to read manner are practical pointers on Diagnosing intermittent problems and power failures Eliminating false alarms Selecting and testing components Locating malfunctions Many other problems which have been know to stump seasoned experts Whether used as a reference or read from cover to cover The Best of Kinks Hints will become the indispensable ally of every alarm specialist who takes pride in doing professional quality work with a minimum of wasted time and energy How to diagnose and solve alarm related problems Over 100 proven tricks of the trade An indispensable tool for every alarm specialist

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