



The Mabively Parallel Procebing System Jump1

Polskie Towarzystwo Matematyczne



The Massively Parallel Processing System JUMP-1:

The Massively Parallel Processing System JUMP-1 Hidehiko Tanaka, Yōichi Muraoka, 1996 The work features the development of the fundamental technologies for massively parallel processing covering research on the applications the language the operating system and the hardware architecture Also the present status and future plans are addressed The following topics are discussed in the section on applications the MGCG Method Parallelization of FEM Modeling of Group Behaviors Parallel Visualization Functional Memory Type Parallel Processing a Parallel Reduction Algorithm and Combination Algorithm As for the programming languages the SIMD Based Language NCX the Dataflow based Language V and the Parallel Object Oriented Language A NETL are discussed In the chapter on operating systems the subjects Design Philosophy and Objectives COS Software Architecture and Elements of the Operating System are amongst others addressed Finally the part on hardware architecture covers an Overview of the JUMP 1 System Memory Architecture Network Architecture I O Architecture and Implementation Issues Massively parallel processing is expected to play a crucial role in the development of almost all advanced technologies for the 21st century This book is intended to serve a large variety of researchers in the area of parallel computing

The Massively Parallel Processing System JUMP-1, 1996 **Correct Models of Parallel Computing** S. Noguchi, Ota Masahiro, 1997 The 21st century will be the age of network computing Among the many key technologies in this field parallel computing and networking technology will play very important roles In this book emphasis is placed on networking and modeling parallel computing The topics cover parallel computing algorithms parallel software massively parallel computing systems and related applications Articles cover parallel computing networking and related applications to initiate discussions Since the appearance of Transputer chip T9000 C104 and standardizations of IEEE1355 Transputer systems seem to have opened a new interesting area of parallel computing networking and many practical applications

High-Performance Computing and Networking Peter Sloot, Marian Bubak, Bob Hertzberger, 1998-04-15 Proceedings Parallel Computing *Object-Based Parallel and Distributed Computation* Jean-Pierre Briot, 1996-07-24 This book contains a refereed collection of revised papers selected from the presentations at the France Japan Workshop on Object Based Parallel and Distributed Computation OBPDC 95 held in Tokyo in June 1995 The 18 full papers included in the book constitute a representative well balanced set of timely research contributions to the growing field of object based concurrent computing The volume is organized in sections on massively parallel programming languages distributed programming languages formalisms distributed operating systems dependable distributed computing and software management

Advances In Computing Techniques: Algorithms, Databases And Parallel Processing H Imai, Kia Fock Loe, 1996-01-11 This proceedings collects papers in the areas of computer algorithms databases and parallel processing The papers were presented by very prominent computer scientists from Japan and Singapore invited to a three days JSPS NUS seminar held in Singapore on Dec 94

Transputer Applications and Systems '94 A. De Gloria, M. R. Jane, D.

Marini,1994 Proceedings Parallel Computing *Conference Proceedings of the 1997 International Conference on Supercomputing* ,1997 Proceedings Parallel Computing **Conference Proceedings** ,1998 *Proceedings of 1994 IEEE Region 10's Ninth Annual International Conference* Tony K. Y. Chan,1994 **Proceedings of the 16th International Conference on Distributed Computing Systems** ,1996 Fundamenta Informaticae Polskie Towarzystwo Matematyczne,1996 **The ... International Conference on Distributed Computing Systems** ,1996 Proceedings of the ... International Symposium on Parallel Architectures, Algorithms, and Networks (ISPAN) .,1999 Proceedings of the 1995 International Conference on Parallel Processing Prithviraj Banerjee,1995-08-08 This set of technical books contains all the information presented at the 1995 International Conference on Parallel Processing This conference held August 14 18 featured over 100 lectures from more than 300 contributors and included three panel sessions and three keynote addresses The international authorship includes experts from around the globe from Texas to Tokyo from Leiden to London Compiled by faculty at the University of Illinois and sponsored by Penn State University these Proceedings are a comprehensive look at all that s new in the field of parallel processing **The British National Bibliography** Arthur James Wells,1996 *Frontiers'99* IEEE Computer Society,1999 Proceedings from the February 1999 symposium illustrate the path that massively parallel technology follows in going from long range research to strategic resource to commercial application Thirty six contributions address such topics as parallel numerical computation and applications optical computing quantum computing petaflops and configurable computing Annotation copyrighted by Book News Inc Portland OR Advances in Computer Systems Architecture Pen-Chung Yew,Jingling Xue,2004-09-14 This book constitutes the refereed proceedings of the 9th Asia Pacific Computer Systems Architecture Conference ACSAC 2004 held in Beijing China in September 2004 The 45 revised full papers presented were carefully reviewed and selected from 154 submissions The papers are organized in topical sections on cache and memory reconfigurable and embedded architectures processor architecture and design power and energy management compiler and operating systems issues application specific systems interconnection networks prediction techniques parallel architectures and programming microarchitecture design and evaluation memory and I O systems and others **The First Aizu International Symposium on Parallel Algorithms/Architecture Synthesis** N. N. Mirenkov,1995 Proceedings Parallel Computing

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