



Integrated Circuit

Technology Of Integrated Circuits

M. J. Morant



Technology Of Integrated Circuits:

Technology of Integrated Circuits D. Widmann, H. Mader, H. Friedrich, 2013-03-09 Strongly involved with SIEMENS Corp in the tremendous recent developments of process technologies for IC fabrication the authors comprehensively record their authoritative knowledge and practical experience New materials modern planar technology process designs for CMOS Bipolar BICMOS and smart power technologies self adjusting doping techniques are just a few of the highlights With its strong application orientation this is a need to have book for professionals in semiconductor industries Senior students in electrical engineering and physics can use it as a textbook because of the systematic treatment of the subjects With regard to their later careers as industrial engineers they will particularly appreciate the deep insight into the actual methods and problems of IC manufacturing

Integrated Circuit Design and Technology M. J. Morant, 2013-12-01 **Technology of Integrated Circuits** D. Widmann, H. Mader, H. Friedrich, 2014-01-15 Integrated Circuit Fabrication Technology David J. Elliott, 1982 High-speed Integrated Circuit Technology Mark J. W. Rodwell, 2001 This book reviews the state of the art of very high speed digital integrated circuits Commercial applications are in fiber optic transmission systems operating at 10 40 and 100 Gb s while the military application is ADCs and DACs for microwave radar The book contains detailed descriptions of the design fabrication and performance of wideband Si SiGe GaAs and InP based bipolar transistors The analysis design and performance of high speed CMOS silicon bipolar and III V digital ICs are presented in detail with emphasis on application in optical fiber transmission and mixed signal ICs The underlying physics and circuit design of rapid single flux quantum RSFQ superconducting logic circuits are reviewed and there is extensive coverage of recent integrated circuit results in this technology Contents Preface M J W Rodwell High Speed and High Data Bandwidth Transmitter and Receiver for Multi Channel Serial Data Communication with CMOS Technology M Fukaishi et al High Performance Si and SiGe Bipolar Technologies and Circuits M Wurzer et al Self Aligned Si BJT SiGe HBT Technology and Its Application to High Speed Circuits K Washio Small Scale InGaP GaAs Heterojunction Bipolar Transistors for High Speed and Low Power Integrated Circuit Applications T Oka et al Prospects of InP Based IC Technologies for 100 Gbit S Class Lightwave Communications Systems T Enoki et al Scaling of InGaAs InAlAs HBTs for High Speed Mixed Signal and mm Wave ICs M J W Rodwell Progress Toward 100 GHz Logic in InP HBT IC Technology C H Fields et al Cantilevered Base InP DHBT for High Speed Digital Applications A L Gutierrez Aitken et al RSFQ Technology Physics and Devices P Bunyk et al RSFQ Technology Circuits and Systems D K Brock Readership Researchers industrialists and academics in electrical and electronic engineering

Silicide Technology for Integrated Circuits Institution of Electrical Engineers, 2004-12-21 This is the first book to provide guidance on the development and application of metal silicide technology as it emerges from the scientific to the prototype and manufacturing stages Other key topics covered are fundamentals present and future silicide technology for Si based devices and characterisation methods Suitable for engineers and students in microelectronics Proceedings for the

Second Workshop on Smart Power/Power Integrated Circuits: Technology and Applications, 1994 *High-speed Integrated Circuit Technology* Mark J. W. Rodwell, 2001 This book reviews the state of the art of very high speed digital integrated circuits Commercial applications are in fiber optic transmission systems operating at 10 40 and 100 Gb s while the military application is ADCs and DACs for microwave radar The book contains detailed descriptions of the design fabrication and performance of wideband Si SiGe GaAs and InP based bipolar transistors The analysis design and performance of high speed CMOS silicon bipolar and III V digital ICs are presented in detail with emphasis on application in optical fiber transmission and mixed signal ICs The underlying physics and circuit design of rapid single flux quantum RSFQ superconducting logic circuits are reviewed and there is extensive coverage of recent integrated circuit results in this technology **Linear Integrated Circuits** D Choudhury Roy, 2003 Designed Primarily For Courses In Operational Amplifier And Linear Integrated Circuits For Electrical Electronic Instrumentation And Computer Engineering And Applied Science Students Includes Detailed Coverage Of Fabrication Technology Of Integrated Circuits Basic Principles Of Operational Amplifier Internal Construction And Applications Have Been Discussed Important Linear Ics Such As 555 Timer 565 Phase Locked Loop Linear Voltage Regulator Ics 78 79 Xx And 723 Series D A And A D Converters Have Been Discussed In Individual Chapters Each Topic Is Covered In Depth Large Number Of Solved Problems Review Questions And Experiments Are Given With Each Chapter For Better Understanding Of Text Salient Features Of Second Edition Additional Information Provided Wherever Necessary To Improve The Understanding Of Linear Ics Chapter 2 Has Been Thoroughly Revised Dc Ac Analysis Of Differential Amplifier Has Been Discussed In Detail The Section On Current Mirrors Has Been Thoroughly Updated More Solved Examples Pspice Programs And Answers To Selected Problems Have Been Added **Large Scale Integrated Circuits Technology: State of the Art and Prospects** Leo Esaki, G. Soncini, 2012-12-06 A NATO Advanced Study Institute on Large Scale Integrated Circuits Technology State of the Art and Prospects was held at Ettore Majorana Centre for Scientific Culture Erice Italy on July 15 27 1981 the first course of the International School of Solid State Device Research This volume contains the School Proceedings fundamentals as well as up to date information on each subject presented by qualified authors The material covered in this volume has been arranged in self consistent chapters Therefore the Proceedings may be used as a suitable textbook or authoritative review for research workers and advanced students in the relevant field The nascent information society is based on advanced technologies which will revolutionize human abilities to manipulate and communicate information One of the most important underpinnings for developing such an information society lies in innovations in semiconductor microelectronics Such innovations indeed are dramatically reducing the cost of transmitting storing and processing information with improved performance ushering in an era characterized by large scale integration the subject of this book

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