

RF and microwave solid-state power amplifiers design requires specialised engineering

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In the world of RF and microwave engineering, the design and development of solid-state amplifiers is a speciality. It has always required many years of specialised engineering experience and a suitable collection of test and measurement equipment. While these will always be necessary, to be successful in the marketplace today, it is also essential to use a combination of specialised and general CAD tools.

The RF and microwave design software we use has removed much of the risk and guess work from creating a new amplifier design. For us, the software design tools have transformed the process of designing an amplifier in terms of speed, substantially shortening the product design cycle and massively improving the probability that the new device will perform as specified at the first attempt.

In the last 10 years or so wide bandgap transistors (SiC MESFETs and GaN HEMTs) have appeared on the market for high power RF/microwave transistors. They offer higher power density and higher voltage operation, which in turn are associated with much lower parasitic capacitances and much higher load-line dynamic resistance, and hence wider bandwidth applications. Of the two kinds the GaN HEMTs offer higher gain performance and became dominant on the market. However, the much wider bandwidth matching networks could not be designed optimally with the traditional Smith Chart and optimisation techniques [1]. The new requirements for broadband high power and high efficiency performance require new and more sophisticated matching networks synthesis techniques such as the real frequency technique [4], [7].

The design software for the RF and microwave amplifiers

The most important part of the design relies upon extensive use of two RF/microwave software programs which are used in tandem [5]. These are the MultiMatch Amplifier Design Wizard and the general simulator/optimizer Microwave Office [7], [8].

In MultiMatch the designer uses the powerful real frequencies synthesis technique

for lossy and lossless matching network design to achieve the optimum performance from the RF/microwave transistors [4], [7]. The designer can also use the new power parameters Ga

properly and fully defined load-line approach) to design power amplifiers [2], [3], [4], [6]. MultiMatch is like a massive amplifier design template where the creation of the schematics

Figure 1: Extraction of design data in Microwave Office.

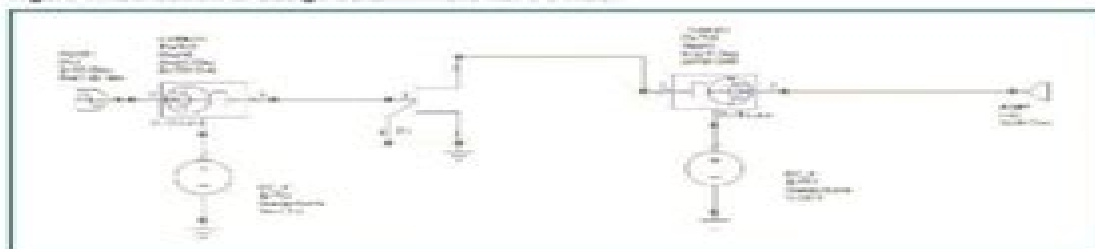


Figure 2: Fitting linear model to the S-parameters in MultiMatch.

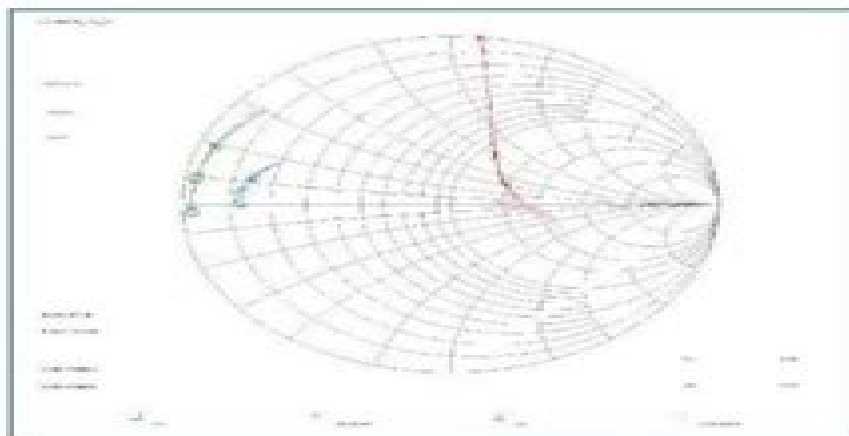


Figure 3: MultiMatch layout.

Solid State Electronic Amplifiers An Empirical Approach

**Tomohiko Ishii, Hisanobu
Wakita, Kazuyoshi Ogasawara, Yang-
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