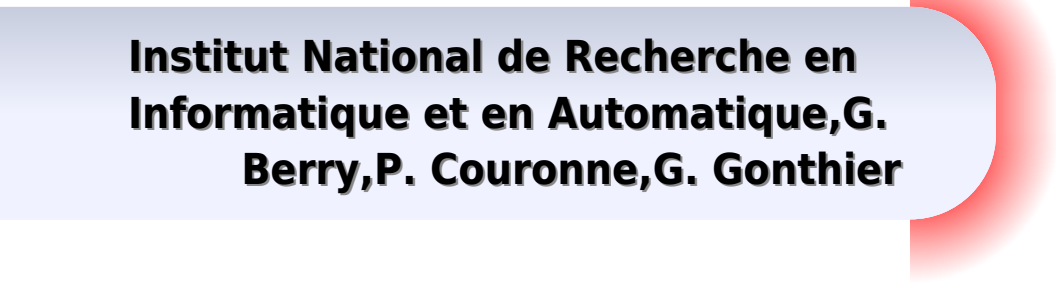

SYNCHRONOUS PROGRAMMING OF REACTIVE SYSTEMS

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Synchronous Programming Of Reactive Systems

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Synchronous Programming Of Reactive Systems:

Synchronous Programming of Reactive Systems Nicolas Halbwachs, 2013-06-29 This book will attempt to give a first synthesis of recent works concerning reactive system design The term reactive system has been introduced in order to avoid the ambiguities often associated with by the term real time system which although best known and more suggestive has been given so many different meanings that it is almost inevitably misunderstood Industrial process control systems transportation control and supervision systems signal processing systems are examples of the systems we have in mind Although these systems are more and more computerized it is surprising to notice that the problem of time in computer science has been studied only recently by pure computer scientists Until the early 1980s time problems were regarded as the concern of performance evaluation or of some unjustly scorned industrial computer engineering or at best of operating systems A second surprising fact in contrast is the growth of research concerning timed systems during the last decade The handling of time has suddenly become a fundamental goal for most models of concurrency In particular Robin Alilner's pioneering works about synchronous process algebras gave rise to a school of thought adopting the following abstract point of view As soon as one admits that a system can instantaneously react to events i.e. *Synchronous Programming of Reactive Systems: an Introduction to ESTEREL*. Institut National de Recherche en Informatique et en Automatique, G.

Berry, P. Couronne, G. Gonthier, 1987 **Synchronous Programming of Reactive Systems** Simon Bloch, 1987 Reactive Systems in Java Clement Escoffier, Ken Finnigan, 2021-11-10 Reactive systems and event driven architecture are becoming indispensable to application design and companies are taking note Reactive systems ensure that applications are responsive resilient and elastic no matter what failures or errors may be occurring while event driven architecture offers a flexible and composable option for distributed systems This practical book helps Java developers bring these approaches together using Quarkus 2 x the Kubernetes native Java framework Clement Escoffier and Ken Finnigan show you how to take advantage of event driven and reactive principles to build robust distributed systems reducing latency and increasing throughput particularly in microservices and serverless applications You'll also get a foundation in Quarkus to help you create true Kubernetes native applications for the cloud Understand the fundamentals of reactive systems and event driven architecture Learn how to use Quarkus to build reactive applications Combine Quarkus with Apache Kafka or AMQP to build reactive systems Develop microservices that utilize messages with Quarkus for use in event driven architectures Learn how to integrate external messaging systems such as Apache Kafka with Quarkus Build applications with Quarkus using reactive systems and reactive programming concepts Embedded Systems Design Bruno Bouysse, Joseph Sifakis, 2005-02-07 Embedded systems now include a very large proportion of the advanced products designed in the world spanning transport avionics space automotive trains electrical and electronic appliances cameras toys televisions home appliances audio systems and cellular phones process control energy production and distribution factory automation and optimization

telecommunications satellites mobile phones and telecom networks and security e commerce smart cards etc The extensive and increasing use of embedded systems and their integration in everyday products marks a significant evolution in information science and technology We expect that within a short timeframe embedded systems will be a part of nearly all equipment designed or manufactured in Europe the USA and Asia There is now a strategic shift in emphasis for embedded systems designers from simply achieving feasibility to achieving optimality Optimal design of embedded systems means targeting a given market segment at the lowest cost and delivery time possible Optimality implies seamless integration with the physical and electronic environment while respecting real world constraints such as hard deadlines reliability availability robustness power consumption and cost In our view optimality can only be achieved through the emergence of embedded systems as a discipline in its own right

Compiler Construction David A. Watt, 2003-06-29 ETAPS2000 was the third instance of the European Joint Conferences on Theory and Practice of Software ETAPS is an annual federated conference that was established in 1998 by combining a number of existing and new conferences This year it comprised five conferences FOSSACS FASE ESOP CC TACAS and satellite workshops CBS CMCS CoFI GRATRA INT seven invited lectures a panel discussion and ten tutorials The events that comprise ETAPS address various aspects of the system development process including specification design implementation analysis and improvement The languages methodologies and tools which support these activities are all well within its scope Different blends of theory and practice are represented with an inclination towards theory with a practical motivation on one hand and soundly based practice on the other Many of the issues involved in software design apply to systems in general including hardware systems and the emphasis on software is not intended to be exclusive

Higher-Level Hardware Synthesis Richard Sharp, 2004-03-12 In the mid 1960s when a single chip contained an average of 50 transistors Gordon Moore observed that integrated circuits were doubling in complexity every year In an influential article published by Electronics Magazine in 1965 Moore predicted that this trend would continue for the next 10 years Despite being criticized for its unrealistic optimism Moore's prediction has remained valid for far longer than even he imagined today chips built using state the art techniques typically contain several million transistors The advances in fabrication technology that have supported Moore's law for four decades have fuelled the computer revolution However this exponential increase in transistor density poses new design challenges to engineers and computer scientists alike New techniques for managing complexity must be developed if circuits are to take full advantage of the vast numbers of transistors available In this monograph we investigate both i the design of high level languages for hardware description and ii techniques involved in translating these high level languages to silicon We propose SAFL a first order functional language designed specifically for behavioral hardware description and describe the implementation of its associated silicon compiler We show that the high level properties of SAFL allow one to exploit program analyses and optimizations that are not employed in existing synthesis systems Furthermore since SAFL fully abstracts the low level details of the implementation technology we

show how it can be compiled to a range of different design styles including fully synchronous design and globally asynchronous locally synchronous GALS circuits

Tool Support for System Specification, Development and Verification Rudolf Berghammer, Yassine Lakhnech, 2012-12-06 The correct development of large complex pieces of software demands a thorough structuring of the design process In a first phase the requirements engineering is relevant for capturing the relevant functionality and its adequate formalization in precise mathematical definitions Prototyping can be used as a means for checking the functional behaviour at this early stage of development The adequate specification resulting from the first phase is then the basis for the second phase which comprises the derivation of an implementation This phase requires the use of formal methods and tools to verify validate the implementation A prerequisite for applying this approach is to have a suitable mechanical support This volume contains the proceedings of the International Workshop Tool Support for System Specification Development and Verification organized June 1-4 1998 in Malente Germany This workshop is the third in a series of events devoted to this topic The first two workshops were held in 1994 in Kiel and 1996 in Bremen Germany The aim of this workshop is to provide a forum for researchers interested in the use and development of tools which support the use of mathematical techniques for the specification development and verification of systems The workshop covers the spectrum from verification tools to graphical editors and compilers The program of the workshop included an invited lecture and 26 talks The invited lecture was given by F W von Henke University of Ulm on Mechanized formal methods and system design

System-Level Synthesis Ahmed Amine Jerraya, Jean Mermet, 2012-12-06 System Level Synthesis deals with the concurrent design of electronic applications including both hardware and software The issue has become the bottleneck in the design of electronic systems including both hardware and software in several major industrial fields including telecommunications automotive and aerospace engineering The major difficulty with the subject is that it demands contributions from several research fields including system specification system architecture hardware design and software design Most existing books cover well only a few aspects of system level synthesis The present volume presents a comprehensive discussion of all the aspects of system level synthesis Each topic is covered by a contribution written by an international authority on the subject

Hybrid Systems V Panos J. Antsaklis, 1999-02-24 This book constitutes the strictly refereed post proceedings of the 5th International Hybrid Systems Workshop held in Notre Dame Indiana USA in September 1998 The 23 revised full papers presented in the book have gone through two rounds of thorough reviewing and revision The volume presents state of the art research results and particularly addresses such areas as program verification concurrent and distributed processes logic programming logics of programs discrete event simulation calculus of variations optimization differential geometry Lie algebras automata theory dynamical systems etc

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