

SWITCHING THEORY FOR LOGIC SYNTHESIS



Tsutomu Sasao

Switching Theory For Logic Synthesis

Wenbin Ji



Switching Theory For Logic Synthesis:

Switching Theory for Logic Synthesis Tsutomu Sasao, 2012-12-06 Switching Theory for Logic Synthesis covers the basic topics of switching theory and logic synthesis in fourteen chapters Chapters 1 through 5 provide the mathematical foundation Chapters 6 through 8 include an introduction to sequential circuits optimization of sequential machines and asynchronous sequential circuits Chapters 9 through 14 are the main feature of the book These chapters introduce and explain various topics that make up the subject of logic synthesis multi valued input two valued output function logic design for PLDs FPGAs EXOR based design and complexity theories of logic networks An appendix providing a history of switching theory is included The reference list consists of over four hundred entries Switching Theory for Logic Synthesis is based on the author's lectures at Kyushu Institute of Technology as well as seminars for CAD engineers from various Japanese technology companies Switching Theory for Logic Synthesis will be of interest to CAD professionals and students at the advanced level It is also useful as a textbook as each chapter contains examples illustrations and exercises

Fundamentals of Switching Theory and Logic Design Jaakko Astola, Radomir S. Stankovic, 2006-03-07 Fundamentals of Switching Theory and Logic Design discusses the basics of switching theory and logic design from a slightly alternative point of view and also presents links between switching theory and related areas of signal processing and system theory Switching theory is a branch of applied mathematic providing mathematical foundations for logic design which can be considered as a part of digital system design concerning realizations of systems whose inputs and outputs are described by logic functions

Fundamentals of Logic Design and Switching Theory Arthur D. Friedman, 1986

Logic Functions and Equations Bernd Steinbach, Christian Posthoff, 2009-01-29 Tsutomu Sasao Kyushu Institute of Technology Japan The material covered in this book is quite unique especially for people who are reading English since such material is quite hard to find in the U S literature German and Russian people have independently developed their theories but such work is not well known in the U S societies On the other hand the theories developed in the U S are not conveyed to the other places Thus the same theory is re-invented or re-discovered in various places For example the switching theory was developed independently in the U S Europe and Japan almost at the same time 4 18 19 Thus the same notions are represented by different terminologies For example the Shegalkin polynomial is often called complement free ring sum Reed Muller expression 10 or Positive polarity Reed Muller expression 19 Anyway it is quite desirable that such a unique book like this is written in English and many people can read it without any difficulties The authors have developed a logic system called XBOOLE It performs logical operations on the given functions With XBOOLE the readers can solve the problems given in the book Many examples and complete solutions to the problems are shown so the readers can study at home I believe that the book containing many exercises and their solutions is quite useful not only for the students but also the professors

VLSI-SoC: Advanced Topics on Systems on a Chip Ricardo Reis, Vincent Mooney, Paul Hasler, 2009-04-13 This book contains extended and revised versions of the best papers that were presented

during the fifteenth edition of the IFIP IEEE WG10 5 International Conference on Very Large Scale Integration a global System on a Chip Design CAD conference The 15th conference was held at the Georgia Institute of Technology Atlanta USA October 15 17 2007 Previous conferences have taken place in Edinburgh Trondheim Vancouver Munich Grenoble Tokyo Gramado Lisbon Montpellier Darmstadt Perth and Nice The purpose of this conference sponsored by IFIP TC 10 Working Group 10 5 and by the IEEE Council on Electronic Design Automation CEDA is to provide a forum to exchange ideas and show industrial and academic research results in the field of microelectronics design The current trend toward increasing chip integration and technology process advancements brings about stimulating new challenges both at the physical and system design levels as well in the test of these systems VLSI SoC conferences aim to address these exciting new issues

Decision Diagram Techniques for Micro- and Nanoelectronic Design Handbook Svetlana N. Yanushkevich, D. Michael Miller, Vlad P. Shmerko, Radomir S. Stankovic, 2018-10-03 Decision diagram DD techniques are very popular in the electronic design automation EDA of integrated circuits and for good reason They can accurately simulate logic design can show where to make reductions in complexity and can be easily modified to model different scenarios Presenting DD techniques from an applied perspective *Decision Diagram Techniques for Micro and Nanoelectronic Design Handbook* provides a comprehensive up to date collection of DD techniques Experts with more than forty years of combined experience in both industrial and academic settings demonstrate how to apply the techniques to full advantage with more than 400 examples and illustrations Beginning with the fundamental theory data structures and logic underlying DD techniques they explore a breadth of topics from arithmetic and word level representations to spectral techniques and event driven analysis The book also includes abundant references to more detailed information and additional applications *Decision Diagram Techniques for Micro and Nanoelectronic Design Handbook* collects the theory methods and practical knowledge necessary to design more advanced circuits and places it at your fingertips in a single concise reference

Recent Developments in Switching Theory Amar Mukhopadhyay, 2013-10-22 Electrical Science Series *Recent Developments in Switching Theory* covers the progress in the study of the switching theory The book discusses the simplified proof of Post's theorem on completeness of logic primitives the role of feedback in combinational switching circuits and the systematic procedure for the design of Lupanov decoding networks The text also describes the classical results on counting theorems and their application to the classification of switching functions under different notions of equivalence including linear and affine equivalences The development of abstract harmonic analysis of combinational switching functions the theory of universal logic modules methods of their construction and upper bounds on the input terminals and cellular logic are also considered The book further tackles the systematic techniques for the realization of multi output logic function by means of multirail cellular cascades the programmable cellular logic and the logical design of programmable arrays Electrical engineers electronics engineers computer professionals and student taking related courses will find the book invaluable

Neural information

processing [electronic resource] Nikil R. Pal, 2004-11-18 Annotation This book constitutes the refereed proceedings of the 11th International Conference on Neural Information Processing ICONIP 2004 held in Calcutta India in November 2004 The 186 revised papers presented together with 24 invited contributions were carefully reviewed and selected from 470 submissions The papers are organized in topical sections on computational neuroscience complex valued neural networks self organizing maps evolutionary computation control systems cognitive science adaptive intelligent systems biometrics brain like computing learning algorithms novel neural architectures image processing pattern recognition neuroinformatics fuzzy systems neuro fuzzy systems hybrid systems feature analysis independent component analysis ant colony neural network hardware robotics signal processing support vector machine time series prediction and bioinformatics *VLSI Circuits and Embedded Systems* Hafiz Md. Hasan Babu, 2022-07-29 Very Large Scale Integration VLSI creates an integrated circuit IC by combining thousands of transistors into a single chip While designing a circuit reduction of power consumption is a great challenge VLSI designs reduce the size of circuits which eventually reduces the power consumption of the devices However it increases the complexity of the digital system Therefore computer aided design tools are introduced into hardware design processes Unlike the general purpose computer an embedded system is engineered to manage a wide range of processing tasks Single or multiple processing cores manage embedded systems in the form of microcontrollers digital signal processors field programmable gate arrays and application specific integrated circuits Security threats have become a significant issue since most embedded systems lack security even more than personal computers Many embedded systems hacking tools are readily available on the internet Hacking in the PDAs and modems is a pervasive example of embedded systems hacking This book explores the designs of VLSI circuits and embedded systems These two vast topics are divided into four parts In the book's first part the Decision Diagrams DD have been covered DDs have extensively used Computer Aided Design CAD software to synthesize circuits and formal verification The book's second part mainly covers the design architectures of Multiple Valued Logic MVL Circuits MVL circuits offer several potential opportunities to improve present VLSI circuit designs The book's third part deals with Programmable Logic Devices PLD PLDs can be programmed to incorporate a complex logic function within a single IC for VLSI circuits and Embedded Systems The fourth part of the book concentrates on the design architectures of Complex Digital Circuits of Embedded Systems As a whole from this book core researchers academicians and students will get the complete picture of VLSI Circuits and Embedded Systems and their applications *Theory and Applications of Models of Computation* Jin-Yi Cai, 2007-05-09 This book constitutes the refereed proceedings of the 4th International Conference on Theory and Applications of Models of Computation TAMC 2007 held in Shanghai China in May 2007 It addresses all major areas in computer science mathematics especially logic and the physical sciences particularly with regard to computation and computability theory The papers particularly focus on algorithms complexity and computability theory

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