

**SWITCHED-CURRENT
DESIGN AND
IMPLEMENTATION OF
OVERSAMPLING A/D
CONVERTERS**

Nianxiong Tan

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Switched Current Design And Implementation Of Oversampling Ad Converters

Bengt E. Jonsson



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Switched-Current Design and Implementation of Oversampling A/D Converters Nianxiong Tan, 2012-10-12

Switched Current Design and Implementation of Oversampling A/D Converters discusses the switched current SI technique and its application in oversampling A/D converters design. The SI technique is an analog sampled data technique that fully exploits the digital CMOS process. Compared with the traditional switched capacitor SC technique, the SI technique has both pros and cons that are highlighted in the book. With the consideration of similarity and difference of SI and SC techniques, oversampling A/D converter architectures are tailored and optimized for SI design and implementation in the book. Switched Current Design and Implementation of Oversampling A/D Converters emphasizes the practical aspects of SI circuits without tedious mathematical derivations and is full of circuit design and implementation examples. There are more than 10 different chips included in the book demonstrating the high speed over 100 MHz and ultra low voltage 1.2 V operation of SI circuits and systems in standard digital CMOS processes. Therefore, the book is of special value as a practical guide for designing SI circuits and SI oversampling A/D converters. Switched Current Design and Implementation of Oversampling A/D Converters serves as an excellent reference for analog designers, especially A/D converter designers, and is of interest to digital designers for real time signal processing who need A/D interfaces. The book may also be used as a text for advanced courses on the subject.

Switched-Current Signal Processing and A/D Conversion Circuits Bengt E. Jonsson, 2013-04-18 Switched Current Signal Processing and A/D Conversion Circuits Design and Implementation describes the design and implementation of switched current SI circuits with emphasis on signal processing and data conversion applications. The work includes theoretical analysis, high level and circuit level simulation results, as well as measurement results from a few of the author's circuit implementations. An extensive overview of the SI field of research is also given. The book contains an extensive overview of the switched current field of research and can therefore be used as a quick reference to the field. The description of each design example has been organized to describe the entire design flow from system level design and simulation to circuit simulation, layout and measurement as accurately as possible. Thus, it is possible to follow each step in the design process. Switched Current Signal Processing and A/D Conversion Circuits Design and Implementation is an invaluable reference for researchers and circuit designers working with one chip mixed signal system solutions and low voltage analog CMOS design. It will also be appreciated by anyone requiring a quick overview of what has been done in the SI field.

CMOS Data Converters for Communications Mikael Gustavsson, J. Jacob Wikner, Nianxiong Tan, 2005-12-15 CMOS Data Converters for Communications distinguishes itself from other data converter books by emphasizing system related aspects of the design and frequency domain measures. It explains in detail how to derive data converter requirements for a given communication system, baseband, passband, and multi-carrier systems. The authors also review CMOS data converter architectures and discuss their suitability for communications. The rest of the book is dedicated to high performance CMOS

data converter architecture and circuit design Pipelined ADCs parallel ADCs with an improved passive sampling technique and oversampling ADCs are the focus for ADC architectures while current steering DAC modeling and implementation are the focus for DAC architectures The principles of the switched current and the switched capacitor techniques are reviewed and their applications to crucial functional blocks such as multiplying DACs and integrators are detailed The book outlines the design of the basic building blocks such as operational amplifiers comparators and reference generators with emphasis on the practical aspects To operate analog circuits at a reduced supply voltage special circuit techniques are needed Low voltage techniques are also discussed in this book CMOS Data Converters for Communications can be used as a reference book by analog circuit designers to understand the data converter requirements for communication applications It can also be used by telecommunication system designers to understand the difficulties of certain performance requirements on data converters It is also an excellent resource to prepare analog students for the new challenges ahead

Systematic Design of Sigma-Delta Analog-to-Digital Converters Ovidiu Bajdechi, Johan Huijsing, 2004-04-30 Systematic Design of Sigma Delta Analog to Digital Converters describes the issues related to the sigma delta analog to digital converters ADCs design in a systematic manner from the top level of abstraction represented by the filters defining signal and noise transfer functions STF NTF passing through the architecture level where topology related performance is calculated and simulated and finally down to parameters of circuit elements like resistors capacitors and amplifier transconductances used in individual integrators The systematic approach allows the evaluation of different loop filters order aggressiveness discrete time or continuous time implementation with quantizers varying in resolution Topologies explored range from simple single loops to multiple cascaded loops with complex structures including more feedbacks and feedforwards For differential circuits with switched capacitor integrators for discrete time DT loop filters and active RC for continuous time CT ones the passive integrator components are calculated and the power consumption is estimated based on top level requirements like harmonic distortion and noise budget This unified systematic approach to choosing the best sigma delta ADC implementation for a given design target yields an interesting solution for a high resolution broadband DSL like ADC operated at low oversampling ratio which is detailed down to transistor level schematics The target audience of Systematic Design of Sigma Delta Analog to Digital Converters are engineers designing sigma delta ADCs and or switched capacitor and continuous time filters both beginners and experienced It is also intended for students academics involved in sigma delta and analog CAD research

CMOS Telecom Data Converters Angel Rodríguez-Vázquez, Fernando Medeiro, Edmond Janssens, 2013-03-09 CMOS Telecom Data Converters compiles the latest achievements regarding the design of high speed and high resolution data converters in deep submicron CMOS technologies The four types of analog to digital converter architectures commonly found in this arena are covered namely sigma delta pipeline folding interpolating and flash For all these types latest achievements regarding the solution of critical architectural and circuital issues are presented and illustrated through IC prototypes with

measured state of the art performances Some of these prototypes are conceived to be employed at the chipset of newest generation wireline modems ADSL and ADSL Others are intended for wireless transceivers Besides analog to digital converters the book also covers other functions needed for communication systems such as digital to analog converters analog filters programmable gain amplifiers digital filters and line drivers

CMOS Current Amplifiers Giuseppe Palmisano, Gaetano Palumbo, Salvatore Pennisi, 2012-12-06 CMOS Current Amplifiers presents design strategies for high performance current amplifiers based on CMOS technology After an introduction to various architectures of operational amplifiers the operating principles of the current amplifier are outlined This book provides the reader with simple and compact design equations for use in a pencil and paper design and the following simulation step Chapter 1 introduces the general aspects of current amplifiers After a preliminary classification of operational amplifiers ideal blocks and models are discussed for different architectures and a first high level comparison is made between traditional amplifiers and current amplifiers Analysis and examples of basic circuits as well as signal processing applications involving current amplifiers are also given Non idealities and second order effects causing limitations in performance are then discussed and evaluated Chapter 2 focuses on low drive current amplifiers Several design examples for current conveyors and class A current amplifiers are discussed in detail and design equations are presented for the main performance parameters which allows a good trade off between requirements High performance solutions for high bandwidth and low voltage capability are also considered and finally current comparators with progressively enhanced performance are reported and analyzed critically Chapter 3 deals with current amplifiers for off chip loads Several class AB current mode output stages are discussed and design strategies which improve performance are presented A detailed analysis of non ideal effect is carried out with particular emphasis on linearity Design examples are given and circuit arrangements for further developments are included CMOS Current Amplifiers serves as an excellent reference for researchers and professionals of analog IC design and may also be used as an advanced text on current amplifiers

Modeling, Analysis, and Applications in Metaheuristic

Computing: Advancements and Trends Yin, Peng-Yeng, 2012-03-31 This book is a collection of the latest developments models and applications within the transdisciplinary fields related to metaheuristic computing providing readers with insight into a wide range of topics such as genetic algorithms differential evolution and ant colony optimization Provided by publisher

Continuous-Time Delta-Sigma Modulators for High-Speed A/D Conversion James A. Cherry, W. Martin Snelgrove, 2006-04-18 Among analog to digital converters the delta sigma modulator has cornered the market on high to very high resolution converters at moderate speeds with typical applications such as digital audio and instrumentation Interest has recently increased in delta sigma circuits built with a continuous time loop filter rather than the more common switched capacitor approach Continuous time delta sigma modulators offer less noisy virtual ground nodes at the input inherent protection against signal aliasing and the potential to use a physical rather than an electrical integrator in the first stage for

novel applications like accelerometers and magnetic flux sensors More significantly they relax settling time restrictions so that modulator clock rates can be raised This opens the possibility of wideband 1 MHz or more converters possibly for use in radio applications at an intermediate frequency so that one or more stages of mixing might be done in the digital domain

Continuous Time Delta Sigma Modulators for High Speed A D Conversion Theory Practice and Fundamental Performance Limits covers all aspects of continuous time delta sigma modulator design with particular emphasis on design for high clock speeds The authors explain the ideal design of such modulators in terms of the well understood discrete time modulator design problem and provide design examples in Matlab They also cover commonly encountered non idealities in continuous time modulators and how they degrade performance plus a wealth of material on the main problems feedback path delays clock jitter and quantizer metastability in very high speed designs and how to avoid them They also give a concrete design procedure for a real high speed circuit which illustrates the tradeoffs in the selection of key parameters Detailed circuit diagrams simulation results and test results for an integrated continuous time 4 GHz band pass modulator for A D conversion of 1 GHz analog signals are also presented

Continuous Time Delta Sigma Modulators for High Speed A D Conversion Theory Practice and Fundamental Performance Limits concludes with some promising modulator architectures and a list of the challenges that remain in this exciting field

Distortion Analysis of Analog Integrated Circuits Piet Wambacq, Willy M.C. Sansen, 2013-04-17 The analysis and prediction of nonlinear behavior in electronic circuits has long been a topic of concern for analog circuit designers The recent explosion of interest in portable electronics such as cellular telephones cordless telephones and other applications has served to reinforce the importance of these issues The need now often arises to predict and optimize the distortion performance of diverse electronic circuit configurations operating in the gigahertz frequency range where nonlinear reactive effects often dominate However there have historically been few sources available from which design engineers could obtain information on analysis techniques suitable for tackling these important problems I am sure that the analog circuit design community will thus welcome this work by Dr Wambacq and Professor Sansen as a major contribution to the analog circuit design literature in the area of distortion analysis of electronic circuits I am personally looking forward to having a copy readily available for reference when designing integrated circuits for communication systems

Oversampling Delta-Sigma Data Converters James C. Candy, Gabor C. Temes, 1991-09-02 This now famous anthology brings together various aspects of oversampling methods and compares and evaluates design approaches It describes the theoretical analysis of converter performances the actual design of converters and their simulation circuit implementations and applications

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