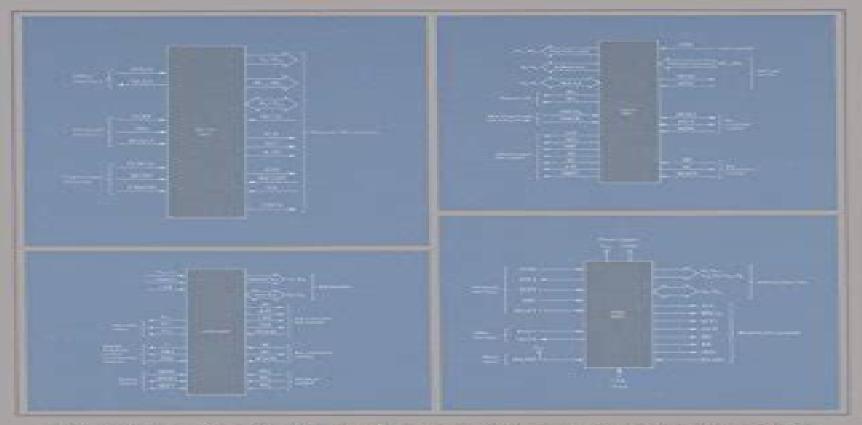
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16-BIT AND 32-BIT MICROPROCESSORS Architecture, Software, and Interfacing Techniques



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Software Defined Chips Shaojun Wei, Leibo Liu, Jianfeng Zhu, Chenchen Deng, 2022-10-20 This is the first book of a two volume book set which introduces software defined chips In this book it introduces the conceptual evolution of software defined chips from the development of integrated circuits and computing architectures Technical principles characteristics and key issues of software defined chips are systematically analyzed The hardware architecture design methods are described involving architecture design primitives hardware design spaces and agile design methods From the perspective of the compilation system the complete process from high level language to configuration contexts is introduced in detail This book is suitable for scientists and researchers in the areas of electrical and electronic engineering and computer science Postgraduate students practitioners and professionals in related areas are also potentially interested in the topic of this book

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