Logical Design of Switching Circuits

Lewin, Douglas

Switching Circuits Logical Design

H. C. Torng

Switching Circuits Logical Design:

Logical Design of Switching Circuits Douglas Lewin,1974 *Introduction to Switching Theory and Logical Design* Fredrick J. Hill,Gerald R. Peterson,1974 For upper level undergraduate courses **Switching Circuits; Theory and Logic Design** H. C. Torng,1972 *Digital Logic Design* Brian Holdsworth,Clive Woods,2002-11-01 New updated and expanded topics in the fourth edition include EBCDIC Grey code practical applications of flip flops linear and shaft encoders memory elements and FPGAs The section on fault finding has been expanded A new chapter is dedicated to the interface between digital components and analog voltages A highly accessible comprehensive and fully up to date digital systems text A well known and respected text now revamped for current courses Part of the Newnes suite of texts for HND 1st year modules

Switching Circuits and Logical Design Samuel Hawks Caldwell,1960 *Logic Design of Switching Circuits* Marcos Olandoski,2015-10-20 Preface The content of this book was developed in order to attend the needs of a Text Book for the course of Theory of Digital Electronics This course belongs to curriculum of Electrical Electronics Telecommunication Engineering and Computer Science Bachelor Degrees Also it belongs to curriculum of Electrical Electronics and Computer Sciences Technological Degrees The contents of the chapter s doesn t need any previous knowledge except mathematical and basic electricity of high school degree Because of the great amount of contents the book is divided in two parts Combinational Circuits Vol 1 and Sequential Circuits Vol 2 It is recommended 4 hours of theoretical classes per week for a semester of 17 week divided in two groups of two hours each or 2 hours per week for a year of 34 weeks This course doesn t require laboratory classes In general this course is given in the same semester of Digital Electronic Circuits that needs laboratory classes and that needs knowledge of Electronics Introduction course The first chapter of Vol 1 presents definitions and basic mathematical structures The following chapters Vol 1 present the methods and algorithms of Combinational Digital Circuits beginning with analysis and proceeding with synthesis Some additional chapters complete the contents with Combinational Circuits Hazards and Iterative Networks In the second part Vol 2 the book presents the knowledge of analysis and synthesis of Sequential Digital Electronics Circuits including Asynchronous and Synchronous machines Additional chapters complete the contents with types of commands and flip flops and various structures that are used in electronic digital projects The contents of this book are enough for the next course of Microprocessors A special attention is need to sections of solved and proposed problems in each chapter It s recommended three intermediate tests and the final exam for a one semester course and four intermediate tests and the final exam for a one year course **Introduction to switching theory and logical design** Fredrick J. Hill, **Logic Circuit Design** Shimon P. Vingron,2012-03-28 In three main divisions the book covers combinational circuits latches and asynchronous sequential circuits Combinational circuits have no memorising ability while sequential circuits have such an ability to various degrees Latches are the simplest sequential circuits ones with the shortest memory The presentation is decidedly non standard The design of combinational circuits is

discussed in an orthodox manner using normal forms and in an unorthodox manner using set theoretical evaluation formulas relying heavily on Karnaugh maps The latter approach allows for a new design technique called composition Latches are covered very extensively Their memory functions are expressed mathematically in a time independent manner allowing the use of normal non temporal Boolean logic in their calculation The theory of latches is then used as the basis for calculating asynchronous circuits Asynchronous circuits are specified in a tree representation each internal node of the tree representing an internal latch of the circuit the latches specified by the tree itself The tree specification allows solutions of formidable problems such as algorithmic state assignment finding equivalent states non recursively and verifying asynchronous circuits   **Library of Congress Subject Headings** Library of Congress,Library of Congress. Subject Cataloging Division,Library of Congress. Office for Subject Cataloging Policy,2013   **Library of Congress Subject Headings** Library of Congress. Cataloging Policy and Support Office,2007   *Introduction to Logic Design, Second Edition* Sajjan G. Shiva,1998-01-20 The second edition of this text provides an introduction to the analysis and design of digital circuits at a logic instead of electronics level It covers a range of topics from number system theory to asynchronous logic design A solution manual is available to instructors only Requests must be made on official school stationery   **Digital Circuits and Logic Designs** Mr. Rohit Manglik,2024-07-07 EduGorilla Publication is a trusted name in the education sector committed to empowering learners with high quality study materials and resources Specializing in competitive exams and academic support EduGorilla provides comprehensive and well structured content tailored to meet the needs of students across various streams and levels   *Library of Congress Subject Headings* Library of Congress. Subject Cataloging Division,1980   Logic Design Glen G. Jr. Langdon,2012-12-02 Logic Design A Review of Theory and Practice describes computer design focusing on the theoretical and practical relationships of sequential machines This book reviews the major technologies that make the computer particularly the switching circuit design involving vacuum tubes discrete transistors and integrated circuits The switching theory associated in the logic design of sequential machine models and synthesis techniques lead to understanding of constraints due to stray delays input change restrictions and memory element operation This text also describes the logic design processes including the use of flow charts design languages simulations and system timing Three aspects needed prior to the design phase that should be considered by the programmer are data flow the micro operations and their sequencing and the timing machine cycle or logic The significance between theoretical and mathematical models can then be determined through fault detection masking digital simulation and test generation This book can be beneficial for computer engineering instructors and advanced students in computer science   **NBS Special Publication** ,1965   **Encyclopedia of Computer Science and Technology** Jack Belzer,2020-02-03 This comprehensive reference work provides immediate fingertip access to state of the art technology in nearly 700 self contained articles written by over 900 international authorities Each article in the Encyclopedia features current developments and trends in

computers software vendors and applications extensive bibliographies of leading figures in the field such as Samuel Alexander John von Neumann and Norbert Wiener and in depth analysis of future directions   **Digital Logic Design** B. Holdsworth,2014-05-12 Digital Logic Design Second Edition provides a basic understanding of digital logic design with emphasis on the two alternative methods of design available to the digital engineer This book describes the digital design techniques which have become increasingly important Organized into 14 chapters this edition begins with an overview of the essential laws of Boolean algebra K map plotting techniques as well as the simplification of Boolean functions This text then presents the properties and develops the characteristic equations of a number of various types of flip flop Other chapters consider the design of synchronous and asynchronous counters using either discrete flip flops or shift registers This book discusses as well the design and implementation of event driven logic circuits using the NAND sequential equation The final chapter deals with simple coding techniques and the principles of error detection and correction This book is a valuable resource for undergraduate students digital engineers and scientists   **NASA Thesaurus** ,1998 Contains the authorized subject terms by which the documents in the NASA STI Database are indexed and retrieved   **NASA Thesaurus Alphabetical Update** ,1971   *NASA Thesaurus Alphabetical Update* United States. National Aeronautics and Space Administration. Scientific and Technical Information Division,1971

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