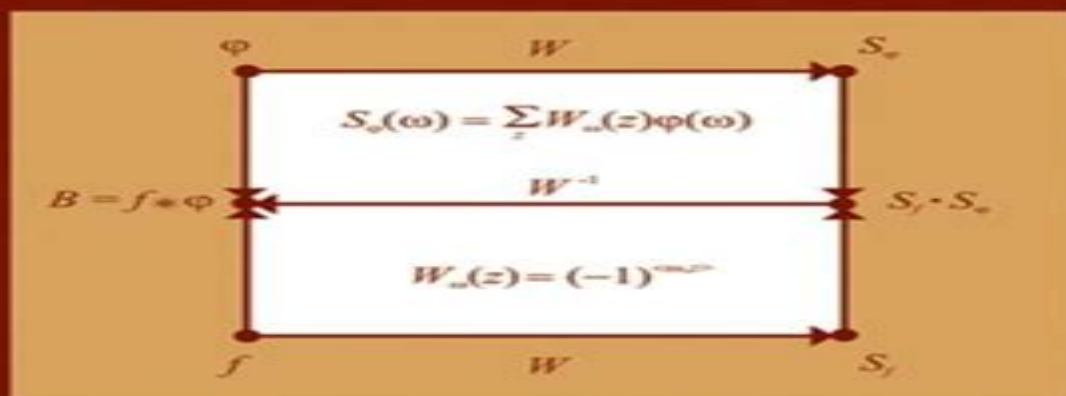


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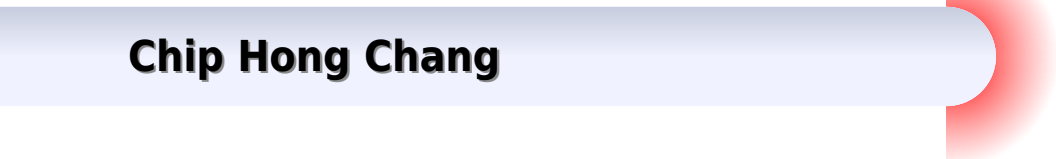
and Its Applications for the Design of Digital Devices



MARK G. KARPOVSKY
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Spectral Techniques In Digital Logic

Chip Hong Chang



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Spectral Techniques in Digital Logic Stanley Leonard Hurst, D. M. Miller, D. Michael Miller, Jon C. Muzio, 1985

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Decision Diagram Techniques for Micro- and Nanoelectronic Design Handbook Svetlana N. Yanushkevich, D. Michael Miller, Vlad P. Shmerko, Radomir S. Stankovic, 2018-10-03 Decision diagram DD techniques are very popular in the electronic design automation EDA of integrated circuits and for good reason They can accurately simulate logic design can show where to make reductions in complexity and can be easily modified to model different scenarios Presenting DD techniques from an applied perspective Decision Diagram Techniques for Micro and Nanoelectronic Design Handbook provides a comprehensive up to date collection of DD techniques Experts with more than forty years of combined experience in both industrial and academic settings demonstrate how to apply the techniques to full advantage with more than 400 examples and illustrations Beginning with the fundamental theory data structures and logic underlying DD techniques they explore a breadth of topics from arithmetic and word level representations to spectral techniques and event driven analysis The book also includes abundant references to more detailed information and additional applications Decision Diagram Techniques for Micro and Nanoelectronic Design Handbook collects the theory methods and practical knowledge necessary to design more advanced circuits and places it at your fingertips in a single concise reference Advanced Boolean Techniques Rolf Drechsler, Mathias Soeken, 2019-07-08 This book describes recent findings in the domain of Boolean logic and Boolean algebra covering application domains in circuit and system design but also basic research in mathematics and theoretical computer science Content includes invited chapters and a selection of the best papers presented at the 13th annual International Workshop on Boolean Problems Provides a single source reference to the state of the art research in the field of logic synthesis and Boolean techniques Includes a selection of the best papers presented at the 13th annual International Workshop on Boolean Problems Covers Boolean algebras Boolean logic Boolean modeling Combinatorial Search Boolean and bitwise arithmetic Software and tools for the solution of Boolean problems Applications of Boolean logic and algebras Applications to real world problems Boolean constraint solving and Extensions of Boolean logic **Modeling Digital Switching Circuits with Linear Algebra** Mitchell A. Thornton, 2022-05-31 Modeling Digital Switching Circuits with Linear Algebra describes an approach for modeling digital information and circuitry that is an alternative to Boolean algebra While the Boolean algebraic model has been wildly successful and is responsible for many advances in modern information technology the approach described in this book offers new insight and different ways of solving problems Modeling the bit as a vector instead of a scalar value in the set 0 1 allows digital circuits to be characterized with transfer functions in the form of a linear transformation matrix The use of transfer functions is ubiquitous in many areas of engineering and their rich background in linear systems theory and signal processing is easily applied to digital switching circuits with this model The

common tasks of circuit simulation and justification are specific examples of the application of the linear algebraic model and are described in detail. The advantages offered by the new model as compared to traditional methods are emphasized throughout the book. Furthermore, the new approach is easily generalized to other types of information processing circuits such as those based upon multiple valued or quantum logic, thus providing a unifying mathematical framework common to each of these areas. Modeling Digital Switching Circuits with Linear Algebra provides a blend of theoretical concepts and practical issues involved in implementing the method for circuit design tasks. Data structures are described and are shown to not require any more resources for representing the underlying matrices and vectors than those currently used in modern electronic design automation (EDA) tools based on the Boolean model. Algorithms are described that perform simulation, justification, and other common EDA tasks in an efficient manner that are competitive with conventional design tools. The linear algebraic model can be used to implement common EDA tasks directly upon a structural netlist, thus avoiding the intermediate step of transforming a circuit description into a representation of a set of switching functions as is commonly the case when conventional Boolean techniques are used. Implementation results are provided that empirically demonstrate the practicality of the linear algebraic model.

Binary Polynomial Transforms and Non-Linear Digital Filters

S. Agaian, Jaakko Astola, Karen Egiazarian, 1995-04-27. This work offers a unified presentation of the theory of binary polynomial transforms and details their numerous applications in nonlinear signal processing. The book also introduces the Rademacher logical functions, considers fast algorithms for computing Rademacher and polynomial logical functions, focuses attention on general auto and cross correlation functions, and more. The work is intended for applied mathematicians, electrical electronics, and other engineers, computer scientists, and upper level undergraduate and graduate students in these disciplines.

Testing and Diagnosis of VLSI and ULSI F. Lombardi, M.G. Sami, 2012-12-06. This volume contains a collection of papers presented at the NATO Advanced Study Institute on Testing and Diagnosis of VLSI and ULSI held at Villa Olmo, Como, Italy, June 22-July 3, 1987. High Density technologies such as Very Large Scale Integration (VLSI), Wafer Scale Integration (WSI), and the not so far promises of Ultra Large Scale Integration (ULSI) have exasperated the problems associated with the testing and diagnosis of these devices and systems. Traditional techniques are fast becoming obsolete due to unique requirements such as limited controllability and observability, increasing execution complexity for test vector generation, and high cost of fault simulation, to mention just a few. New approaches are imperative to achieve the highly sought goal of the three months turn around cycle time for a state of the art computer chip. The importance of testing and diagnostic processes is of primary importance if costs must be kept at acceptable levels. The objective of this NATO ASI was to present, analyze, and discuss the various facets of testing and diagnosis with respect to both theory and practice. The contents of this volume reflect the diversity of approaches currently available to reduce test and diagnosis time. These approaches are described in a concise yet clear way by renowned experts of the field. Their contributions are aimed at a wide readership; the uninitiated researcher will

find the tutorial chapters very rewarding The expert will be introduced to advanced techniques in a very comprehensive manner VLSI Custom Microelectronics Stanley L. Hurst,1998-11-05 Focuses on the design and production of integrated circuits specifically designed for a particular application from original equipment manufacturers The book outlines silicon and GaAs semiconductor fabrication techniques and circuit configurations compares custom design style discusses computer aided design tools and more Representations of Discrete Functions Tsutomu Sasao,Masahira Fujita,2012-12-06

Representations of Discrete Functions is an edited volume containing 13 chapter contributions from leading researchers with a focus on the latest research results The first three chapters are introductions and contain many illustrations to clarify concepts presented in the text It is recommended that these chapters are read first The book then deals with the following topics binary decision diagrams BDDs multi terminal binary decision diagrams MTBDDs edge valued binary decision diagrams EVBDDs functional decision diagrams FDDs Kronecker decision diagrams KDDs binary moment diagrams BMDs spectral transform decision diagrams STDDs ternary decision diagrams TDDs spectral transformation of logic functions other transformations of logic functions EXOR based two level expressions FPRM minimization with TDDs and MTBDDs complexity theories on FDDs multi level logic synthesis and complexity of three level logic networks Representations of Discrete Functions is designed for CAD researchers and engineers and will also be of interest to computer scientists who are interested in combinatorial problems Exercises prepared by the editors help make this book useful as a graduate level textbook

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