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The Sixth International Symposium on

High-Performance Computer Architecture

January 8-12, 2000 • Toulouse, France

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Babak Falsafi, Mario Lauria



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Proceedings, Sixth International Symposium on High-Performance Computer Architecture, HPCA-6 ,1999

Proceedings ,2000 *6th International Symposium on High-Performance Computer Architecture: Proceedings HPCA-6, January 8-12, 2000, Toulouse, France ,1999* **Languages and Compilers for Parallel Computing** Samuel P. Midkiff, Jose E. Moreira, Manish Gupta, Siddhartha Chatterjee, Jeanne Ferrante, Jan Prins, William Pugh, Chau-Wen Tseng, 2003-06-29 This

volume contains the papers presented at the 13th International Workshop on Languages and Compilers for Parallel Computing It also contains extended abstracts of submissions that were accepted as posters The workshop was held at the IBM T J Watson Research Center in Yorktown Heights New York As in previous years the workshop focused on issues in optimizing compilers languages and software environments for high performance computing This continues a trend in which languages compilers and software environments for high performance computing and not strictly parallel computing has been the organizing topic As in past years participants came from Asia North America and Europe This workshop reflected the work of many people In particular the members of the steering committee David Padua Alex Nicolau Utpal Banerjee and David Gelernter have been instrumental in maintaining the focus and quality of the workshop since it was first held in 1988 in Urbana Champaign The assistance of the other members of the program committee Larry Carter Sid Chatterjee Jeanne Ferrante Jan Prins Bill Pugh and Chau wen Tseng was crucial The infrastructure at the IBM T J Watson Research Center provided trouble free logistical support The IBM T J Watson Research Center also provided financial support by underwriting much of the expense of the workshop Appreciation must also be extended to Marc Snir and Pratap Pattnaik of the IBM T J Watson Research Center for their support *Network-Based Parallel Computing - Communication, Architecture, and Applications* Babak Falsafi, Mario Lauria, 2007-03-29

Proceedings of the ... Midwest Symposium on Circuits and Systems ,2003 **The Computer Engineering Handbook** Vojin G. Oklobdzija, 2001-12-26 There is arguably no field in greater need of a comprehensive handbook than computer engineering The unparalleled rate of technological advancement the explosion of computer applications and the now in progress migration to a wireless world have made it difficult for engineers to keep up with all the developments in specialties outside their own OpenMP Shared Memory Parallel Programming Michael J. Voss, 2007-03-05 The refereed proceedings of the International Workshop on OpenMP Applications and Tools WOMPAT 2003 held in Toronto Canada in June 2003 The 20 revised full papers presented were carefully reviewed and selected for inclusion in the book The papers are organized in sections on tools and tool technology OpenMP implementations OpenMP experience and OpenMP on clusters Digital Systems and Applications Vojin G.

Oklobdzija, 2017-12-19 New design architectures in computer systems have surpassed industry expectations Limits which were once thought of as fundamental have now been broken Digital Systems and Applications details these innovations in

systems design as well as cutting edge applications that are emerging to take advantage of the fields increasingly sophisticated capabilities This book features new chapters on parallelizing iterative heuristics stream and wireless processors and lightweight embedded systems This fundamental text Provides a clear focus on computer systems architecture and applications Takes a top level view of system organization before moving on to architectural and organizational concepts such as superscalar and vector processor VLIW architecture as well as new trends in multithreading and multiprocessing includes an entire section dedicated to embedded systems and their applications Discusses topics such as digital signal processing applications circuit implementation aspects parallel I O algorithms and operating systems Concludes with a look at new and future directions in computing Features articles that describe diverse aspects of computer usage and potentials for use Details implementation and performance enhancing techniques such as branch prediction register renaming and virtual memory Includes a section on new directions in computing and their penetration into many new fields and aspects of our daily lives *Books in Print Supplement* ,2002 *EURO-PAR* '... ,2001 *ISPASS* ,2001

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High-Performance Computing Jesus Labarta,Kazuki Joe,Toshinori Sato,2008-01-19 This book constitutes the refereed joint post conference proceedings of the 6th International Symposium on High Performance Computing ISHPC 2005 held in Japan in 2005 It also includes the refereed post proceedings of the First International Workshop on Advanced Low Power Systems 2006 ALPS2006 and some from the Workshop on Applications for PetaFLOPS Computing APC 2005 A total of 42 papers were carefully selected from 76 submissions covering a huge range of topics *HPCA* ,2001 Topics covered in this text include microarchitecture memory architectures multiprocessor systems code generation techniques energy and thermal management prediction techniques application specific designs performance modelling and analysis and latency tolerance techniques *High Performance Computing* Mateo Valero,Kazuki Joe,Masaru Kitsuregawa,Hidehiko Tanaka,2003-06-29 I wish to welcome all of you to the International Symposium on High Performance Computing 2000 ISHPC 2000 in the megalopolis of Tokyo After having two great successes with ISHPC 97 Fukuoka November 1997 and ISHPC 99 Kyoto May 1999 many people have requested that the symposium would be held in the capital of Japan and we have agreed I am very pleased to serve as Conference Chair at a time when high performance computing HPC has a significant influence on computer science and technology In particular HPC has had and will continue to have a significant impact on the advanced technologies of the IT revolution The many conferences and symposiums that are held on the subject around the world are an indication of the importance of this area and the interest of the research community One of the goals of this symposium is to provide a forum for the discussion of all aspects of HPC from system architecture to real applications in a more informal and personal fashion Today we are delighted to have this symposium which includes excellent invited talks tutorials and workshops as well as high quality technical papers *HPCA-9 2003* ,2003-01-01 The symposium brings together the research community from

industry and academia for discussions and exchange of ideas in almost all aspects of high performance computer system architecture The HPCA 9 being at the forefront of computer system design technology addressees all critical issues for the design of next generation computer systems whether it is a system targeted for a server in a computer room or a portable and low power digital processing device

**Proceedings of the 6th International Conference on High Performance
Compilation, Computing and Communications** ,2022 HPCA-12 2006 ,2006-01-01 **High Performance**

Computing Mateo Valero,Kazuki Joe,Masaru Kitsuregawa,Hidehiko Tanaka,2000-09-27 I wish to welcome all of you to the International Symposium on High Performance Computing 2000 ISHPC 2000 in the megalopolis of Tokyo After having two great successes with ISHPC 97 Fukuoka November 1997 and ISHPC 99 Kyoto May 1999 many people have requested that the symposium would be held in the capital of Japan and we have agreed I am very pleased to serve as Conference Chair at a time when high performance computing HPC has a significant influence on computer science and technology In particular HPC has had and will continue to have a significant impact on the advanced technologies of the IT revolution The many conferences and symposiums that are held on the subject around the world are an indication of the importance of this area and the interest of the research community One of the goals of this symposium is to provide a forum for the discussion of all aspects of HPC from system architecture to real applications in a more informal and personal fashion Today we are delighted to have this symposium which includes excellent invited talks tutorials and workshops as well as high quality technical papers

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