A Computational Logic Handbook

Robert S. Boyer

J Strother Moore

PERSPECTIVES IN COMPUTING
W. RHEINBOLDT, D. SIEWIOREK, EDITORS

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PROCEEDINGS OF THE 20TH CONFERENCE ON FORMAL METHODS IN COMPUTER-AIDED DESIGN – FMCAD 2020 Alexander Ivrii,Ofer Strichman,2020-09-11 Formal Methods in Computer Aided Design FMCAD is a conference series on the theory and applications of formal methods in hardware and system verification FMCAD provides a leading forum to researchers in academia and industry for presenting and discussing ground breaking methods technologies theoretical results and tools for reasoning formally about computing systems FMCAD covers formal aspects of computer aided system design including verification specification synthesis and testing   **A Computational Logic Handbook** Robert S. Boyer,J Strother Moore,2014-05-10 Perspectives in Computing A Computational Logic Handbook contains a precise description of the logic and a detailed reference guide to the associated mechanical theorem proving system including a primer for the logic as a functional programming language an introduction to proofs in the logic and a primer for the mechanical theorem The publication first offers information on a primer for the logic formalization within the logic and a precise description of the logic Discussions focus on induction and recursion quantification explicit value terms dealing with features and omissions elementary mathematical relationships Boolean operators and conventional data structures The text then takes a look at proving theorems in the logic mechanized proofs in the logic and an introduction to the system The text examines the processes involved in using the theorem prover four classes of rules generated from lemmas and aborting or interrupting commands Topics include executable counterparts toggle elimination of irrelevancy heuristic use of equalities representation of formulas type sets and the crucial check points in a proof attempt The publication is a vital reference for researchers interested in computational logic   Theorem Provers in Circuit Design Ramayya Kumar,Thomas Kropf,1995-03-06 This two volume set contains papers presented at the International Conference on Computational Engineering Science ICES 95 held in Mauna Lani Hawaii from 30 July to 3 August 1995 The contributions capture the state of the science in computational modeling and simulation in a variety of engineering disciplines civil mechanical aerospace materials and electronics engineering   **Intelligent Computer Mathematics** Fairouz Kamareddine,Claudio Sacerdoti Coen,2021-07-20 This book constitutes the refereed proceedings of the 14th International Conference on Intelligent Computer Mathematics CICM 2021 held in Timisoara Romania in July 2021 The 12 full papers 7 system descriptions 1 system entry and 3 abstracts of invited papers presented were carefully reviewed and selected from a total of 38 submissions The papers focus on advances in formalization automatic theorem proving and learning search and classification teaching and geometric reasoning and logic and systems among other topics The conference was held virtually due to the COVID 19 pandemic   **Theories of Programming** Cliff B. Jones,Jayadev Misra,2021-09-26 Sir Tony Hoare has had an enormous influence on computer science from the Quicksort algorithm to the science of software development concurrency and program verification His contributions have been widely recognised He was awarded the ACM s Turing Award in 1980 the Kyoto Prize from the Inamori Foundation

in 2000 and was knighted for services to education and computer science by Queen Elizabeth II of England in 2000 This book presents the essence of his various works the quest for effective abstractions both in his own words as well as chapters written by leading experts in the field including many of his research collaborators In addition this volume contains biographical material his Turing award lecture the transcript of an interview and some of his seminal papers Hoare s foundational paper An Axiomatic Basis for Computer Programming presented his approach commonly known as Hoare Logic for proving the correctness of programs by using logical assertions Hoare Logic and subsequent developments have formed the basis of a wide variety of software verification efforts Hoare was instrumental in proposing the Verified Software Initiative a cooperative international project directed at the scientific challenges of large scale software verification encompassing theories tools and experiments Tony Hoare s contributions to the theory and practice of concurrent software systems are equally impressive The process algebra called Communicating Sequential Processes CSP has been one of the fundamental paradigms both as a mathematical theory to reason about concurrent computation as well as the basis for the programming language occam CSP served as a framework for exploring several ideas in denotational semantics such as powerdomains as well as notions of abstraction and refinement It is the basis for a series of industrial strength tools which have been employed in a wide range of applications This book also presents Hoare s work in the last few decades These works include a rigorous approach to specifications in software engineering practice including procedural and data abstractions data refinement and a modular theory of designs More recently he has worked with collaborators to develop Unifying Theories of Programming UTP Their goal is to identify the common algebraic theories that lie at the core of sequential concurrent reactive and cyber physical computations *Computer Hardware Description Languages and their Applications* D. Borrione,R. Waxman,2014-06-28 The topic areas presented within this volume focus on design environments and the applications of hardware description and modelling including simulation verification by correctness proofs synthesis and test The strong relationship between the topics of CHDL 91 and the work around the use and re standardization of the VHDL language is also explored The quality of this proceedings and its significance to the academic and professional worlds is assured by the excellent technical programme here compiled **Computer Hardware Description Languages and their Applications** D. Agnew,L. Claesen,R. Camposano,2014-05-21 Hardware description languages HDLs have established themselves as one of the principal means of designing electronic systems The interest in and usage of HDLs continues to spread rapidly driven by the increasing complexity of systems the growth of HDL driven synthesis the research on formal design methods and many other related advances This research oriented publication aims to make a strong contribution to further developments in the field The following topics are explored in depth BDD based system design and analysis system level formal verification formal reasoning on hardware languages for protocol specification VHDL HDL based design methods high level synthesis and text graphical HDLs There are short papers covering advanced design capture and recent work in

high level synthesis and formal verification In addition several invited presentations on key issues discuss and summarize recent advances in real time system design automatic verification of sequential circuits and languages for protocol specification *Handbook of Automated Reasoning* Alan J.A. Robinson,Andrei Voronkov,2001-06-21 Handbook of Automated Reasoning Theorem Proving in Higher Order Logics Konrad Slind,Annette Bunker,Ganesh C. Gopalakrishnan,2004-09-01 This volume constitutes the proceedings of the 17th International Conference on Theorem Proving in Higher Order Logics TPHOLs 2004 held September 14 17 2004 in Park City Utah USA TPHOLs covers all aspects of theorem proving in higher order logics as well as related topics in theorem proving and veri cation There were 42 papers submitted to TPHOLs 2004 in the full research ca gory each of which was refereed by at least 3 reviewers selected by the program committee Of these submissions 21 were accepted for presentation at the c ference and publication in this volume In keeping with longstanding tradition TPHOLs 2004 also o ered a venue for the presentation of work in progress where researchers invited discussion by means of a brief introductory talk and then discussed their work at a poster session A supplementary proceedings c taining papers about in progress work was published as a 2004 technical report of the School of Computing at the University of Utah The organizers are grateful to Al Davis Thomas Hales and Ken McMillan for agreeing to give invited talks at TPHOLs 2004 The TPHOLs conference traditionally changes continents each year in order to maximize the chances that researchers from around the world can attend **Theorem Proving in Higher Order Logics** Richard J. Boulton,Paul B. Jackson,2003-06-30 This volume constitutes the proceedings of the 14th International Conference on Theorem Proving in Higher Order Logics TPHOLs 2001 held 3 6 September 2001 in Edinburgh Scotland TPHOLs covers all aspects of theorem proving in higher order logics as well as related topics in theorem proving and veri cation TPHOLs 2001 was collocated with the 11th Advanced Research Working Conference on Correct Hardware Design and Veri cation Methods CHARME 2001 This was held 4 7 September 2001 in nearby Livingston Scotland at the Institute for System Level Integration and a joint half day session of talks was arranged for the 5th September in Edinburgh An excursion to Traquair House and a banquet in the Playfair Library of Old College University of Edinburgh were also jointly organized The proceedings of CHARME 2001 have been p lished as volume 2144 of Springer Verlag s Lecture Notes in Computer Science series with Tiziana Margaria and Tom Melham as editors Each of the 47 papers submitted in the full research category was refereed by at least 3 reviewers who were selected by the Program Committee Of these submissions 23 were accepted for presentation at the conference and publication in this volume In keeping with tradition TPHOLs 2001 also o ered a venue for the presentation of work in progress where researchers invite discussion by means of a brief preliminary talk and then discuss their work at a poster session A supplementary proceedings containing associated papers for work in progress was published by the Division of Informatics at the University of Edinburgh **Automated Deduction - A Basis for Applications Volume I Foundations - Calculi and Methods Volume II Systems and Implementation Techniques Volume III Applications** Wolfgang Bibel,P.H.

Schmitt,2013-03-09 1 BASIC CONCEPTS OF INTERACTIVE THEOREM PROVING Interactive Theorem Proving ultimately aims at the construction of powerful reasoning tools that let us computer scientists prove things we cannot prove without the tools and the tools cannot prove without us Interaction typi cally is needed for example to direct and control the reasoning to speculate or generalize strategic lemmas and sometimes simply because the conjec ture to be proved does not hold In software verification for example correct versions of specifications and programs typically are obtained only after a number of failed proof attempts and subsequent error corrections Different interactive theorem provers may actually look quite different They may support different logics first or higher order logics of programs type theory etc may be generic or special purpose tools or may be tar geted to different applications Nevertheless they share common concepts and paradigms e g architectural design tactics tactical reasoning etc The aim of this chapter is to describe the common concepts design principles and basic requirements of interactive theorem provers and to explore the band width of variations Having a person in the loop strongly influences the design of the proof tool proofs must remain comprehensible proof rules must be high level and human oriented persistent proof presentation and visualization becomes very important *Intelligent Computer Mathematics* Christoph Benzmüller,Bruce Miller,2020-07-17 This book constitutes the refereed proceedings of the 13th International Conference on Intelligent Computer Mathematics CICM 2020 held in Bertinoro Italy in July 2020 The 15 full papers 1 invited paper and 2 abstracts of invited papers presented were carefully reviewed and selected from a total of 35 submissions The papers focus on advances in automated theorem provers and formalization computer algebra systems and their libraries and applications of machine learning among other topics The conference was held virtually due to the COVID 19 pandemic First International Workshop on Larch Ursula Martin,Jeannette M. Wing,2013-11-11 The papers in this volume were presented at the First International Workshop on Larch held at MIT Endicott House near Boston on 13 15 July 1992 Larch is a family of formal specification languages and tools and this workshop was a forum for those who have designed the Larch languages built tool support for them particularly the Larch Prover and used them to specify and reason about software and hardware systems The Larch Project started in 1980 led by John Guttag at MIT and James Horning then at Xerox Palo Alto Research Center and now at Digital Equipment Corporation Systems Research Center DEC SRC Major applications have included VLSI circuit synthesis medical device communications compiler development and concurrent systems based on Lamport s TLA as well as several applications to classical theorem proving and algebraic specification Larch supports a two tiered approach to specifying software and hardware modules One tier of a specification is wrillen in the Larch Shared Language LSL An LSL specification describes mathematical abstractions such as sets relations and algebras its semantics is defined in terms of first order theories The second tier is written in a Larch interface language one designed for a specific programming language An interface specification describes the effects of individual modules e g state changes resource allocation and exceptions its semantics is defined in terms of first order predicates over two states where

state is defined in terms of the programming language s notion of state Thus LSL is programming language independent a Larch interface language is programming language dependent *Artificial Intelligence Today* Michael J. Wooldridge,Manuela Veloso,2007-03-06 Artificial Intelligence is one of the most fascinating and unusual areas of academic study to have emerged this century For some AI is a true scientific discipline that has made important and fundamental contributions to the use of computation for our understanding of nature and phenomena of the human mind for others AI is the black art of computer science Artificial Intelligence Today provides a showcase for the field of AI as it stands today The editors invited contributions both from traditional subfields of AI such as theorem proving as well as from subfields that have emerged more recently such as agents AI and the Internet or synthetic actors The papers themselves are a mixture of more specialized research papers and authorative survey papers The secondary purpose of this book is to celebrate Springer Verlag s Lecture Notes in Artificial Intelligence series *Microprocessor Based Protection Systems* A.R. Churchley,1991-11-30 From a symposium or perhaps a series of symposia no information is provided 15 papers discuss the use of computers to control potentially hazardous industrial processes The sections cover guidelines standards and design reliability analysis software production and research and industrial applications Annotation copyrighted by Book News Inc Portland OR *Automated Mathematical Induction* Hantao Zhang,2012-12-06 It has been shown how the common structure that defines a family of proofs can be expressed as a proof plan 5 This common structure can be exploited in the search for particular proofs A proof plan has two complementary components a proof method and a proof tactic By prescribing the structure of a proof at the level of primitive inferences a tactic 11 provides the guarantee part of the proof In contrast a method provides a more declarative explanation of the proof by means of preconditions Each method has associated effects The execution of the effects simulates the application of the corresponding tactic Theorem proving in the proof planning framework is a two phase process 1 Tactic construction is by a process of method composition Given a goal an applicable method is selected The applicability of a method is determined by evaluating the method s preconditions The method effects are then used to calculate subgoals This process is applied recursively until no more subgoals remain Because of the one to one correspondence between methods and tactics the output from this process is a composite tactic tailored to the given goal 2 Tactic execution generates a proof in the object level logic Note that no search is involved in the execution of the tactic All the search is taken care of during the planning process The real benefits of having separate planning and execution phases become appar ent when a proof attempt fails *Handbook of Computability and Complexity in Analysis* Vasco Brattka,Peter Hertling,2021-06-04 Computable analysis is the modern theory of computability and complexity in analysis that arose out of Turing s seminal work in the 1930s This was motivated by questions such as which real numbers and real number functions are computable and which mathematical tasks in analysis can be solved by algorithmic means Nowadays this theory has many different facets that embrace topics from computability theory algorithmic randomness computational complexity

dynamical systems fractals and analog computers up to logic descriptive set theory constructivism and reverse mathematics In recent decades computable analysis has invaded many branches of analysis and researchers have studied computability and complexity questions arising from real and complex analysis functional analysis and the theory of differential equations up to geometric measure theory and topology This handbook represents the first coherent cross section through most active research topics on the more theoretical side of the field It contains 11 chapters grouped into parts on computability in analysis complexity dynamics and randomness and constructivity logic and descriptive complexity All chapters are written by leading experts working at the cutting edge of the respective topic Researchers and graduate students in the areas of theoretical computer science and mathematical logic will find systematic introductions into many branches of computable analysis and a wealth of information and references that will help them to navigate the modern research literature in this field **Theorem Proving in Higher Order Logics** Jim Grundy,Malcolm Newey,1998-09-09 This book constitutes the refereed proceedings of the 11th International Conference on Theorem Proving in Higher Order Logics TPHOLs 98 held in Canberra Australia in September October 1998 The 26 revised full papers presented were carefully reviewed and selected from a total of 52 submissions Also included are two invited papers The papers address all current aspects of theorem proving in higher order logics and formal verification and program analysis Besides the HOL system the theorem provers Coq Isabelle LAMBDA LEGO NuPrl and PVS are discussed *Higher Order Logic Theorem Proving and its Applications* L.J.M. Claesen,M.J.C. Gordon,2014-05-23 The HOL system is a higher order logic theorem proving system implemented at Edinburgh University Cambridge University and INRIA Its many applications from the verification of hardware designs at all levels to the verification of programs and communication protocols are considered in depth in this volume Other systems based on higher order logic namely Nuprl and LAMBDA are also discussed Features given particular consideration are novel developments in higher order logic and its implementations in HOL formal design and verification methodologies for hardware and software public domain availability of the HOL system Papers addressing these issues have been divided as follows Mathematical Logic Induction General Modelling and Proofs Formalizing and Modelling of Automata Program Verification Hardware Description Language Semantics Hardware Verification Methodologies Simulation in Higher Order Logic Extended Uses of Higher Order Logic Academic and industrial researchers involved in formal hardware and software design and verification methods should find the publication especially interesting and it is hoped it will also provide a useful reference tool for those working at software institutes and within the electronics industries **Computer Aided Verification** Gregor von Bochmann,David K. Probst,1993-03-30 This volume gives the proceedings of the Fourth Workshop on Computer Aided Verification CAV 92 held in Montreal June 29 July 1 1992 The objective of this series of workshops is to bring together researchers and practitioners interested in the development and use of methods tools and theories for the computer aided verification of concurrent systems The workshops provide an opportunity for comparing various verification

methods and practical tools that can be used to assist the applications designer Emphasis is placed on new research results and the application of existing results to real verification problems The volume contains 31 papers selected from 75 submissions These are organized into parts on reduction techniques proof checking symbolic verification timing verification partial order approaches case studies model and proof checking and other approaches The volume starts with an invited lecture by Leslie Lamport entitled Computer hindered verification humans can do it too

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