A Computational Logic Handbook

Robert S. Boyer J Strother Moore



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A Computational Logic Handbook Robert S. Boyer, J Strother Moore, 2014-05-10 Perspectives in Computing A Computational Logic Handbook contains a precise description of the logic and a detailed reference guide to the associated mechanical theorem proving system including a primer for the logic as a functional programming language an introduction to proofs in the logic and a primer for the mechanical theorem The publication first offers information on a primer for the logic formalization within the logic and a precise description of the logic Discussions focus on induction and recursion quantification explicit value terms dealing with features and omissions elementary mathematical relationships Boolean operators and conventional data structures The text then takes a look at proving theorems in the logic mechanized proofs in the logic and an introduction to the system The text examines the processes involved in using the theorem prover four classes of rules generated from lemmas and aborting or interrupting commands Topics include executable counterparts toggle elimination of irrelevancy heuristic use of equalities representation of formulas type sets and the crucial check points in a proof attempt The publication is a vital reference for researchers interested in computational logic **PROCEEDINGS** OF THE 20TH CONFERENCE ON FORMAL METHODS IN COMPUTER-AIDED DESIGN - FMCAD 2020 Alexander Ivrii.Ofer Strichman, 2020-09-11 Formal Methods in Computer Aided Design FMCAD is a conference series on the theory and applications of formal methods in hardware and system verification FMCAD provides a leading forum to researchers in academia and industry for presenting and discussing ground breaking methods technologies theoretical results and tools for reasoning formally about computing systems FMCAD covers formal aspects of computer aided system design including verification specification synthesis and testing **Theorem Provers in Circuit Design** Ramayya Kumar, Thomas Kropf, 1995-03-06 This two volume set contains papers presented at the International Conference on Computational Engineering Science ICES 95 held in Mauna Lani Hawaii from 30 July to 3 August 1995 The contributions capture the state of the science in computational modeling and simulation in a variety of engineering disciplines civil mechanical aerospace materials and electronics engineering **Computer Hardware Description Languages and their Applications** D. Borrione, R. Waxman, 2014-06-28 The topic areas presented within this volume focus on design environments and the applications of hardware description and modelling including simulation verification by correctness proofs synthesis and test The strong relationship between the topics of CHDL 91 and the work around the use and re standardization of the VHDL language is also explored The quality of this proceedings and its significance to the academic and professional worlds is Theories of Programming Cliff B. Jones, Jayadev assured by the excellent technical programme here compiled Misra, 2021-09-26 Sir Tony Hoare has had an enormous influence on computer science from the Quicksort algorithm to the science of software development concurrency and program verification His contributions have been widely recognised He was awarded the ACM s Turing Award in 1980 the Kyoto Prize from the Inamori Foundation in 2000 and was knighted for

services to education and computer science by Queen Elizabeth II of England in 2000 This book presents the essence of his various works the quest for effective abstractions both in his own words as well as chapters written by leading experts in the field including many of his research collaborators In addition this volume contains biographical material his Turing award lecture the transcript of an interview and some of his seminal papers Hoare's foundational paper An Axiomatic Basis for Computer Programming presented his approach commonly known as Hoare Logic for proving the correctness of programs by using logical assertions Hoare Logic and subsequent developments have formed the basis of a wide variety of software verification efforts Hoare was instrumental in proposing the Verified Software Initiative a cooperative international project directed at the scientific challenges of large scale software verification encompassing theories tools and experiments Tony Hoare's contributions to the theory and practice of concurrent software systems are equally impressive The process algebra called Communicating Sequential Processes CSP has been one of the fundamental paradigms both as a mathematical theory to reason about concurrent computation as well as the basis for the programming language occam CSP served as a framework for exploring several ideas in denotational semantics such as powerdomains as well as notions of abstraction and refinement It is the basis for a series of industrial strength tools which have been employed in a wide range of applications This book also presents Hoare's work in the last few decades These works include a rigorous approach to specifications in software engineering practice including procedural and data abstractions data refinement and a modular theory of designs More recently he has worked with collaborators to develop Unifying Theories of Programming UTP Their goal is to identify the common algebraic theories that lie at the core of sequential concurrent reactive and cyber physical computations Handbook of Automated Reasoning Alan J.A. Robinson, Andrei Voronkov, 2001-06-21 Handbook of Automated Reasoning

Computer Hardware Description Languages and their Applications D. Agnew,L. Claesen,R. Camposano,2014-05-21 Hardware description languages HDLs have established themselves as one of the principal means of designing electronic systems. The interest in and usage of HDLs continues to spread rapidly driven by the increasing complexity of systems the growth of HDL driven synthesis the research on formal design methods and many other related advances. This research oriented publication aims to make a strong contribution to further developments in the field. The following topics are explored in depth BDD based system design and analysis system level formal verification formal reasoning on hardware languages for protocol specification VHDL HDL based design methods high level synthesis and text graphical HDLs. There are short papers covering advanced design capture and recent work in high level synthesis and formal verification. In addition several invited presentations on key issues discuss and summarize recent advances in real time system design automatic verification of sequential circuits and languages for protocol specification. First International Workshop on Larch Ursula Martin, Jeannette M. Wing, 2013-11-11. The papers in this volume were presented at the First International Workshop on Larch held at MIT Endicott House near Boston on 13.15 July 1992 Larch is a family of formal specification languages and

tools and this workshop was a forum for those who have designed the Larch languages built tool support for them particularly the Larch Prover and used them to specify and reason about software and hardware systems The Larch Project started in 1980 led by John Guttag at MIT and James Horning then at Xerox Palo Alto Research Center and now at Digital Equipment Corporation Systems Research Center DEC SRC Major applications have included VLSI circuit synthesis medical device communications compiler development and concurrent systems based on Lamport s TLA as well as several applications to classical theorem proving and algebraic specification Larch supports a two tiered approach to specifying software and hardware modules One tier of a specification is wrillen in the Larch Shared Language LSL An LSL specification describes mathematical abstractions such as sets relations and algebras its semantics is defined in terms of first order theories The second tier is written in a Larch interface language one designed for a specific programming language An interface specification describes the effects of individual modules e g state changes resource allocation and exceptions its semantics is defined in terms of first order predicates over two states where state is defined in terms of the programming language s notion of state Thus LSL is programming language independent a Larch interface language is programming language dependent Theorem Proving in Higher Order Logics Konrad Slind, Annette Bunker, Ganesh C. Gopalakrishnan, 2004-12-07 This volume constitutes the proceedings of the 17th International Conference on Theorem Proving in Higher Order Logics TPHOLs 2004 held September 14 17 2004 in Park City Utah USA TPHOLs covers all aspects of theorem proving in higher order logics as well as related topics in theorem proving and veri cation There were 42 papers submitted to TPHOLs 2004 in the full research ca gory each of which was refereed by at least 3 reviewers selected by the program committee Of these submissions 21 were accepted for presentation at the c ference and publication in this volume In keeping with longstanding tradition TPHOLs 2004 also o ered a venue for the presentation of work in progress where researchers invited discussion by means of a brief introductory talk and then discussed their work at a poster session A supplementary proceedings c taining papers about in progress work was published as a 2004 technical report of the School of Computing at the University of Utah The organizers are grateful to Al Davis Thomas Hales and Ken McMillan for agreeing to give invited talks at TPHOLs 2004 The TPHOLs conference traditionally changes continents each year in order to maximize the chances that researchers from around the world can attend **Automated Deduction - A Basis for** Applications Volume I Foundations - Calculi and Methods Volume II Systems and Implementation Techniques Volume III Applications Wolfgang Bibel, P.H. Schmitt, 2013-03-09 1 BASIC CONCEPTS OF INTERACTIVE THEOREM PROVING Interactive Theorem Proving ultimately aims at the construction of powerful reasoning tools that let us computer scientists prove things we cannot prove without the tools and the tools cannot prove without us Interaction typically is needed for example to direct and control the reasoning to speculate or generalize strategic lemmas and sometimes simply because the conjec ture to be proved does not hold In software verification for example correct versions of specifications and programs typically are obtained only after a number of failed proof attempts and subsequent error corrections Different interactive theorem provers may actually look quite different They may support different logics first or higher order logics of programs type theory etc may be generic or special purpose tools or may be tar geted to different applications Nevertheless they share common concepts and paradigms e g architectural design tactics tactical reasoning etc The aim of this chapter is to describe the common concepts design principles and basic requirements of interactive theorem provers and to explore the band width of variations Having a person in the loop strongly influences the design of the proof tool proofs must remain comprehensible proof rules must be high level and human oriented persistent proof presentation and visualization becomes very important

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