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Supercompilers for Parallel and Vector Computers

Hans Zima with Barbara Chapman

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Supercompilers For Parallel And Vector Computers

David E. Hudak, Santosh G. Abraham



Supercompilers For Parallel And Vector Computers:

Supercompilers for Parallel and Vector Computers Hans Zima, 1990 **Languages and Compilers for Parallel Computing** Keshav Pingali, 1995-01-26 This volume presents revised versions of the 32 papers accepted for the Seventh Annual Workshop on Languages and Compilers for Parallel Computing held in Ithaca NY in August 1994 The 32 papers presented report on the leading research activities in languages and compilers for parallel computing and thus reflect the state of the art in the field The volume is organized in sections on fine grain parallelism alignment and distribution postlinear loop transformation parallel structures program analysis computer communication automatic parallelization languages for parallelism scheduling and program optimization and program evaluation *Languages and Compilers for Parallel Computing* Utpal Banerjee, 1994-01-28 This book contains papers selected for presentation at the Sixth Annual Workshop on Languages and Compilers for Parallel Computing The workshop was hosted by the Oregon Graduate Institute of Science and Technology All the major research efforts in parallel languages and compilers are represented in this workshop series The 36 papers in the volume are grouped under nine headings dynamic data structures parallel languages High Performance Fortran loop transformation logic and dataflow language implementations fine grain parallelism scalar analysis parallelizing compilers and analysis of parallel programs The book represents a valuable snapshot of the state of research in the field in 1993 *Compiling Parallel Loops for High Performance Computers* David E. Hudak, Santosh G. Abraham, 2012-12-06 4 2 Code Segments 96 4 3 Determining Communication Parameters 99 4 4 Multicast Communication Overhead 103 4 5 Partitioning 103 4 6 Experimental Results 117 4 7 Conclusion 121 5 COLLECTIVE PARTITIONING AND REMAPPING FOR MULTIPLE LOOP NESTS 125 5 1 Introduction 125 5 2 Program Enclosure Trees 128 5 3 The CPR Algorithm 132 5 4 Experimental Results 141 5 5 Conclusion 146 BIBLIOGRAPHY 149 INDEX 157 LIST OF FIGURES Figure 1 1 The Butterfly Architecture 5 1 2 Example of an iterative data parallel loop 7 1 3 Contiguous tiling and assignment of an iteration space 13 2 1 Communication along a line segment 24 2 2 Access pattern for the access offset 3 2 25 2 3 Decomposing an access vector along an orthogonal basis set of vectors 26 2 4 An analysis of communication patterns 29 2 5 Decomposing a vector along two separate basis sets of vectors 31 2 6 Cache lines aligning with borders 33 2 7 Cache lines not aligned with borders 34 2 8 nh is the difference of nd and nb 42 2 9 nh is the sum of nd and nb 42 2 10 The ADAPT system 44 2 11 Code segment used in experiments 46 2 12 Execution rates for various partitions 47 2 13 Execution time of partitions on Multimax 48 2 14 Performance increase as processing power increases 49 2 15 Percentage miss ratios for various aspect ratios and line sizes **Languages and Compilers for Parallel Computing** Chua-Huang Huang, 1996-01-24 This book presents the refereed proceedings of the Eighth Annual Workshop on Languages and Compilers for Parallel Computing held in Columbus Ohio in August 1995 The 38 full revised papers presented were carefully selected for inclusion in the proceedings and reflect the state of the art of research and advanced applications in parallel languages

restructuring compilers and runtime systems The papers are organized in sections on fine grain parallelism interprocedural analysis program analysis Fortran 90 and HPF loop parallelization for HPF compilers tools and libraries loop level optimization automatic data distribution compiler models irregular computation object oriented and functional parallelism

Shared Memory Parallel Programming with Open MP Barbara M. Chapman, 2005-01-25 This book contains the Proceedings of the 5th Workshop on OpenMP Applications and Tools WOMPAT2004 which took place at the University of Houston Houston Texas on May 17 and 18 2004 Previous workshops in this series took place in Toronto Canada Fairbanks Alaska Purdue Indiana and San Diego California The purpose of the workshop was to bring together users and developers of the OpenMP API for shared memory parallel programming to disseminate their ideas and experiences and discuss the latest developments in OpenMP and its application To support this aim the program comprised a mixture of invited talks from research and industry experience reports and submitted papers the last of which are presented in this volume A tutorial introduction to OpenMP was held at the same location on May 18 by Ruud van der Pas from Sun Microsystems Further a two day lab session called OMPlab was held immediately following the workshop and the tutorial on May 19 and 20 and was attended by both novice and advanced users Many of the hardware vendors and several researchers gave in depth tutorials on their software and made their systems available to both novice and advanced attendees during OMPlab Contributors to the WOMPAT 2004 OMPlab included IBM Intel Sun the University of Tennessee NASA the University of Greenwich Cornell University the University of Oregon and the University of Houston The OpenMP API is a widely accepted standard for high level shared memory parallel programming that was put forth by a consortium of vendors in 1997

Languages and Compilers for Parallel Computing Barbara Chapman, José Moreira, 2022-02-15 This book constitutes the thoroughly refereed post conference proceedings of the 33rd International Workshop on Languages and Compilers for Parallel Computing LCPC 2020 held in Stony Brook NY USA in October 2020 Due to COVID 19 pandemic the conference was held virtually The 15 revised full papers were carefully reviewed and selected from 19 submissions The contributions were organized in topical sections named as follows Code and Data Transformations OpenMP and Fortran Domain Specific Compilation Machine Language and Quantum Computing Performance Analysis Code Generation

Optimizing Supercompilers for Supercomputers Michael Joseph Wolfe, 1989 Effective use of a supercomputer requires users to have a good algorithm and to express this algorithm in an appropriate language and requires compilers to generate efficient code This book investigates several problems facing compiler design for supercomputers including building efficient and comprehensive data dependence graphs recurrence relations the management of compiler temporary variables and WHILE loops The book first proposes an efficient means of representing the flow of data in a program by labeling the arcs in a data dependence graph with direction vectors to show how the flow of data corresponds to the loop structure of the program These data dependence direction vectors are then used in several high level compiler loop optimizations loop vectorization loop concurrentization loop fusion and loop

interchanging The book shows how to perform these transformations and how to use them to optimize programs for a wide range of supercomputers The problems of recurrence relations studied include arithmetic recurrences with IF statements and recurrences involving both data and control dependence relations in a cycle The wavefront method of solving recurrences is also treated The book discusses ways to make the problem of managing temporary arrays more tractable It concludes by offering several methods for executing WHILE loops and describes a general structure of an optimizing compiler for supercomputers developed from the author s experience with a test bed compiler Michael Wolfe is Associate Professor in the Computer Science and Engineering Department at the Oregon Graduate Center Optimizing Supercompilers for Supercomputers is included in the series Research Monographs in Parallel Computing Copublished with Pitman Publishing *Languages and Compilers for Parallel Computing* Vikram Adve, María Jesús Garzarán, 2008-08-04 This book constitutes the thoroughly refereed post conference proceedings of the 20th International Workshop on Languages and Compilers for Parallel Computing LCPC 2007 held in Urbana IL USA in October 2007 The 23 revised full papers presented were carefully reviewed and selected from 49 submissions The papers are organized in topical sections on reliability languages parallel compiler technology libraries run time systems and performance analysis and general compiler techniques

Shared Memory Parallel Programming with Open MP ,2005-02-09 This book constitutes the thoroughly refereed postproceedings of the 5th International Workshop on Open MP Application and Tools WOMPAT 2004 held in Houston TX USA in May 2004 The 12 revised full papers presented were carefully selected during two rounds of reviewing and improvement The papers are devoted to using Open MP for large scale applications on several computing platforms consideration of Open MP parallelization strategies discussion and evaluation of several proposed language features and compiler and tools technology

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web centimeters the interior dimensions length of the volkswagen crafter cargo space is 2600mm l1 3625mm l2 4300mm l3 and 4700mm l4 the interior width of the crafter is 1780mm and the exterior width is 2426mm the interior height of the volkswagen crafter is 1650mm h1 1940mm h2 and 2140mm h3

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