

Solder Mechanics

A State of the Art Assessment



Santa Fe, New Mexico - 1990

D.R. Frear, W.B. Jones, K.R. Kinsman

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Handbook of Lead-Free Solder Technology for Microelectronic Assemblies Karl J. Puttlitz, Kathleen A. Stalter, 2004-02-27 This reference provides a complete discussion of the conversion from standard lead tin to lead free solder microelectronic assemblies for low end and high end applications Written by more than 45 world class researchers and practitioners the book discusses general reliability issues concerning microelectronic assemblies as well as factors specific to the tin rich replacement alloys commonly utilized in lead free solders It provides real world manufacturing accounts of the introduction of reduced lead and lead free technology and discusses the functionality and cost effectiveness of alternative solder alloys and non solder alternatives replacing lead tin solders in microelectronics

Proceedings of the XII Space Photovoltaic Research and Technology Conference (SPRAT XII), 1993

Modeling and Simulation for Microelectronic Packaging Assembly Shen Liu, Yong Liu, 2011-08-24 Although there is increasing need for modeling and simulation in the IC package design phase most assembly processes and various reliability tests are still based on the time consuming test and try out method to obtain the best solution Modeling and simulation can easily ensure virtual Design of Experiments DoE to achieve the optimal solution This has greatly reduced the cost and production time especially for new product development Using modeling and simulation will become increasingly necessary for future advances in 3D package development In this book Liu and Liu allow people in the area to learn the basic and advanced modeling and simulation skills to help solve problems they encounter Models and simulates numerous processes in manufacturing reliability and testing for the first time Provides the skills necessary for virtual prototyping and virtual reliability qualification and testing Demonstrates concurrent engineering and co design approaches for advanced engineering design of microelectronic products Covers packaging and assembly for typical ICs optoelectronics MEMS 2D 3D SiP and nano interconnects Appendix and color images available for download from the book s companion website Liu and Liu have optimized the book for practicing engineers researchers and post graduates in microelectronic packaging and interconnection design assembly manufacturing electronic reliability quality and semiconductor materials Product managers application engineers sales and marketing staff who need to explain to customers how the assembly manufacturing reliability and testing will impact their products will also find this book a critical resource Appendix and color version of selected figures can be found at www.wiley.com/go/liu_packaging

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Robust Design of Microelectronics Assemblies Against Mechanical Shock, Temperature and Moisture E-H Wong, Y.-W. Mai, 2015-05-23 Robust Design of Microelectronics Assemblies Against Mechanical Shock Temperature and Moisture discusses how the

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