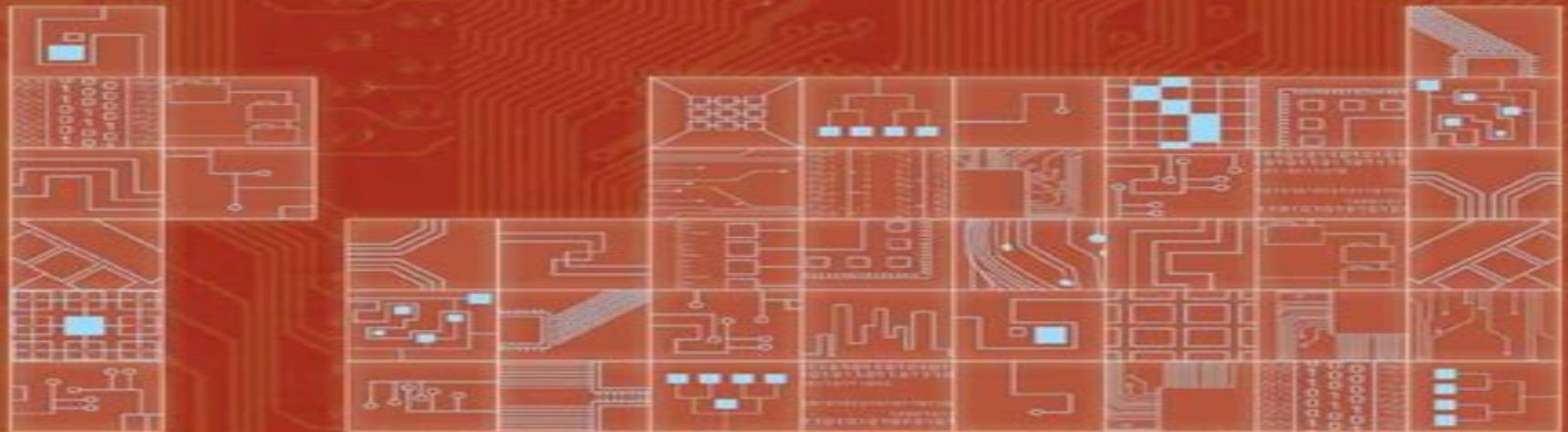


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# **Taxonomies** for the Development and Verification of **Digital Systems**



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# Taxonomies For The Development And Verification Of Digital Systems

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## **Taxonomies For The Development And Verification Of Digital Systems:**

**Taxonomies for the Development and Verification of Digital Systems** Brian Bailey, Grant Martin, Thomas Anderson, 2005-04-12 Thorough set of definitions for the terms and models used in the creation refinement and verification of complex systems from the conceptual level down to its implementation Considering both the hardware and software components of the system Also covers the emerging area of platform based design Provides both knowledge of models and terms and understanding of these models and how they are used

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**Intellectual Property for Electronic Systems** Brian Bailey, Kathy Werner, 2007 Featuring articles by top experts from such companies as Rambus IBM Hewlett Packard and FreeScale this collection addresses the issues that concern those in the ICT field looking to keep

systems safe and secure without sacrificing quality or ease of use This book cogently addresses verification standards handoff and legal issues to create a comprehensive look at one of the most important yet sometimes under appreciated topics in the industry

ESL Models and their Application Brian Bailey, Grant Martin, 2009-12-15 This book arises from experience the authors have gained from years of work as industry practitioners in the field of Electronic System Level design ESL At the heart of all things related to Electronic Design Automation EDA the core issue is one of models what are the models used for what should the models contain and how should they be written and distributed Issues such as interoperability and tool transportability become central factors that may decide which ones are successful and those that cannot get sufficient traction in the industry to survive Through a set of real examples taken from recent industry experience this book will distill the state of the art in terms of System Level Design models and provide practical guidance to readers that can be put into use This book is an invaluable tool that will aid readers in their own designs reduce risk in development projects expand the scope of design projects and improve developmental processes and project planning

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